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Real-Time Systems

Ming Fan

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Abstract

Since 2004, most of chip vendors have begun to shift their major focus from singlecore to multi-core architecture (W. Wolf. Signal Processing Magazine, IEEE, 26(6):50– 54, 2009). One major reason of this shift is that it reaches a physical limit by scaling transistor size and increasing the clock frequency to improve the computing performance on a single-core architecture (Agarwal et al. Proceedings of the 27th International Symposium on, pages 248–259, June 2000), that is, the overall chip cannot be reached within a single clock cycle. Multi-core architecture, however, brings innovative and promising opportunities to further improve the computing performance. By providing multiple processing cores on a single chip, multi-core systems can dramatically increase the computing performance and mitigate the power and thermal issues with the same performance achievement as single-core systems. As multi-core architecture has been more and more dominant in the industrial market, there is an urgent demand for effective and efficient techniques for the design of multi-core systems.

In this chapter, we first analyze the thermal behavior on multi-core real-time systems by taking the heat transfer among different cores into consideration. Then we analyze the energy consumption for a given speed scheduling on multi-core systems.

Keywords: Real-Time, Multi-Core, Thermal, Power, Energy, Periodic Scheduling

1. Introduction

Today, multi-core architecture has been widely supported by most of major chip vendors, including Intel, AMD, IBM, Nvidia, ARM, Sum microsystems, Qualcomm, Broadcom, and so on. Some of the chip manufacturers have already launched 16-core chips into the market, that is, AMD OpteronTM 6300 Series [1]. It is not surprising that in the coming future, hundreds or even thousands of cores will be integrated into a single chip [22]. When moving toward



© 2016 The Author(s). Licensee InTech. This chapter is distributed under the terms of the Creative Commons Attribution License (http://creativecommons.org/licenses/by/3.0), which permits unrestricted use, distribution, and reproduction in any medium, provided the original work is properly cited. multi-core architecture, it comes with new and critical challenges in design of multi-core systems, particularly multi-core real-time systems.

As architecture becomes more and more complicated, besides the timing constraint, many other design constraints are taken into consideration in real-time system design and development. Traditional approaches focus exclusively on timing constraints [3,4,6,7]. Today, many other design constraints (e.g. power/energy, thermal, and reliability) also need to be considered seriously in real-time system design [8,9,11,12,14].

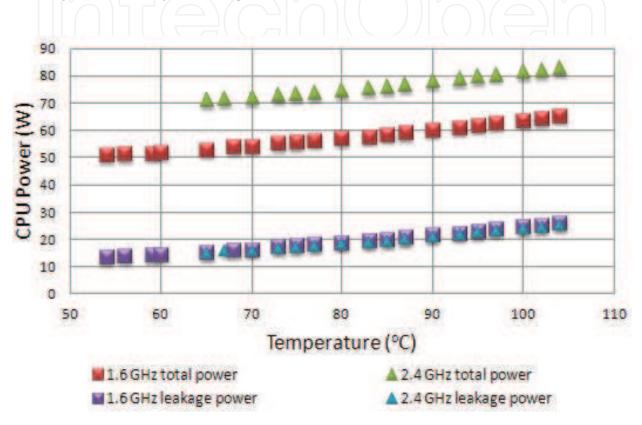


Figure 1. Power vs. Temperature [9]: Intel Core i5-2500K (32 nm Sandy Bridge), voltage 1.26 V, frequency at 1.6 GHz and 2.4 GHz, respectively.

1.1. Power/energy analysis in multi-core real-time systems

Catalyzed by continuous transistor scaling, hundreds of billions of transistors have been integrated on a single chip [13]. One of the immediate consequence caused by the tremendous increase of transistor density is the soaring power consumption [5], which further results in severe challenges in energy and temperature[11,17]. Today, power has become a critical and challenging design objective in front of system designers.

1.2. Thermal analysis in multi-core real-time systems

The continuously increased power consumption has resulted in a soaring chip temperature. Moreover, as design paradigm shifts to deep submicron domain, high chip temperature leads to a substantial increase in leakage power consumption [13], which in turn further deteriorates the power situation due to the interdependency between temperature and leakage power. For instance, with Intel core i5-2500K (32 nm Sandy Bridge), the leakage power roughly grows up to 2× from 55°C (13 W) to 105°C (26 W), see **Figure 1**. Furthermore, the soaring chip temperature adversely impacts the performance, reliability, and packaging/cooling costs [17]. As a result, power and thermal issues have become critical and significant for advanced multi-core system design. In next section, we introduce some necessary backgrounds of multi-core scheduling with power and thermal awareness, respectively.

The aggressive semiconductor technology scaling has pushed the chip power density doubled every two to three years [16,20], which immediately results in an exponential increasing in heat density. High temperature can degrade the performance of systems in various ways. Therefore, there is a great need of advanced techniques for thermal/temperature aware design of multi-core systems.

2. Preliminary

2.1. Multi-core platform and task model

The multi-core platform consists of *M* processing cores, $M \ge 2$, denoted as **P**, **P** = { $P_1, P_2, ..., P_M$ }. Each core P_i has N running modes, each of which is characterized by a 2-tuple set (v_k, f_k), where v_k represents the supply voltage and f_k represents the frequency under mode k, $1 \le k \le N$.

Let S represent a static and periodic speed schedule, which indicates how to vary the supply voltage and working frequency for each core at different time instants. A speed schedule S is constituted by several state intervals, which is described as below:

Definition 1 [25]: Given a speed schedule S for a multi-core system, an interval $[t_{q-1}, t_q]$ is called a state interval if each core runs only at one mode during that interval.

Recall that speed schedule S is a periodic schedule, let L denote the length of one scheduling period. According to Definition 1, a speed schedule S essentially consists of a number of non-overlapped state intervals. Let Q represent the number of non-overlapped state intervals within one scheduling period of S, then we have that [25]

1.
$$\cup_{q=1}^{Q} [t_{q-1}, t_q] = [0, L]$$

2.
$$[t_{a-1}, t_a] \cap [t_{n-1}, t_n] = \emptyset$$
, if $q \neq p$

For a single state interval $[t_{q-1}, t_q]$, let \mathbf{K}_q represent the interval mode, $\mathbf{K}_q = \{k_1, k_2, ..., k_M\}$, where ki denotes the running mode of core P_i in interval $[t_{q-1}, t_q]$.

2.2. Power model

The overall power consumption (in Watt) of each core is composed of two parts: dynamic power P_{dyn} and leakage power P_{leak} . We assume that: (1) P_{dyn} is varied with respect of supply voltage but independent of temperature, (2) P_{leak} is sensitive to both temperature and supply voltage. Specifically, for the dynamic power, we know that it is proportional to the square of

supply voltage and linearity of working frequency. We assume that the working frequency is linearly proportional to supply voltage, thus the power consumption of the i-th core (P_i) under running mode k_i can be formulated as below [18].

$$P_{dyn,i} = \gamma_{ki} * v_{ki}^3 \tag{1}$$

where γ_{ki} is a constant value determined by the platform and running mode, and v_{ki} is the supply voltage of core P_i determined by the running mode.

For leakage power, although there is a very complicated relationship between leakage power and temperature from circuit level perspective, Liu et al. [23] found that a linear approximation of the leakage temperature dependency is fairly accurate. Work [12] further formulated the leakage power of core P_i as below:

$$P_{leak,i} = (\alpha_{ki} + \beta_{ki} * T_i(t)) * v_{ki}$$
⁽²⁾

where α_{ki} and β_{ki} are constants depending on the core running mode, that is, mode *ki*.

Consequently, the total power consumption of core P_i at time t, denoted as $P_i(t)$, can be formulated as:

$$P_{i}(t) = (\alpha_{ki} + \beta_{ki} * T_{i}(t)) * v_{ki} + \gamma_{ki} * v_{ki}^{3}$$
(3)

For convenience in our presentation, we rewrite the above formula by separating the elements into temperature independent/dependent parts such that

$$P_{i}(t) = \psi_{i} + \phi_{i} * T_{i}(t)$$

$$(4)$$
where $\psi_{i} = \alpha_{ki} * v_{ki} + \gamma_{ki} * v_{ki}^{3}$ and $\phi_{i} = \beta_{ki} * v_{ki}$.
$$\begin{bmatrix} P_{1}(t) \\ \vdots \\ P_{M}(t) \end{bmatrix} = \begin{bmatrix} \psi_{1} \\ \vdots \\ \psi_{M} \end{bmatrix} + \begin{bmatrix} \phi_{1} & \cdots & 0 \\ \vdots & \ddots & \vdots \\ 0 & \cdots & \phi_{M} \end{bmatrix} \begin{bmatrix} T_{1}(t) \\ T_{M}(t) \end{bmatrix}$$

$$(5)$$

Or

$$\mathbf{P}(t) = \mathbf{\Psi} + \mathbf{\Phi} \mathbf{T}(t) \tag{6}$$

Note that we use the bold text for a vector/matrix and the unbolded text for a value, for example, **T** represents a temperature vector while *T* represents a temperature value.

2.3. Thermal model

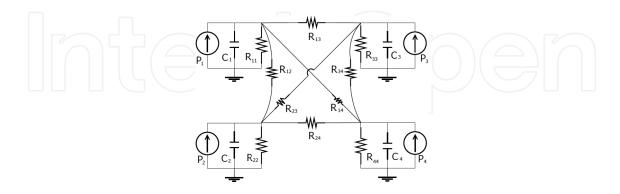


Figure 2. Illustration for thermal phenomena on multi-core system [26].

Figure 2 illustrates the thermal circuit model for a multi-core platform consisting of four processing cores. C_i represents the thermal capacitance (in Watt/°C) of core P_i , and R_{ij} represents the thermal resistance (in J/°C) between core P_i and P_j . Note that the thermal model adopted here is similar to the one used in related work [19,21]. Let T_{amb} represent the ambient temperature, then the thermal phenomena of core P_i in **Figure 2** can be formulated as

$$C_{i}\frac{dT_{i}(t)}{dt} + \frac{T_{i}(t) - T_{amb}}{R_{ii}} + \sum_{j \neq i} \frac{T_{i}(t) - T_{j}(t)}{R_{ij}} = P_{i}(t)$$
(7)

Let

$$\begin{cases} \delta i = \frac{T_{amb}}{R_{ii}} \\ g_{ij} = \begin{cases} \sum_{k=1}^{M} \frac{1}{R_{ik}}, \text{ if } j=i \\ \frac{-1}{R_{ij}}, \text{ otherwise} \end{cases} \end{cases}$$
(8)

Then the thermal model in equation (7) can be rewritten as

$$C_{i}\frac{dT_{i}(t)}{dt} + \sum_{j=1}^{M} g_{ij}T_{j}(t) = P_{i}(t) + \delta i$$
(9)

Accordingly, for the entire system, the thermal model can be represented as

$$\mathbf{C}\frac{d\mathbf{T}(t)}{dt} + \mathbf{g}\mathbf{T}(t) = \mathbf{P}(t) + \mathbf{\delta}$$
(10)

where C and g are MxM matrices

$$\mathbf{C} = \begin{bmatrix} C_1 & \cdots & 0 \\ \vdots & \ddots & \vdots \\ 0 & \cdots & C_M \end{bmatrix}, \mathbf{g} = \begin{bmatrix} g_{11} & \cdots & g_{1M} \\ \vdots & \ddots & \vdots \\ g_{M1} & \cdots & g_{MM} \end{bmatrix}$$
(11)

and δ is an *M*x1 vector

$$\boldsymbol{\delta} = \begin{bmatrix} \delta_1 \\ \vdots \\ \delta_M \end{bmatrix}$$
(12)

Note that **C**, **g**, and δ are all constants that are determined by the multi-core platform only. Moreover, **C** is the thermal capacitance matrix with none zero values only on the diagonal, and **g** is a thermal conductance matrix. The thermal model adopted here is a generic model which takes the heat transfer among different cores into consideration. Thus, it can be directly applied on thermal analysis for both temperature transient state and temperature stable state.

3. Temperature analysis in multi-core real-time systems

There is an interactive effect between power and temperature, that is, high power leads to high temperature, which in turn further aggravates the power consumption. In order to calculate the energy consumption accurately and efficiently, it is necessary to develop an efficient solution to calculate the temperature first.

In this section, we first present a temperature formulation within thermal transient state for a constant speed schedule interval. Then we present an analytical solution to calculate the temperature within thermal steady state for a periodic speed schedule.

3.1. Temperature analysis in system thermal transient state

In this subsection, we will formulate the temperature variation [26] within one state interval $[t_{q-1}, t_q]$ in the system thermal transient state.

First, by applying power model given by equation (6) into thermal model given by equation (10), we can derive that

$$\mathbf{C}\frac{d\mathbf{T}(t)}{dt} + \mathbf{g}\mathbf{T}(t) = \mathbf{\Psi} + \mathbf{\Phi}\mathbf{T}(t) + \mathbf{\delta}$$
(13)

Then we simplify the above equation by letting $G=g-\Phi$. Thus the above equation can be rewritten as

 $C\frac{dT(t)}{dt} + GT(t) = \Psi + \delta$ (14)

Since **C** represents the capacitance matrix, according to the circuit nature, we know that matrix **C** contains no zero values only on its diagonal. Thus, we can see matrix **C** is non-singular. Therefore, the inverse of **C**, that is, C^{-1} exists. Then we can further simplify equation (14) into below

$$\frac{d\mathbf{T}(t)}{dt} = \mathbf{AT}(t) + \mathbf{B}$$
(15)

where $\mathbf{A} = -\mathbf{C}^{-1}\mathbf{G}$ and $\mathbf{B} = \mathbf{C}^{-1}(\Psi + \delta)$. The system thermal model formulated by equation (15) has a form of first order Ordinary Differential Equations (ODE). Particularly, if all coefficients are constant, then there exists a solution which can be formulated as

$$\mathbf{T}(t) = e^{t\mathbf{A}}\mathbf{T}_0 + \mathbf{A}^{-1}(e^{t\mathbf{A}} - \mathbf{I})\mathbf{B}$$
(16)

For a state interval $[t_{q-1}, t_q]$, it is important to point out that all coefficients in the above are constant. Specifically, let K_q be the corresponding interval mode, and let $\mathbf{T}(t_{q-1})$ be the temperature at the starting point of that interval. Then according to equation (16), the ending temperature of that interval, that is, $\mathbf{T}(t_{q-1})$, can be directly formulated as



3.2. Temperature analysis in system thermal steady state

In this subsection, we will formulate the temperature variation [26] within one state interval $[t_{q-1}, t_q]$ in the system thermal steady state.

Consider a periodic speed schedule S, and let T(0) be the initial temperature at time instant 0. For an arbitrary state interval $[t_{q-1}, t_q]$ within speed schedule S, to obtain its steady-state temperature, one intuitive way is to trace the entire schedule S by consecutively calculating the temperature from the first scheduling period until system reaches its thermal steady state. Although this approach works from theoretical point of view, when considering the practical computational time cost (which would be extremely expensive), it turns out that this intuitive approach would be inefficient or even impractical. Thus, it would be desirable and useful to develop an efficient solution to rapidly calculate steady-state temperatures for a periodic speed schedule.

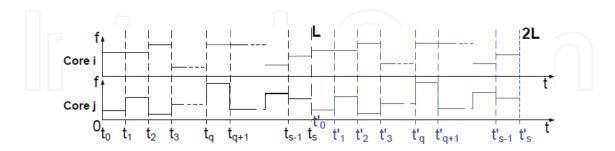


Figure 3. A speed schedule within two scheduling periods.

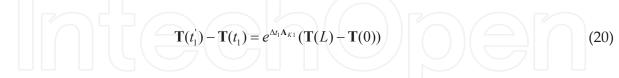
Let us first consider the temperature variation at the end of each scheduling period, that is, t = nL, where $n \ge 1$. Let the scheduling points of S(t) in the first period be $t_0, t_1, ..., t_{s'}$ respectively. After repeating S(t), let the corresponding points in the second scheduling period be $t_0', t_1', ..., t_{s'}$, respectively (see **Figure 3**). Note that

 $t_0 = 0$, $t_0' = t_s = L$, and $t_s' = 2L$. According to equation (17), at time t_1 and t_1' , we have

$$\mathbf{T}(t_1) = e^{\Delta t_1 \mathbf{A}_{K1}} \mathbf{T}(t_0) + \mathbf{A}_{K1}^{-1} (e^{\Delta t_1 \mathbf{A}_{K1}} - \mathbf{I}) \mathbf{B}_{K1}$$
(18)

$$\mathbf{T}(t_{1}^{'}) = e^{\Delta t_{1}^{'} \mathbf{A}_{K1}} \mathbf{T}(t_{0}^{'}) + \mathbf{A}_{K1}^{^{-1}} (e^{\Delta t_{1}^{'} \mathbf{A}_{K1}} - \mathbf{I}) \mathbf{B}_{K1}$$
(19)

Subtract equation (18) from (19) on both sides, and simplify the result by applying $\Delta t_1 = \Delta t_1$, $t_0 = 0$ and $t_0 = L$, we get



Follow the same trace of the above derivation, we have that

$$\mathbf{\Gamma}(t_{2}') - \mathbf{T}(t_{2}) = e^{\Delta t_{2} \mathbf{A}_{K_{2}}} e^{\Delta t_{1} \mathbf{A}_{K_{1}}} (\mathbf{T}(L) - \mathbf{T}(0))$$

$$\cdots$$

$$\mathbf{\Gamma}(t_{s}') - \mathbf{T}(t_{s}) = e^{\Delta t_{s} \mathbf{A}_{K_{s}}} \dots e^{\Delta t_{2} \mathbf{A}_{K_{2}}} e^{\Delta t_{1} \mathbf{A}_{K_{1}}} (\mathbf{T}(L) - \mathbf{T}(0))$$
(21)

Since $t_s = L$, $t_s = 2L$, and let $e^{\Delta t_s \mathbf{A}_{Ks}} \dots e^{\Delta t_2 \mathbf{A}_{K2}} e^{\Delta t_1 \mathbf{A}_{K1}} = \mathbf{K}$, the last equation in (21) can be rewritten as

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$$\mathbf{T}(2L) - \mathbf{T}(L) = \mathbf{K}(\mathbf{T}(L) - \mathbf{T}(0))$$
(22)

In the same way, we can construct that

$$\mathbf{T}(xL) - \mathbf{T}((x-1)L) = \mathbf{K}^{x-1}(\mathbf{T}(L) - \mathbf{T}(0))$$
(23)
where x = 1,2, ...,n. Sum up the above n equations, we get
$$\mathbf{T}(nL) = \mathbf{T}(0) + \sum_{x=1}^{n} \mathbf{K}^{x-1}(\mathbf{T}(L) - \mathbf{T}(0))$$
(24)

In the above, $\{\mathbf{K}^{x-1} | x = 1, 2, ..., n\}$ forms a matrix geometric sequence. If $(\mathbf{I}-\mathbf{K})$ is invertible, then we have

$$\mathbf{T}(nL) = \mathbf{T}(0) + (\mathbf{I} - \mathbf{K})^{-1}(\mathbf{I} - \mathbf{K}^{n})(\mathbf{T}(L) - \mathbf{T}(0))$$
(25)

Next, we consider the temperature variation for an arbitrary time instant when repeating a periodic speed schedule. Given a periodic speed schedule S(t), let t_q ($t_q \in [0,L]$) be an arbitrary time instant within schedule S(t). Moreover, let's repeat S(t) for n times, where $n \ge 1$. Let $T(nL + t_q)$ denote the temperature of $T(t_q)$ in the n-th scheduling period, by following the similar way of the above derivation, we can get that

$$\mathbf{T}(nL+t_q) = \mathbf{T}(t_q) + \mathbf{K}_q (\mathbf{I} - \mathbf{K})^{-1} (\mathbf{I} - \mathbf{K}^n) (\mathbf{T}(L) - \mathbf{T}(0))$$
(26)

where $\mathbf{K}_q = e^{\Delta t_q \mathbf{A}_{Kq}} \dots e^{\Delta t_2 \mathbf{A}_{K2}} e^{\Delta t_1 \mathbf{A}_{K1}}$.

Until now, we have been able to formulate the temperature variation of an arbitrary time instant in the n-th scheduling period. Next, we will further formulate the temperature variation of an arbitrary time instant in the system steady state. Consider an arbitrary time instant, that is, t_q , $0 \le t_q \le L$, within the first scheduling period. The brief idea of calculating the steady-state temperature corresponding to t_q is to let n go to infinity in equation (27). We formally describe our method in Theorem 1.

Theorem 1 [25]: Given a periodic speed schedule S(t), let T(L) and $T(t_q)$ be the temperatures at time instant *L* and t_q , $t_q \in [0,L]$, respectively. If for each eigenvalue λ_i of **K**, we have $|\lambda_i| < 1$, then the steady-state temperature corresponding to t_q can be formulated as

$$\mathbf{T}_{ss}(t_q) = \mathbf{T}(t_q) + \mathbf{K}_q (\mathbf{I} - \mathbf{K})^{-1} (\mathbf{T}(L) - \mathbf{T}(0))$$
(27)

Proof:

First, based on equation (26), by letting $n \rightarrow +\infty$, the steady-state temperature of the q-th scheduling point in S(*t*) can be represented as

$$\mathbf{T}_{ss}(t_q) = \mathbf{T}(t_q) + \mathbf{K}_q (\mathbf{I} - \mathbf{K})^{-1} (\mathbf{I} - \lim_{n \to \infty} \mathbf{K}^n) (\mathbf{T}(L) - \mathbf{T}(0))$$
(28)

When $n \rightarrow +\infty$, the matrix sequence \mathbf{K}^n converges if and only if $|\lambda_i| < 1$, for each eigenvalue λ_i of **K** [14]. Under this condition, we have $\lim_{n\to\infty} \mathbf{K}^n = 0$. Moreover, if $|\lambda_i| < 1$ holds, then (**I-K**) is invertible. Thus, the steady-state temperature of the q-th scheduling point in S(*t*) can be further formulated as

$$\mathbf{T}_{ss}(t_a) = \mathbf{T}(t_a) + \mathbf{K}_a (\mathbf{I} - \mathbf{K})^{-1} (\mathbf{T}(L) - \mathbf{T}(0))$$
(29)

Note that as $n \rightarrow +\infty$, unless the temperature runs away and causes the system to break down, we know that the system could eventually achieve its thermal steady state. That means for each eigenvalue λi of **K**, the condition of $|\lambda i| < 1$ should always hold. Therefore, it is reasonable and practical to make such assumption shown in Theorem 1.

4. Energy analysis in multi-core real-time systems

Besides temperature, energy consumption is another important and challenging issue in the design of multi-core real-time systems. We have now been able to formulate the temperature variation in a multi-core system in the previous section. In this section, we will discuss how to formulate the energy consumption on multi-core systems with consideration of the interdependence between leakage power and temperature.

In the rest of this section, we first present an analytical solution to calculate energy consumption of an arbitrary state interval. Then we present a solution to calculate the total energy consumption of the entire speed schedule.

4.1. Energy analysis for one scheduling state interval

Consider a state interval, that is, $[t_{q-1}, t_q]$ with initial temperature of $T(t_{q-1})$. The energy consumption of all cores within that interval can be simply formulated as

$$\mathbf{E}(t_{q-1}, t_q) = \int_{t_{q-1}}^{t_q} \mathbf{P}(t) dt$$
(30)

Based on our system power model, given by equation (6), we have

$$\mathbf{E}(t_{q-1}, t_q) = \Delta t_q \mathbf{\Psi} + \mathbf{\Phi} \int_{t_{q-1}}^{t_q} \mathbf{T}(t) dt$$
(31)

Theorem 2 given below is targeted to solve the above energy calculation problem.

Theorem 2 [25]: Given a state interval $[t_{q-1}, t_q]$, let \mathbf{T}_{q-1} be the temperature at time t_{q-1} . Then the overall system energy consumption within interval $[t_{q-1}, t_q]$ can be formulated as

$$\mathbf{E}(t_{q-1}, t_q) = \Delta t_q \mathbf{\Psi} + \mathbf{\Phi} \mathbf{G}^{-1} \mathbf{H}$$
(32)
where $\Delta t_q = t_q - t_{q-1}$, and $\mathbf{H} = \Delta t_q (\mathbf{\Psi} + \mathbf{\delta}) - \mathbf{C}(\mathbf{T}(t_q) - \mathbf{T}(t_{q-1}))$.

Proof:

In equation (31), let $\mathbf{X} = \int_{t_{q-1}}^{t_q} \mathbf{T}(t) dt$, then the energy formula can be rewritten as

$$\mathbf{E}(t_{q-1}, t_q) = \Delta t_q \mathbf{\Psi} + \mathbf{\Phi} \mathbf{X}$$
(33)

On the other hand, according to the system thermal model given by equation (10), we have

$$\mathbf{C}\frac{d\mathbf{T}(t)}{dt} + \mathbf{G}\mathbf{T}(t) = \mathbf{\Psi} + \mathbf{\delta}$$
(34)

where $G = g - \Phi$. By integrating on both sides of the above equation with respect to time *t*, where $t \in [t_{q-1}, t_q]$, we can get

$$\mathbf{C}(\mathbf{T}(t_q) - \mathbf{T}(t_{q-1})) + \mathbf{G} \int_{t_{q-1}}^{t_q} \mathbf{T}(t) = \Delta t_q (\mathbf{\Psi} + \mathbf{\delta})$$
(35)

Note that $\mathbf{X} = \int_{t_{q-1}}^{t_q} \mathbf{T}(t) dt$; thus from the above, we can derive that $\mathbf{X} = \mathbf{G}^{-1} \mathbf{H}$ (36)

where $\mathbf{H} = \Delta t_q (\Psi + \delta) - \mathbf{C}(\mathbf{T}(t_q) - \mathbf{T}(t_{q-1}))$

Applying equation (36) into (33), we can see that

$$\mathbf{E}(t_{q-1'}, t_q) = \Delta t_q \Psi + \mathbf{\Phi} \mathbf{G}^{-1} \mathbf{H}$$
(37)

From Theorem 2, we can see that for an arbitrary state interval $[t_{q-1}, t_q]$, once the beginning temperature $\mathbf{T}(t_{q-1})$ is known, the total energy consumption within $[t_{q-1}, t_q]$ can be easily and directly calculated.

Subsequently, given a periodic speed schedule S and an initial temperature T_{0} , we are able to calculate the energy consumption within an arbitrary state interval in any scheduling period.

Corollary 1 [25]: Given a periodic speed schedule S(t) consisting of Q state intervals, let \mathbf{T}_0 be the initial temperature. Then the energy consumption within the q-th state interval in the n-th scheduling period, denoted as $\mathbf{E}(t_{q-1} + nL, t_q + nL)$, can be calculated as

$$\mathbf{E}(t_{q-1}+nL, t_q+nL) = \Delta t_q \Psi_{Kq} + \Phi_{Kq} \mathbf{G}_{Kq}^{-1} \mathbf{H}_{Kq}$$
(38)
where $\Delta t_q = t_q - t_{q-1}$, and $\mathbf{H}_{Kq} = \Delta t_q (\Psi_{Kq} + \delta) - \mathbf{C}(\mathbf{T}(t_q+nL) - \mathbf{T}(t_{q-1}+nL)).$

Corollary 1 can be easily derived from Theorem 2. With the help of Corollary 1, given any periodic speed schedule on a multi-core platform, when repeating that schedule, we can quickly calculate the energy consumption within any state interval.

Accordingly, given a periodic speed schedule, when analyzing the system thermal steady state, we can directly calculate the energy consumption of any state interval within the system steady state.

Corollary 2 [25]: Given a periodic speed schedule S(t) consisting of Q state intervals, let \mathbf{T}_0 be the initial temperature. Then the energy consumption within the q-th state interval in the system steady state, denoted as $\mathbf{E}_{ss}(t_{q-1}, t_q)$, can be calculated as

$$\mathbf{E}_{ss}(t_{q-1}, t_q) = \Delta t_q \mathbf{\Psi}_{Kq} + \mathbf{\Phi}_{Kq} \mathbf{G}_{Kq}^{-1} \mathbf{H}_{ss Kq}$$
(39)

where $\Delta t_q = t_q - t_{q-1}$, and $\mathbf{H}_{ssKq} = \Delta t_q (\Psi_{Kq} + \delta) - \mathbf{C}(\mathbf{T}_{ss}(t_q) - \mathbf{T}_{ss}(t_{q-1}))$.

Corollary 2 is directly derived from Corollary 1 by replacing the transient temperatures with steady-state temperatures.

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