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## Metal Induced Crystallization

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### 1. Introduction

Polycrystalline silicon thin films have attracted the attention of semiconductor industries in the past few decades due to their wide applications in thin film transistors, solar cells, display units and sensors (Schropp & Zeman, 1998; Choi et al., 2005; Mahan et al., 2008). Polycrystalline Si thin films are generally fabricated by crystallizing amorphous Si (a-Si) thin films, because these can render larger grains compared to the conventional poly Si film deposition. As a consequence, a variety of methods for lowering the crystallization temperature of a-Si have been developed. Excimer laser annealing is one of the promising ways to achieve large grain size poly Si films at lower substrate temperatures. Its high costs and nonuniform grain size, however, are significant obstacles that prevent its wide use (Parr et al., 2002). The other promising technique is the solid phase crystallization method. But this technique is essentially a high-temperature process and many substrates, including most forms of glass, cannot withstand the thermal processing. In order to achieve lower costs and have a wider range of application, inexpensive materials such as glass and special polymers have to substitute quartz or Pyrex<sup>TM</sup> substrates. In the case of glass substrates, all of the processing steps need to be limited to temperatures below 550 °C. The other known technique is rapid thermal annealing (RTA). In RTA infrared radiation is used as a heating source, and has the advantage of the high heating speed (up to 60 °C/s) that reduces the crystallization time. In RTA radiation is applied in pulses to heat the sample without heating the glass substrate (which is transparent to the infrared radiation). However, the grain size obtained in the crystallization of a-Si is also in the range of a few micrometers.

In an effort to reduce the crystallization temperature and crystallization time, and to increase the grain size, metal-induced crystallization (MIC) has been investigated as an alternative crystallization process for thin-film device fabrication. The MIC process involves the deposition of a-Si films on top of which a layer of suitable metal is deposited. This bilayer of metal and Si is then annealed in a furnace at temperatures ranging from 150 to 700 °C for durations between one minute to several hours leading to crystallization of the a-Si. As a result the Si nanocrystals grow in the metal/metal silicide/metal oxide matrix and their growth rate depends on the annealing conditions of the bilayer. Thus, to grow the Si nanocrystals, both thermodynamics and kinetics of the process have to be understood in detail. In the MIC process, metals like aluminum, nickel, gold, silver etc are used to decrease the crystallization temperature below 700 °C. Among the metals employed for the study of the MIC (Jin et al., 1999; Kumar et al., 2007; Kumar et al., 2008; Mohiddon & Krishna, 2011), the preferred metal up to date has been nickel (Ni) due to its low residual metal

contamination in the poly-Si region (Wang & Wong, 2001). The mobility and the transfer characteristics of the poly Si TFT are improved by reducing the leakage current caused by the metal agents. To achieve this, metal-induced lateral crystallization (MILC) has been introduced. Ni induced MILC TFTs' are now widely used in large display applications. Large number of metals have been investigated to crystallize the a-Si at possible low temperature. These metals are classified in to two groups basing on the mechanism they follow in crystallizing the a-Si matrix. One group of metals forms eutectic with a-Si (follows Layer exchange mechanism) and the other reacts with a-Si and forms silicide. The eutectic phase forming metals can produce extended continuous poly-Si films with large grain size of 10  $\mu\text{m}$  (Oliver & Hartmann, 2000). However, these metals contaminate the so produced c-Si and degrade its properties. Thus, the search for metals that would lead to crystallization of a-Si thin film at low temperatures and also reduce the contamination is a subject of recent research.

The aim of this review is to present an overview of the current state of understanding of the mechanisms involved in MIC and to discuss some of the recent work of the current authors in this area. The motivation is to crystallize a-Si films using metals that have not been investigated in detail so far, in addition to Ni which is a well studied metal used in the MIC process. The two other metals that were selected for this study are Tin (Sn) and Chromium (Cr). The former is known to form a eutectic alloy with the Si whereas the latter is a silicide/oxide forming metal, similar to Nickel. Furthermore, results on two different types of geometries are presented; the first involves the deposition of metal on top of the a-Si film, while in the second geometry the a-Si film is deposited on top of the metal film. This provides very interesting insights in to the mechanisms that cause crystallization of a-Si.

## 2. Mechanisms of metal induced crystallization

The main mechanisms of metal induced crystallization are discussed in this section.

### 2.1 Layer exchange mechanism

The proposed formation of poly-Si films on foreign substrates by eutectic forming metals relies on the overall layer exchange of adjacent Si and metal films during the transformation of amorphous to polycrystalline Si. The phenomenon is named as "layer exchange mechanism". The general driving force behind metal-induced crystallization is the reduction of the free energy of the silicon material during the transformation of the amorphous to the crystalline phase. When a-Si is in contact with certain metals, electronic screening of the covalent bonding in the Si material occurs, which weakens the Si bonds, and therefore, facilitates the inter diffusion of the metal and silicon atoms (Hiraki, 1980). The representatives of the eutectic forming metals are Au, Ag and Al. Aluminum-induced crystallization [AIC] is extensively studied metal to form extended continuous poly-Si films of large-grained material on glass, which is desirable for thin film solar cells.

The AIC comprises the diffusion of Si atoms into the Al layer, which occurs due to the fact that adjacent Al and Si layers are not in thermal equilibrium at elevated temperatures. According to the Al/Si phase diagram, up to 1.5 at.% of Si can be dissolved in Al at temperatures below the eutectic temperature of 577 °C (Murray & McAlister, 1984). The initial interaction of the Al and a-Si layers involves the partial dissolution of the Al or Si

oxide interface layer, depending on the layer sequence, to allow the inter diffusion of the materials. The Si oxide layer is transformed by the Al into a mixture of Al oxide and an  $\text{Al}_x\text{Si}$  phase. This newly formed  $\text{Al}_x\text{Si}$  phase provides a diffusion channel for the Si and Al atoms (Nast & Wenham, 2000). The evaporated Al films are of polycrystalline nature, there are four different diffusion paths available for the diffusion of the Si atoms. 1) Diffusion inside the Al grains, 2) Diffusion along Al grain boundaries, 3) Diffusion along the Al/*a*-Si interfaces, and 4) Diffusion along the glass/Al interface.

The diffused Si atoms start crystallizing near the Al/*a*-Si interface. The Al grain boundaries are preferential nucleation sites for the dissolved Si atoms, since they are the sites of low critical free energy for nucleus formation. According to the random successive nucleation model, at early stage of crystallization, Si nuclei are formed at preferred sites located at the Al/Si interface. The released heat (crystallization energy and strain energy) leads to rise the local temperature in ambient regions. The temperature field propagates fast, since the thermal diffusion is faster than the atomic diffusion, so that the heat flow can stimulate new nuclei appearing randomly in nearby region. These nuclei of the next generation also produce a local temperature rise. This process repeats many time during annealing, resulting in a fractal formation.

Silicon atoms, dissolved in the Al layer, have a high mobility and, hence, diffuse quickly within the film and/or along the interfaces towards the growing Si grains (McCaldin & Sankur, 1971). It is this fast growth within the Al layer compared to any other growth normal to the layer structure that leads to the formation of a continuous poly-Si film. At an early stage of the crystallization process the newly formed Si grains within the Al matrix are far apart and do not influence each other. The nucleation rate strongly depends on the defects and grain boundaries in the Al layer as well as the temperature and corresponding silicon concentration. Si solute depletion occurs up to the effective diffusion distance in the vicinity of the growing grain (Zener, 1949). In this region the possibility of new nucleation decreases with increasing depletion towards the advancing Al/Si grain interface. When the effective diffusion distances of adjacent grains begin to overlap, competition for the available Si atoms dissolved in the Al layer occurs. At this point, the possibility of new nuclei formation decreases. This type of crystallization pattern, where isolated grains start to interfere at an early stage of the process, is different to solid-phase crystallization of amorphous silicon (Spinella et al., 1998).

The grain-size distribution is dependent on the ratio of the grain growth to the nucleation rate. This ratio increases with decreasing annealing temperature (Nast & Wenham, 2000). The decrease in grain growth due to a lower annealing temperature is less than the decrease in nucleation rate. The effective diffusion distance is longer at lower annealing temperatures leading to larger depletion areas around the growing grains, which prevents further nucleation. Therefore, the grains grow to a larger size before impingement occurs. The nucleations as well as the diffusion-controlled growth are thermally activated processes. Once the size of the Si grains is equivalent to the thickness of the Al layer, they solely advance laterally since they are constrained by the substrate and the *a*-Si/Al interface. Throughout the poly-Si growth process the Al layer is gradually displaced. The continuous supply of Si atoms from *a*-Si layer and gradual displacement of Al atoms leads to the growth of poly Si film with the large grain size. As an alternative member of this group of metals, we have investigated the tin induced *a*-Si crystallization.

## 2.2 Mechanism of diffusion assisted crystallization

Two main mechanisms of diffusion assisted crystallization are proposed in literature. One is based on the diffusion and movement of Ni in the *a*-Si matrix causing crystallization of Si around dispersed nanostructured NiSi<sub>2</sub> seeds (Ni diffusion assisted MIC); the other is based on the formation of the silicides at the interface between Ni and Si layers, which then moves into the *a*-Si matrix, leading to crystallization of Si (MILC)

According to the first mechanism, the metal (Ni) at first diffuses through *a*-Si matrix and then forms NiSi<sub>2</sub> at a sufficient high temperature. This silicide will then act as seed for the crystallization of *a*-Si. The concept of metal diffusion is proposed on the basis of the experimental observations reported in the literature. Park et al. [Park et al., 2001] reported secondary ion mass spectroscopy (SIMS) and transmission electron microscope studies on excimer laser annealed Ni induced crystallization of poly Si. They measured the melting temperatures as a function of Ni content and reported that *a*-Si, just below the Ni layer, melted at lower temperature and the melting point increases with movement away from the Ni rich layer. The change in melting point was attributed to change in Ni concentration. Their explanation for the observed behaviour was that, the Ni diffuses through *a*-Si matrix only when a sufficiently high temperature is reached. Ferri et al. [Ferri et al., 2001] studied diluted metal contaminated *a*-Si system by detailed Raman spectroscopy measurement and came to similar conclusions.

The knowledge of formation, structure and electrical behaviour of a metal-semiconductor interfaces plays an important role in semiconductor technology. The nickel monosilicide (NiSi) is a low resistance silicide in Ni-Si binary system, and is a key material to reduce the contact resistance of gate and source-drain regions. I. H. Hong et al. (Hong et al., 2006) carried out scanning photoelectron spectromicroscopy on the *a*-Si/Ni films, which were deposited by chemical vapor deposition at ultra high vacuum pressure. They state that when the Si/Ni interface is heated, Ni<sub>2</sub>Si, which is an unstable phase with high resistivity, is first formed at 200-300 °C. At around 300 °C or above, Ni<sub>2</sub>Si starts transformation into a low resistivity phase NiSi. The Ni<sub>2</sub>Si phase disappears at above 400°C. Another high resistivity phase, NiSi<sub>2</sub>, nucleates at above 750 °C. Another method of enhancing metal diffusion is electric field assisted MIC (FAMIC). In FAMIC, the crystallization of *a*-Si is enhanced by applying an electric field to the metal/Si binary system.

The two major driving forces for the MIC process are (1) the free-energy gradient between silicon and metal silicides, (2) the concentration gradient caused by atomic diffusion in metal/silicon stacked films. In addition to these driving forces, the applied electrical field introduces an additional driving force to lower the activation energy for crystallization. Lee (Lee et al., 2000) suggested in the study of a Cu/Si FAMIC system that the electrons can charge up at the *a*-Si surface of the Cu silicide/*a*-Si interface under an applied voltage. The Cu ions with positive charges could then migrate at a faster speed, resulting in a directional and rapid lateral movement of Cu silicide/*a*-Si interface and so the crystallized poly-Si can be formed at the enhanced speed. The similar enhanced growth rate is reported in Ni/Si system. It has been postulated that the enhanced growth rate is a result of field-enhanced diffusion of nickel atoms (Park et al., 1999; Yoon et al., 2001). Having an effective charge of -0.3299e in the Si lattice, Ni atoms move toward the positive electrode under an electric field



(Yoon et al., 2001). The other mechanism of FAMIC considers the bombardment of Ni atoms by electrons traveling in an *a*-Si layer. Nickel atoms are pushed toward the positive electrode under a large number of low-momentum collisions (electron wind). Also, energetic electrons are capable of breaking bonds by exciting the bonding electrons through the collision. In fact both field-enhanced diffusion under electric force and electron bombardment effects are responsible for the enhanced crystallization in FAMIC. In both mechanisms, the diffusion of metal is a common argument. Grisenti et al. (Grisenti et al., 2008) reported the Extended x-ray absorption fine structure (EXAFS) analysis on Ni induced *a*-Si produced by co-sputtering. They came to conclusion that, Ni segregates as NiSi<sub>2</sub> even at temperatures as low as 200 °C and this NiSi<sub>2</sub> enhances the MIC of *a*-Si. The transformation from one kind of silicide to other is well explained by Ni diffusion through Ni/Si interface and this mechanism is widely accepted in the semiconductor technology.

### 2.3 Metal Induced Lateral Crystallization (MILC)

In case of MILC first metal silicides are formed at the interface between metal and Si layers at the lower annealing temperatures. On further annealing, nucleation of *c*-Si on metal silicide precipitate starts, which is then followed by migration of the metal silicide precipitates throughout the *a*-Si thin film, resulted in crystallization of the entire *a*-Si film. This mechanism is supported by the following experimental observations. Jin et al. (Jin et al., 1998) studied the XPS depth profile of Ni (10 nm) covered *a*-Si films (70 nm) which was annealed at 500 °C for 1 hr. Ar ions with energy of 4 keV were used for sputtering during depth profiling. They reported the concentration variation of selected elements (Ni, O and Si) across the 100 nm stack. From their results it is found that the Ni concentration first decreased with Ar sputtering then stabilized to about 4% in the bulk of the film and finally increased to about 10% near the interface of the film. No Si was detected on the surface of the stack before the Ar sputtering, indicating the diffusion of Si in Ni and NiO<sub>x</sub> was slow at the heat treatment temperature of 500 °C. The presence of excess metallic Ni at the bottom of the stack was attributed to the diffusion of the NiSi<sub>2</sub> nodules. Hayzelden et al. (Hayzelden & Batstone, 1993) reported an in-situ transmission electron microscopy study on Ni induced *a*-Si crystallization and suggested a similar mechanism for the diffusion of NiSi<sub>2</sub> in *a*-Si matrix. According to the mechanism proposed by these authors, Si first crystallizes on one of the eight faces of NiSi<sub>2</sub> crystalline seeds and then it dissociates as Ni and Si atoms at the *c*-Si/NiSi<sub>2</sub> interface. The Nickel atoms then diffuse and react again with *a*-Si to form NiSi<sub>2</sub> at *a*-Si/NiSi<sub>2</sub> interface. The cause of such dissociation, diffusion and reformation of NiSi<sub>2</sub> was explained by chemical potential free energy of the reactions at different interfaces. The chemical potential of Ni is lower at NiSi<sub>2</sub>/*a*-Si interface, while the chemical potential of Si is lower at the NiSi<sub>2</sub>/*c*-Si interface. Thus Ni moves to the NiSi<sub>2</sub>/*a*-Si interface and Si atoms are forced to diffuse to the NiSi<sub>2</sub>/*c*-Si interface. The consumption of *a*-Si at the NiSi<sub>2</sub>/*a*-Si interface and diffusion of the Ni atoms result in the growth of needle like Si crystallites. The diffusion of Ni in crystalline Si is much faster than that in *a*-Si. This supports the fast diffusion of Ni from the *c*-Si interface to the *a*-Si interface through a NiSi<sub>2</sub> crystallite. Hwang et al. (Hwang & Li, 2005) studied the Auger depth profile on Ni induced MIC and came to a similar conclusion. Our recent work on the transmission electron microscopy study of Ni induced MIC of *a*-Si films also supports the NiSi<sub>2</sub> diffusion mechanism. The details of the experiment and result are presented in the section 4.2 of the chapter.

### 3. Tin induced a-Si crystallization

Tin is a representative of the eutectic forming group along with Al and Au. The Sn–Si alloy has a relatively low eutectic temperature of 232 °C (Jeon et al., 2010) compared other members of the group. Our recent work on Tin induced a-Si revealed that it follows the eutectic forming metal mechanism well below the Al–Si system (Mohiddon & Krishna, 2011). Jeon et al. (Jeon et al., 2010) studied the Sn–Si system for growing Si nanowires and quoted that Sn is a favorable catalyst for low temperature synthesis of Si nanowires.

The experimental details are as follows. Sn films of 500 nm thickness were deposited onto Borosilicate Glass (BSG) substrates by resistive thermal evaporation. Si films of 500 nm thickness were deposited by electron beam evaporation over the Sn layers. Fig. 1 shows the block diagram of experimental deposition setup of electron beam evaporator and block diagram of film annealing setup. The starting materials were granular pure silicon powder (99.999%) and Sn (99.99% pure). A pressure  $5 \times 10^{-6}$  Torr was maintained throughout the depositions. The depositions were carried out at ambient temperature and in all cases the substrate to source distance was kept constant at 10 cm. The thickness of the films was measured after deposition using a surface profilometer (model XP-1 of Ambios Technology, USA). The films were annealed in a furnace atmosphere at different temperatures for 1 hr. X-ray diffraction patterns were recorded on a powder x-ray diffractometer (CPS120 of Inel, France) machine equipped with a Co K $\alpha$  x-ray source (wavelength = 0.178896 nm) and gas phase position sensitive detector.

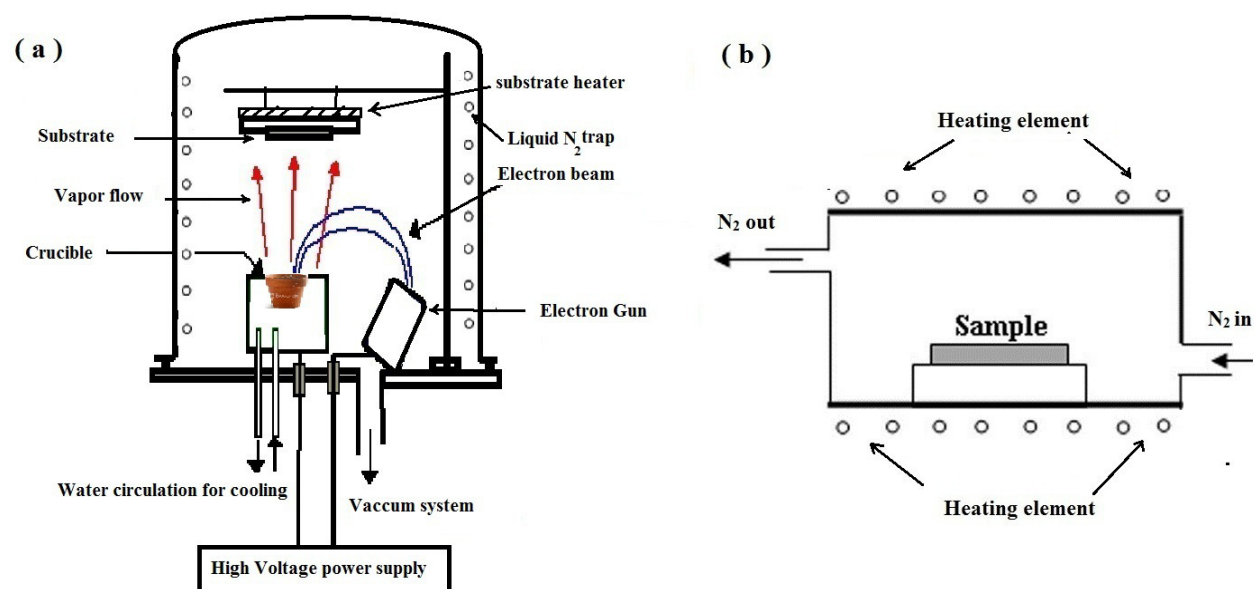


Fig. 1. Schematic diagram of (a) film deposition by Electron beam evaporator (b) annealing of the film in muffle furnace

X-ray diffraction patterns of as deposited BSG/Sn/a-Si stacks and stacks post deposition annealed at different temperatures from 300 °C to 500 °C each for 1 hour are presented in Fig. 2. XRD pattern of Sn film deposited on BSG and annealed at 500 °C for 15 hrs is also presented in the same figure. It is evident from the XRD pattern of the annealed Sn film that, it is well crystallized and the pattern can be identified as belonging to the orthorhombic phase of SnO<sub>2</sub> (the peaks were indexed according to the PCPDF file no-781063). The as

deposited BSG/Sn/*a*-Si stack, shows sharp diffraction peaks along with a broad diffuse hump around  $2\theta = 27^\circ$ , due to the amorphous substrate and *a*-Si film. The peaks can be assigned to the (200), (101), (220), (211), (112) and (312) planes of the tetragonal phase of metallic Sn (the peaks are indexed according to the PCPDF file no- 23456). There is no evidence for the presence of crystalline Si.

On annealing this stack at 300 °C for 1 hr, there is a very interesting transformation in the crystalline behavior of the films. Significantly, all the peaks due to metallic Sn are completely suppressed and the x-ray diffraction pattern resembles that of an amorphous film except for a very diffuse peak at  $2\theta = 33.58^\circ$  assigned to the (111) plane of the diamond cubic form of Si. The intensity of this peak increases with increasing annealing temperature up to 500 °C as shown in Fig. 2 indicating that the extent of crystallization of Si is improved by increasing the annealing temperature. However, there is no evidence for crystalline Sn in the films. The reason for this is, most probably, partial oxidation of Sn. The peak at  $2\theta = 33.58^\circ$  assigned to the (111) plane of diamond cubic silicon (PCPDF file no- 23345) is shifted by  $0.45^\circ$  which is attributable to strain in the film. Evidently, the onset of crystallization of *a*-Si occurs at 300 °C by contacting with the Sn metal. M Joen et al. have also reported Sn induced crystallization of *a*-Si at 300 °C (Jeon et al., 2010). Significantly, there is no evidence for the formation of silicides in the Sn-Si system even after annealing at 500 °C. Deconvolution of the peak at  $2\theta = 33.58^\circ$  provides more insight into the mechanism of crystallization of *a*-Si. The peak was deconvoluted by smoothening and best fitting with a Gaussian function. The data is shown in the inset of Fig.2 for the samples annealed at 300, 400 and 500 °C. Closer observation of the peak corresponding to the sample annealed at 500 °C shows that the peak can be resolved into two peaks centered at  $33.7^\circ$  and  $34^\circ$ . The deconvoluted peaks fitted using Gaussian function is shown in the inset of the Fig 2. The peak centered at  $2\theta=34^\circ$  is assigned to the (113) plane of orthorhombic SnO<sub>2</sub> phase (PCPDF file no- 781063). By eliminating the effect of SnO<sub>2</sub> phase in 500 °C annealed samples, the crystallite size is estimated as 18 nm. The average nanocrystal size calculated by Scherrer's equation (Mohiddon & Yadav, 2008) is found to increase from 5 nm to 14 nm with increasing the annealing temperature from 300 to 400 °C. The solid solubility of Sn in Si is known to be nil, *i.e.* they are immiscible in the solid state (Leonard & Koch, 1992). Hence it is not expected to form a silicide. X-ray diffraction data presented earlier indicates that this is the case even in the present samples. The crystallographic evolution of the c-Si phase in the current case is traced by studying the dependence of the crystallite size of Sn and Si on the annealing temperature. This is plotted in Fig. 3, from which it is evident that the crystallite size of Si is zero in the as-deposited film while that of the Sn phase is high. Interestingly, there is a cross-over region below 300 °C when the crystallite sizes of both phases are low. This indicates that at this temperature, the film is essentially amorphous. On increasing the annealing temperature to 400 and 500 °C, there is further increase in the crystallite size of Si while crystallite size of the Sn phase is invariant at zero. This behavior is very similar to that observed in the case of Al induced crystallization (AIC) of *a*-Si (McCaldin & Sanku, 1971; Zener, 1949; Spinella et al., 1998) with one very crucial difference. No peaks corresponding to crystalline Sn are observed on annealing the films unlike the AIC case. This is evidently due to oxidation of Sn during the annealing process as revealed by the deconvolution of the (111) peak of Si in the inset of Fig.2. Thus we conclude from this work that *a*-Si crystallizes when it is brought in contact with Sn metal layer without involving the formation of a silicide, exactly as followed by eutectic forming metals at 300 °C.



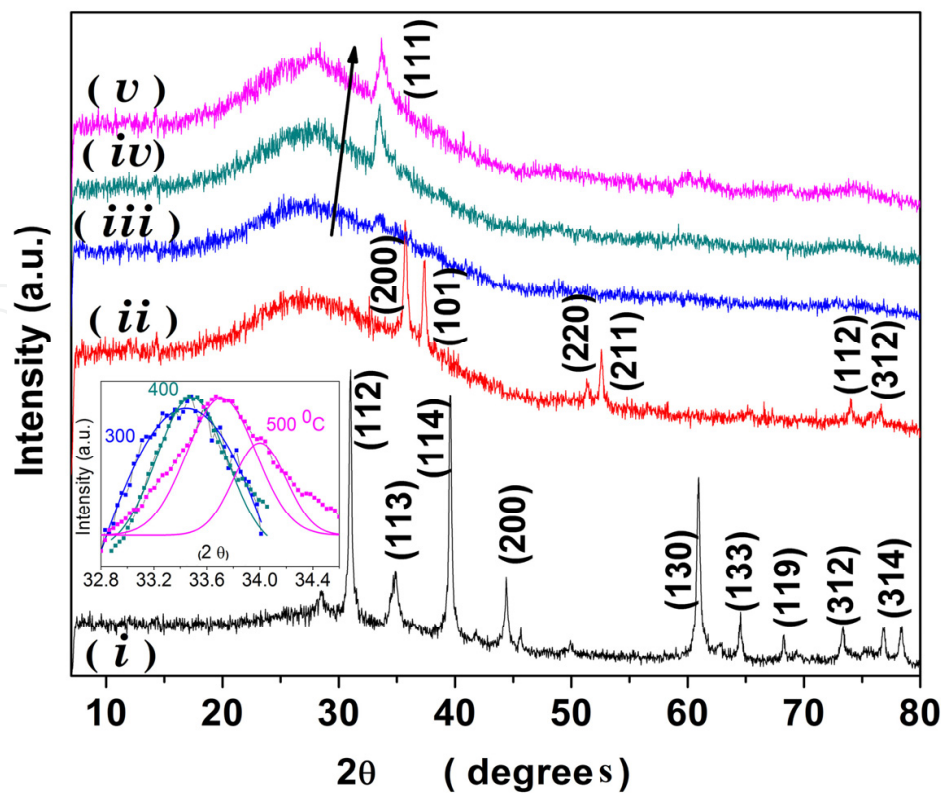


Fig. 2. X-ray diffraction pattern of (i) BSG/Sn film annealed at 500 °C for 15 hours. (ii) as-deposited BSG/Sn/a-Si stack and BSG/Sn/a-Si stack annealed at (iii) 300, (iv) 400 and (v) 500 °C along with the expanded and smoothened (111) peak of  $c$ -Si for BSG/Sn/a-Si stack, deconvoluted to show the presence of  $\text{SnO}_2$  (Mohiddon et al., 2012).

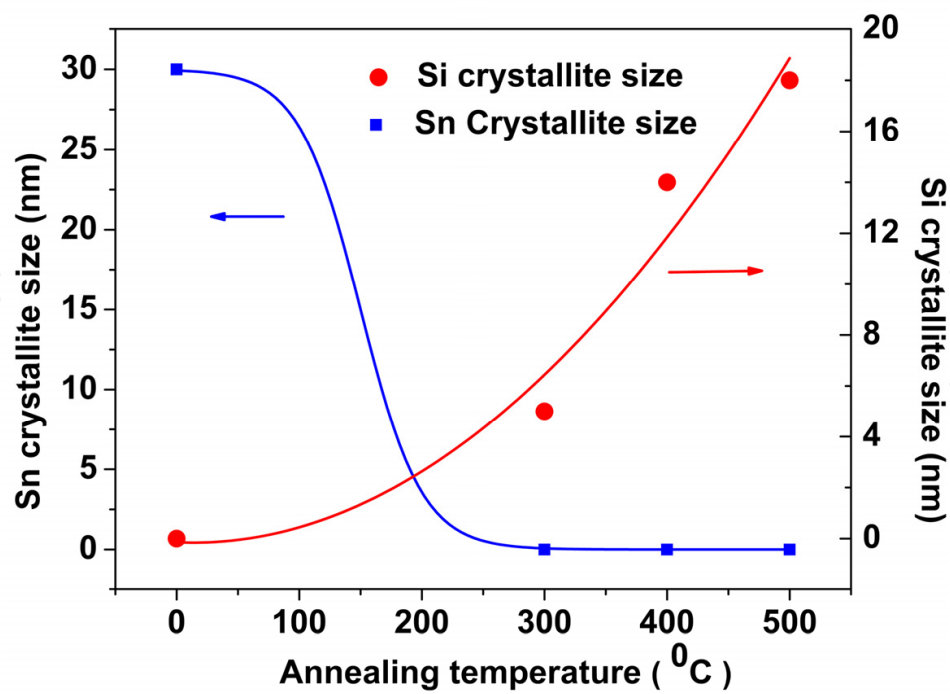


Fig. 3. Variation in crystallite size of Si and Sn phases as a function of annealing temperature (Mohiddon et al., 2012).

## 4. Nickel and Chromium induced crystallization

The second group of metals used for MIC, reacts with silicon and forms silicides. e.g.: Ni, Cr, Pd, Mo etc. A very small concentration of metals contamination crystallizes large part of *a*-Si and hence they are highly useful in electronic applications. In this section we review our work on Nickel and Chromium induced crystallization of amorphous Si films.

### 4.1 Nickel induced crystallization

Nickel induced MIC is one of best representatives of this group. To investigate Nickel based MIC, two types of experiments were carried out. In the first case, a 400 nm *a*-Si film is deposited on a fused silica (FS) substrate followed by 200 nm Ni film forming a FS/*a*-Si(400nm)/Ni(200nm) (FSN) stack. The depositions were carried out at different substrate temperatures from 200 to 400 °C. After the deposition is completed, the films were annealed in the vacuum, in the same chamber without removing the vacuum for 30 min at the deposition temperature. The second growth process involves the deposition of 200 nm Ni films on fused silica (FS) substrates followed by deposition of 400 nm amorphous Si films at 200 °C substrate temperature, forming a FS/Ni(200nm)/*a*-Si(400nm) (FNS) stack.

All films were deposited by the electron beam evaporation technique. The starting materials were granular pure silicon powder (99.999%) and nickel powder (99.99% pure). The vacuum chamber was evacuated using a diffusion-rotary pump combination equipped with a liquid nitrogen trap. A pressure  $5 \times 10^{-6}$  Torr was maintained throughout the depositions. In all cases the substrate to source distance was kept constant at 10 cm. The thickness of the film is measured after deposition using a surface profilometer (model XP-1 Ambios Tech., USA). X-ray diffraction patterns were recorded on a powder x-ray diffractometer (CPS120 of Inel, France) machine equipped with a Co  $K\alpha = 0.178896$  nm and gas phase position sensitive detector. XAFS measurements on the FNS stacks were performed at the Ni K-edge at 8333 eV, in fluorescence mode at two different incidence geometries. The two incident angles are 2° and total reflection (TR) geometries. In total reflection geometry only few nanometers of top layer are monitored (10 to 20 nm). At 2 degree incident angle, the entire thickness of the film is under focus. The radiation source was the European Synchrotron Radiation Facility (ESRF, Grenoble, France) and XAS measurements were performed at the BM08 (GILDA) beamline, with an average storage ring current of 180 mA. Data are collected in fluorescence mode using a 13- element hyper pure Ge detector.

Figure 4 shows the X-ray diffraction pattern (XRD) of FS/ *a*-Si / Ni (FSN) stack deposited at different substrate temperatures. The FSN stack deposited at 200 and 300 °C, shows similar XRD pattern. These XRD patterns, indexed according to the PCPDF file no -701849 and refined using PowderX software, are identified as belonging to face centred cubic (FCC) structure of metallic Nickel. The refined FCC unit cell parameter is  $a=3.53\text{\AA}$ . No change in the unit cell parameter is observed for the samples deposited at 200 and 300 °C. In the FSN stack deposited at 400 °C, it is observed that, a new set of XRD peaks are grown along with the metallic Ni XRD peaks. These new set of peaks belong to the face centre cubic structure of NiSi<sub>2</sub> indexed according to the PCPDF file no-652974. The FCC unit cell parameter of metallic Ni calculated using (111) peak is found to be 3.52 Å and that of NiSi<sub>2</sub> using (111) peak is 5.36 Å. There is no change in the unit cell parameter of metallic Ni for the samples

deposited at 300 and 400 °C. The small change of 0.01 Å is considered within the range of instrumental measurement error. The disagreement of our result with that of I. H. Hong et al. (Hong et al., 2006) work is expected to be due to the ultra high vacuum pressure deposition conditions, which may facilitate the reaction of the diffused metallic Ni atoms at lower temperature to form different silicide phases.

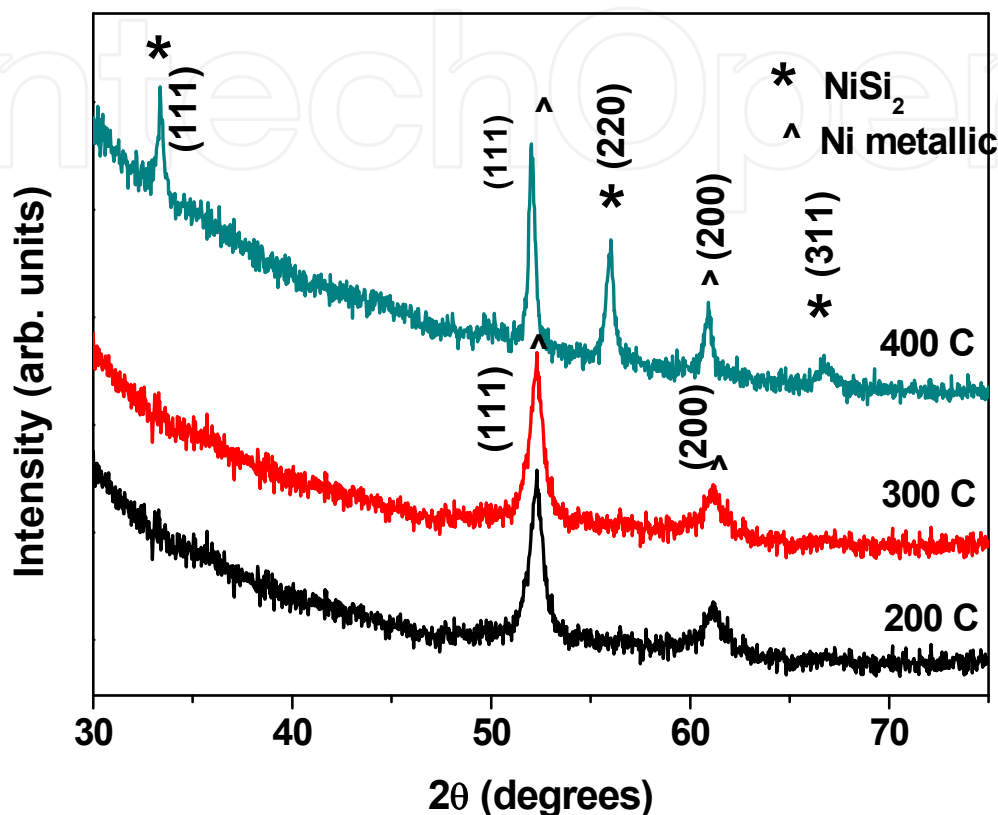


Fig. 4. XRD pattern of FS/Si/Ni stack deposited at different substrate temperature

Figure 5(a) compares the experimental XAFS spectrum of Ni K edge in the FNS stack, which were measured in two different incident angles (TR and 2 degree), along with the reference metal Ni foil spectra. The first derivative near the XANES region of the spectra is presented in the inset of the Fig. 5(a). The observation of the inset figure shows that the XANES spectra of FNS stack deposited at 200 °C and measured in TR and 2 degree has a resemblance with that of the metallic Ni reference spectra. The qualitative analysis of the XAFS spectra shows that the nature of Ni impurity measured in TR and 2 degree are similar and has close resemblance with that of the metallic Ni. Thus at 200 °C annealing temperature the metallic Ni, which is deposited at the bottom, is diffused through 400 nm a-Si layer. The similar observation is found in the EXAFS part of the spectra. Figure 5 (b) compares the Fourier Transform (FT) of the EXAFS signal at the K edge of Ni presented for the FNS stack deposited at 200 °C and measured in two different incident angles, along with the metallic Ni. It shows that the FNS stack deposited at 200 °C and measured in two different incident angles has the features of metallic Ni. The detailed quantitative analyses were carried by FEFF8 and FEFFIT theoretical code and are presented elsewhere (Mohiddon et al., 2011). The conclusions of the work are in good agreement with that of the qualitative analysis

presented above. Thus the EXAFS work carried on the FNS spectra support the Ni diffusion assisted MIC mechanism.

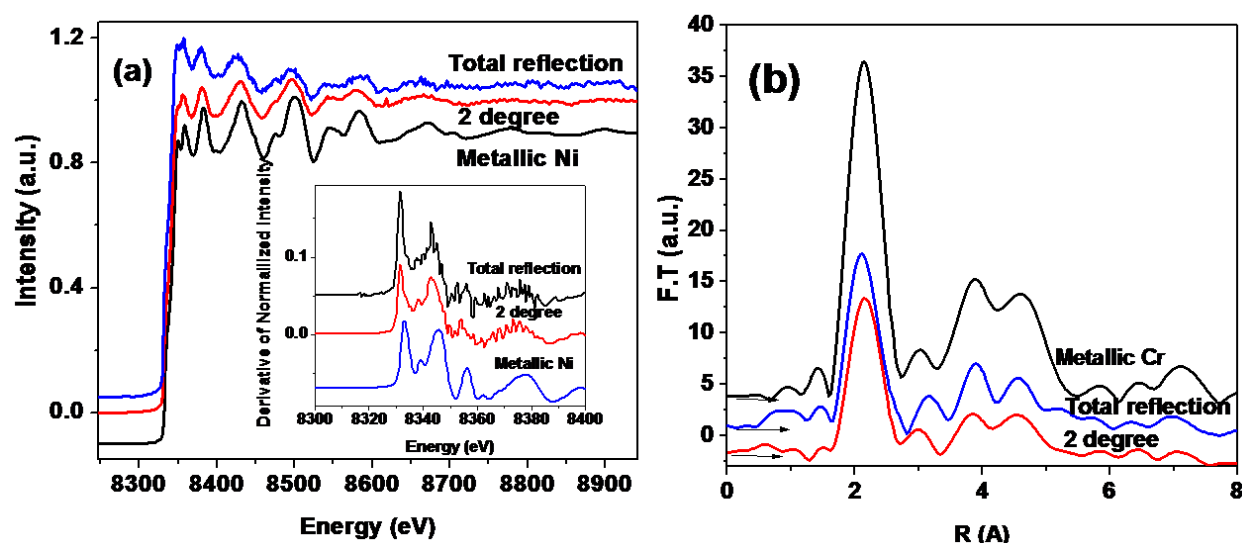


Fig. 5. (a) X-ray absorption spectra along with its derivative near XANES in the inset (b) F.T of EXAFS part of FS/Ni/a-Si stack deposited at 200 °C measured in total reflectance and 2 degree along with metallic Ni reference compound.

#### 4.2 Ni induced Lateral Crystallization (NILC)

As discussed in Sec 2.3, in case of MILC first metal silicides are formed at the interface between metal and Si layers at the lower annealing temperatures. On further annealing, nucleation of c-Si on metal silicide precipitate starts, which is then followed by migration of the metal silicide precipitates throughout the a-Si thin film, resulted in crystallization of the entire a-Si film. We have carried a detailed transmission electron microscope study and found similar observation. The details of the experiment are as follows. The Ni and Si films are deposited using electron beam evaporation at  $4 \times 10^{-6}$  Torr and ambient temperature onto Si (311) substrates. The starting materials were granular pure silicon powder (99.999%) and nickel powder (99.99% pure). The (311) oriented Si wafer was well cleaned and subjected to heat treatment of 1000 °C for 2 hours in normal furnace atmosphere to form a thick SiO<sub>2</sub> layer, before the deposition is started. The SiO<sub>2</sub> layer is expected to act as a barrier to stop the diffusion of Ni atom into the Si substrate. X-ray diffraction patterns were recorded in grazing incidence of 0.5 degree on a powder x-ray diffractometer (CPS120 of Inel, France) machine. A 50 nm film of Ni was deposited on the c-Si/SiO<sub>2</sub> substrate, followed by the deposition of the 400 nm Si film without breaking vacuum in the deposition chamber. The thickness of the films was measured after deposition using a surface profilometer (model XP-1 Ambios Tech., USA). The films were subsequently annealed in a furnace (in air) at 600 °C for 1 hr. Transmission electron micrographs were obtained by a Tecnai 20 G2 STwin, FEI electron microscope, operated at 200 kV. Electron diffraction patterns (EDPs) were recorded with a Gatan CCD camera. A 10 nm gold film deposited on the grid was used for purpose of camera length calibration. The samples for TEM measurement were prepared by scratching the film and transferring it on to the grid.

Figure 6 is a bright-field transmission electron micrograph obtained from a region of the Ni/Si thin film crystallized by the MIC process. The typical microstructure shown in Fig. 6 consists of dark region with an irregular shape. The microstructure consists of dark dendrite like nanowire structures spread over the bright looking matrix. To find the nature of nanostructures at two different regions of the sample, selected area electron diffraction (SAED) patterns were recorded at both the dark and bright regions of the sample. A typical diffraction pattern from the dark part of the image is shown in the inset of the Fig. 6. The diffraction pattern consists mainly of bright diffraction spots radially positioned on rings. The diffraction spots arise from the crystalline part of the sample, whereas the ring originates from the polycrystalline parts of the sample. The inset of the Fig 6 shows that, the diffraction spots A, B and C lie on the polycrystalline ring of radius 0.314 nm, which is indexed to the (111) plane of the Silicon diamond cubic phase. The spot D has a  $d=0.185$  nm which can be indexed as belonging to the  $(2\bar{2}0)$  plane of the diamond cubic system [JCPDS-892955]. However, an alternative assignment is possible for the same diffraction spots (and the higher order diffraction spots), because they can be equally assigned to the face centred cubic  $c$ -NiSi<sub>2</sub>, with  $d$  values of 0.312 nm, 0.191 nm for (111) and  $(2\bar{2}0)$  planes, respectively [JCPDS-652974]. Hence, the diffraction taken in dark region of the sample shown in Fig 6 may belong either to diamond cubic Si or to face centre cubic NiSi<sub>2</sub>.

The diffraction pattern from the bright area region is shown in other inset of Fig.6. Even at the higher magnifications, the microstructure of this bright region is seen to contain dark dendritic lines. It was thus impossible to eliminate the presence of the darker regions. Hence, the diffraction pattern shown in second inset of Fig 6 has a combined effect, with a small contribution from dark nanostructures and large contribution from the bright area. The diffraction pattern is slightly different from that of dark region diffraction pattern. It consists of one diffuse diffraction rings with  $d=0.314$  nm and a bright diffraction spots with  $d = 0.192$  nm (indicated by E). There are, however, no diffraction spots disposed on the ring, thus suggesting that the dark region is better crystallized compared to bright region although both the regions show similar diffraction patterns.

To exactly identify the material(s) that are contributing to the diffraction from the bright and dark regions of the samples, Z (atomic number)-contrast scanning transmission electron microscopy (STEM) study was carried out. Fig. 7 is a Z-contrast STEM image of the same selected area that is shown in Fig. 6. The change in contrast in the image is due to change in Z value of Ni ( $Z= 28$ ) and Si ( $Z= 14$ ). This observation suggests that the central high contrast region is due to the contribution from Ni component, i.e. NiSi<sub>2</sub> and the low contrast region is from Si. Hence, it can be inferred that the NiSi<sub>2</sub>, after its formation, starts spreading through the matrix of  $\alpha$ -Si like a dendrite and leading to its crystallization. The results presented in this part of the study supports the mechanism of movement of NiSi<sub>2</sub> dendrites in the  $\alpha$ -Si matrix as the cause for crystallization. Similar movement of Cu<sub>3</sub>Si dendrites with time of annealing was reported using in-situ bright field transmission electron microscopy, but no diffraction study was reported (Russell et al.,1991). It is evident from the study of SAED and STEM, that when Ni makes contact with the Si layer at high temperature it forms NiSi<sub>2</sub>. This silicide starts diffusing as dendrites into the  $\alpha$ -Si matrix and crystallizes the silicon at the nanoscale. This strongly supports the mechanism of silicide movement as the basis for crystallization of  $\alpha$ -Si by the MILC process using silicide forming metals.



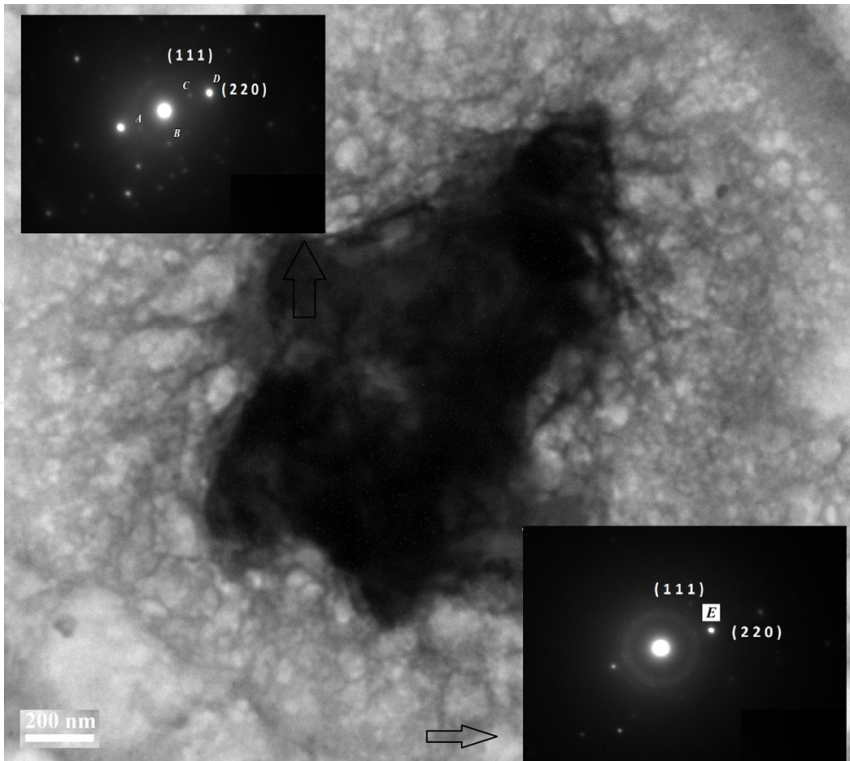


Fig. 6. Selected Bright field image of Ni/Si film annealed at 600 °C for 1 hr along with the diffraction pattern in the dark and bright regions (Mohiddon et al., 2012).

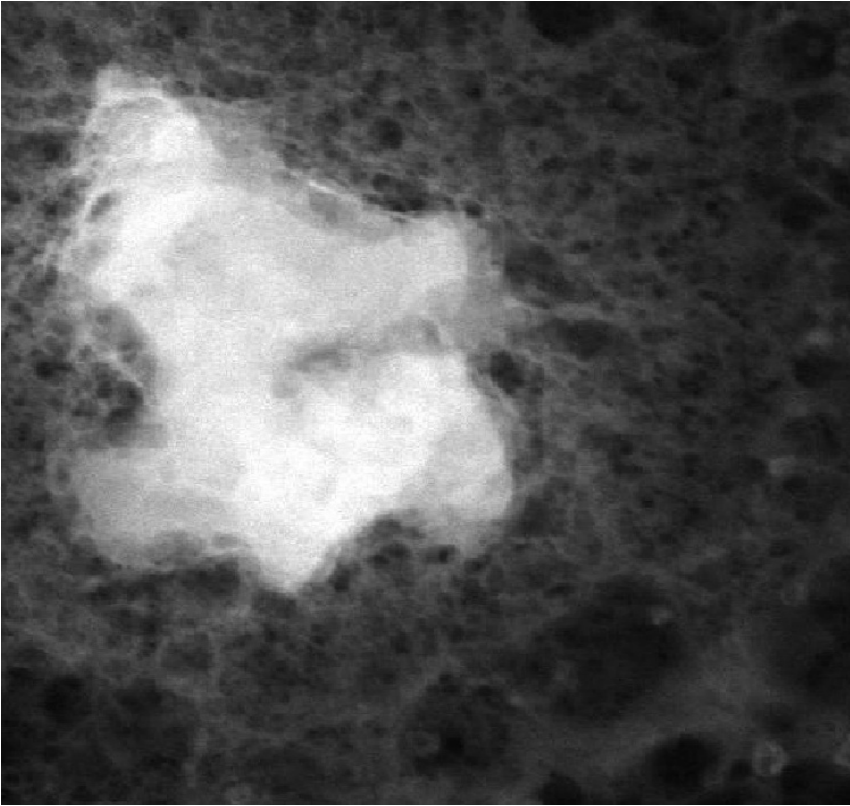


Fig. 7. STEM image of Ni/Si film annealed at 600 °C for 1 hr, selected the same region as that selected for bright field image in Fig. 6 (Mohiddon et al., 2012).

### 4.3 Stabilization of wurtzite Si

Silicon (Si) usually crystallizes in the cubic diamond structure with fourfold coordinated symmetry. Numerous high-pressure experiments have been performed, revealing no less than 12 different polymorphs of silicon between the well characterized diamond cubic phase and theoretically intractable amorphous phase. With the release of pressure several metastable phases are observed (Wu et al., 2000). For example, a nonmetal to metal transition of cubic-diamond phase, which occur by changing pressure from ambient atmosphere pressure to 10–13 GPa to form a  $\beta$ -tin structure, which further transforms to rhombohedral phase on slow pressure release. The latter transforms reversibly at a pressure of 2 GPa to a body-centered-cubic phase (bc8). Hexagonal-wurtzite silicon (*w*-Si) can be formed by heating the bc8 phase to above 200 °C, or directly from the cubic-diamond phase in the presence of shear stresses at twin intersections, or a nonhydrostatic stress of 8 GPa in indentation experiments (Kailer et al., 1997). *w*-Si material is rarely studied by spectroscopic measurements, because it cannot be obtained in a stable phase. Zhang et al. (Zhang et al., 1999) produced stable *w*-Si phase by laser ablation. Its identification by electron diffraction has been confirmed by micro Raman spectroscopy. Bandet et al. (Bandet et al., 2002) deposited *w*-Si during elaboration of SiO<sub>2</sub> thin films and reported that oxygen seems to play a crucial role in the stabilization of the uncommon metastable structure. Kim et al. (Kim & Lee, 1996) reported that the micrometer-sized diamond cubic silicon (*c*-Si) crystals contain minority part of hexagonal silicon, when *a*-Si films crystallized by a pulsed laser. In the present work we have crystallized the silicon film with a metal contact and found that silicon nanocrystals are in wurtzite phase. The experimental details are as follows.

Nickel and Silicon films were deposited by electron beam evaporation on to BoroSilicate Glass (BSG) substrates in high vacuum of the order of 10<sup>-6</sup> Torr. First a 50 nm thin nickel blanket bottom layer was deposited on BSG substrates maintained at ambient temperature. This is followed by the deposition of a 700 nm thick Si film, without breaking vacuum, to form a BSG/Ni/Si stack. The thickness of the films is measured after deposition using a surface profilometer (model XP-1 Ambios Tech., USA). The films were annealed in a furnace atmosphere (in air) at 550 °C for 1 hr. Transmission electron micrographs were obtained by a Tecnai 20 G2 STwin, FEI electron microscope, operated at 200 kV. Electron diffraction patterns (EDPs) were recorded with a Gatan CCD camera. A 10 nm gold film deposited on the grid was used for camera length calibration purposes. The Raman spectra were recorded in air using an Nd-YAG 532nm laser in the back scattering geometry in a CRM spectrometer equipped with a confocal microscope and 100× objective (1  $\mu$ m diameter focal spot size) with a CCD detector (model alpha 300 of WiTec Germany). The phase content within the samples was investigated in a spectral region 200–1500 cm<sup>-1</sup>.

Figure 8 shows the electron diffraction pattern along with a bright-field transmission electron micrograph obtained from a region in the silicon thin film crystallized by a MIC process in the inset. The typical microstructure shown in inset of Fig. 8 consists of spherical particles with approximate diameter of 40 nm. The diffraction spots can be identified as belonging to well defined orientations. The calculated interplanar distances of the lattice planes at different spots of the diffraction pattern assigned by symbols A, B and C in Fig. 8 are A=0.256nm, B=0.237nm and C=0.237nm respectively. From the calculated interplanar distances the diffraction spots were indexed as (1 0 0), (1  $\bar{1}$  1), and (0  $\bar{1}$  1), which belong to

the  $[1\bar{2}13]$  zone axis. These diffraction spots were diffracted from a grain which has a hexagonal structure and oriented in the  $[1\bar{2}13]$  direction and the spots A, B and C were diffracted from the  $(1\ 0\ 0)$ ,  $(1\ \bar{1}\ 1)$ , and  $(0\ \bar{1}\ 1)$  planes, respectively. The corresponding lattice parameters were calculated as  $a=0.296\text{nm}$ ,  $c=0.629\text{nm}$  with  $c/a$  ratio of 2.13. The angle calculated between these planes using  $c/a$  value is found to be exactly matching with the measured values suggesting that Si crystallizes in the hexagonal structure. For diamond cubic Si crystals the interplanar distances of the lattice planes should be  $0.331\text{ nm}$ ,  $0.331\text{ nm}$  and  $0.331\text{ nm}$ , respectively and the angles between transmitted beam and the spots will be  $60^\circ$ ,  $60^\circ$  and  $60^\circ$ , with the diffraction pattern showing the expected six fold symmetry. But in our case the angles between the dots are  $\angle AOB=65^\circ$ ,  $\angle BOC=55^\circ$  and  $\angle AOC=120^\circ$  respectively, clearly not cubic Si. Thus, the TEM analysis shows that the BSG/Ni/Si stack after heat treatment consists of  $40\text{ nm}$  particles of hexagonal Silicon with  $c/a$  ratio of 2.11.

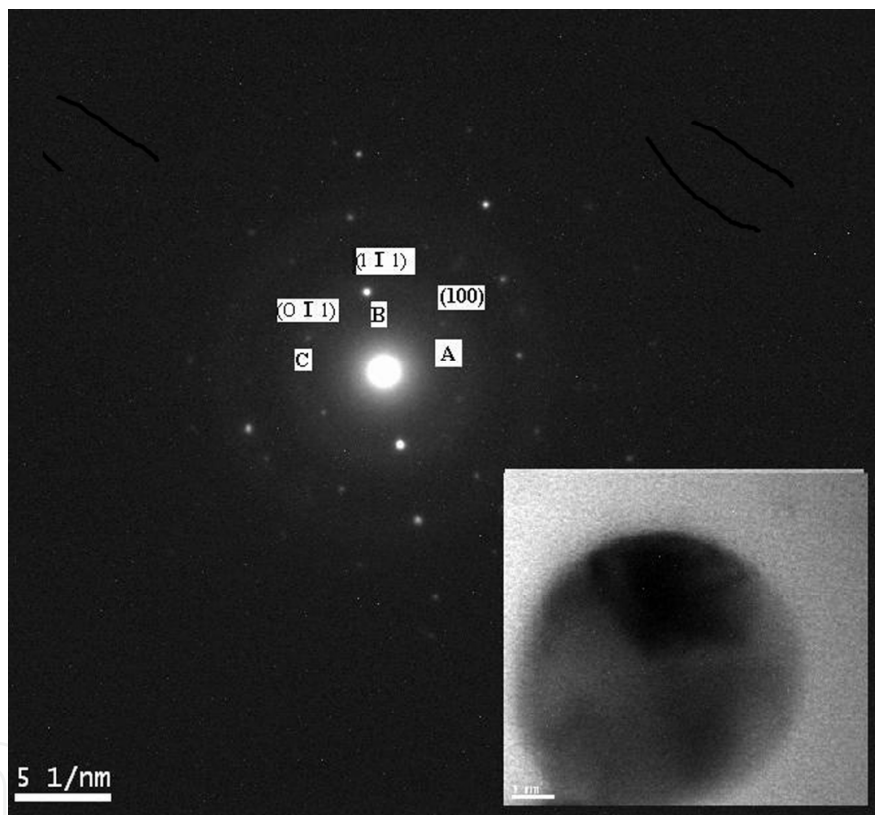


Fig. 8. Electron diffraction pattern along with indexing of selected *w*-Si part of BSG/Ni/Si stack, inset shows the bright field image of the above diffraction area (Mohiddon et al., 2011).

Additional evidence for the formation of *w*-Si is provided in the form of Raman spectra of the films. It has earlier been used to characterize porous Si (Sui et al., 1992) and Si nanostructures (Kozłowski et al., 1991). The Raman shift and the shape of the Raman peak yield information on the degree of crystallinity achieved in Si by MIC process. Fig. 9 shows Raman spectra from the Silicon film annealed at  $550^\circ\text{C}$  for 1 hrs. A sharp peak at  $504\text{ cm}^{-1}$  and weak broad peaks around  $300$  and  $950\text{ cm}^{-1}$ , are associated with optical phonons, two transverse acoustic (2TA) phonons, and two transverse optical (2TO) phonons of *w*-Si (Mohiddon & Kirshna, 2011) respectively. The asymmetric nature of the peak at  $504\text{ cm}^{-1}$  can

be attributed to the presence of small amounts of *a*-Si, which has its broad peak at 480  $\text{cm}^{-1}$ . In general, when Si crystallizes in cubic diamond structure the Raman peak occurs at 520  $\text{cm}^{-1}$ . The broad peaks around 283  $\text{cm}^{-1}$  in our observation may belong to NiSi, NiSi<sub>2</sub> or to wurtzite TA mode. However, this observation implies that, at 550 °C nickel silicides are present in the samples. Tan et al. (Tan et al., 1981) reported a stress-induced metastable form of hexagonal silicon, which has the wurtzite structure with the *c/a* ratio close to 1.63, the interplanar distances and angular relationships obtained from our experiment were compared with those of silicon which has the wurtzite structure. However, for silicon with the wurtzite structure, no exact coincidence was obtained. Kim et al. reported a similar observation and suggested a hexagonal phase of Si with *a*=0.382nm, *c*=1.024nm and *c/a* ratio of 2.68 (Kim & Lee, 1996). Parson and Hoelke suggested similar hexagonal structure for Ge with *c/a* ratio of 2.17 (Parsons & Hoelke, 1983). Zhang *et al.* (Zhang et al., 1994) have reported wurzite silicon by laser ablation, and they observed Raman peaks at 516 and 518  $\text{cm}^{-1}$  due to the hexagonal silicon. Kumar et al. observed *w*-Si in chromium induced Si crystallization evidenced by a peak at 495  $\text{cm}^{-1}$  (Kumar & Krishna, 2008). Kailer et al. (Kailer et al., 1997) performed indentation Raman investigations and reported that, shear deformation leads to *w*-Si phase formation. They also stated that high pressure phases leads to the formation of *w*-Si at moderate temperatures and to the reversal to the original diamond structure at higher temperatures.

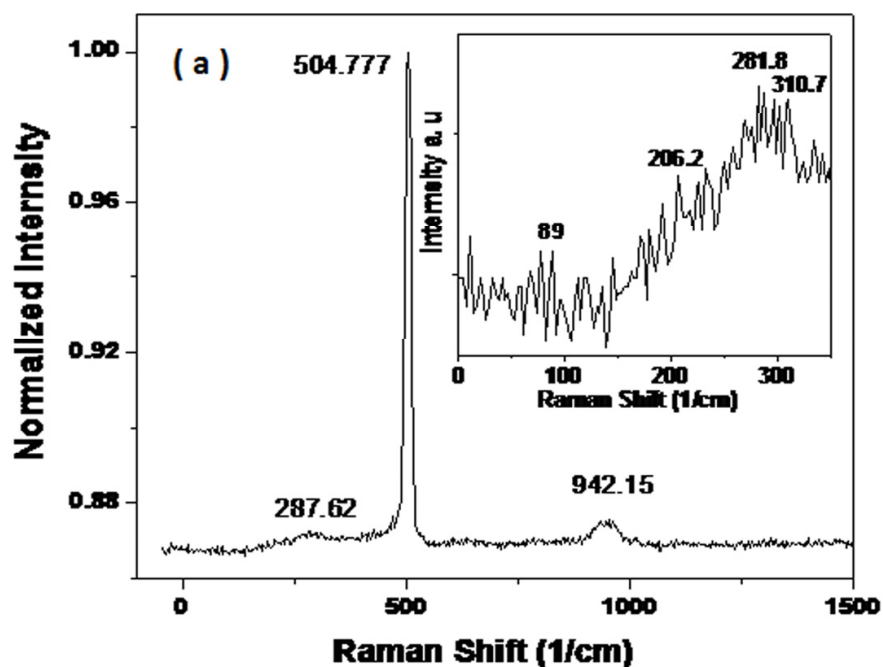


Fig. 9. Raman spectra of BSG/Ni/Si stack annealed at 550 °C for 1 hr (Mohiddon et al., 2011).

#### 4.4 Chromium induced crystallization

The search for new metal in the MIC process to reach the best device needs is still continuing. Chromium is another member of the silicide forming group. Chromium induced MIC has attracted our attention due to its ability to crystallize the *a*-Si into wurtzite phase (Kumar et al., 2008). Our recent work on Cr induced MIC has revealed that Cr deposited at



ambient substrate temperature, remains in metallic form up to an annealing temperature of 500 °C (Mohiddon et al.,2012). As an extension of this work we deposited Cr/Si stacks at higher substrate temperature. The details of the experiment are as follows. A Cr(200nm)/Si(400nm) stack was deposited over a fused silica substrate at 400 °C substrate temperature and a similar stack of Cr(50nm)/Si(400nm) was deposited at ambient substrate temperature by electron beam evaporation. We have carried out a detailed EXAFS analysis on these set of samples (Mohiddon et al., 2012) and concluded that, in case of film deposited at ambient substrate temperature, major part of Cr is in metallic form apart from being oxidized. In the case of the stack deposited at 400 °C, the entire Cr is turned into either  $\text{Cr}_2\text{O}_3$  or  $\text{CrSi}_2$ . After the EXAFS measurements were completed, these samples were heat treated at different temperatures. Samples were annealed at 600, 650 and 700 °C for 1 hr. in excess Nitrogen flux. At each heat treatment, the samples were cooled to room temperature and then the Raman spectra were collected at three to four different position of the each sample. The details of the Raman measurements are discussed in the section 4.3 of the chapter.

Figure 10(a) and 10(b) compares the Raman spectra of the FS/Cr(200nm)/Si(400nm) stack deposited at 400 °C substrate temperature and FS/Cr(50nm)/Si(400nm) stack deposited ambient substrate temperature then annealed at different temperature respectively. Fig. 10(a) shows that the as deposited stack contains a broad diffuse peak around 480  $\text{cm}^{-1}$  that can be assigned to the a-Si phase. After the heat treatment at 600 °C, a sharp peak, around 520  $\text{cm}^{-1}$ , appears that can be assigned to c-Si. The intensity of the c-Si peak increases with annealing temperature up to 650 °C. No such change in the Raman spectra is observed in the Fig. 10 (b). By combining the EXAFS and Raman results we conclude that the FCS stack deposited at 400°C possesses  $\text{CrSi}_2$  in the as deposited condition. When this stack is further heat treated the  $\text{CrSi}_2$  acts as a seed for the Si crystallization. In the FCS stack deposited at minimum substrate temperature, no  $\text{CrSi}_2$  is observed from the EXAFS analysis. Thus even after heat treatment up to 700 C, no Si crystallization is observed.

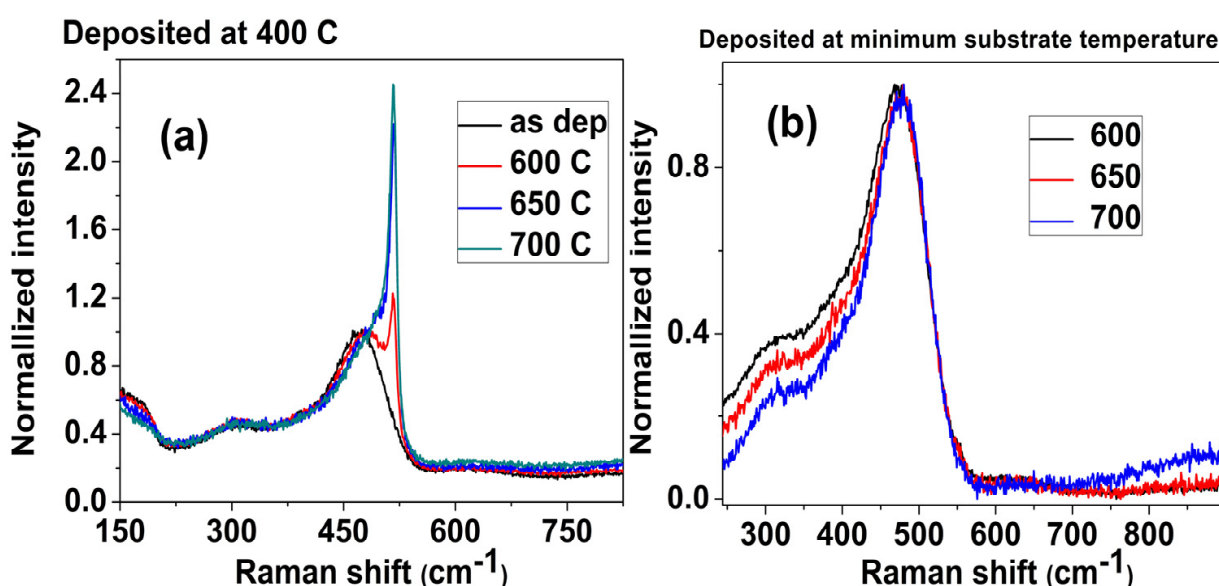


Fig. 10. Raman spectra of (a) FS/Cr(200nm)/Si(400nm) stack deposited at 400 C; (b) FS/Cr(50nm)/Si(400nm) stack deposited at minimum substrate temperature



## 5. Conclusions

An overview of the metal induced crystallization of amorphous Silicon films has been presented. The possible mechanisms of crystallization have been discussed. Two metals Sn and Cr, which have not been used frequently for MIC, have been shown to be as attractive as Ni for metal induced crystallization of a-Si. Our results clearly show that these are very useful materials for the microelectronics industry. The crystallization occurs at temperatures ranging from 200 to 600 °C, which is much lower than the normal crystallization temperature of Si (1100 °C). The stabilization of the wurtzite form of Si has also been demonstrated.

## 6. Acknowledgements

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