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Enhanced Control of Carbon Nanotube Properties Using MPCVD with DC Electrical Bias

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1. Introduction

The engineering properties of carbon nanotubes (CNTs) allow for an extraordinarily large potential application space including thermal management, integrated circuits, mechanical reinforcement, and medical devices, among others. CNTs are generally characterized by the quantity of concentric graphene shells comprising their cylindrical wall structure. CNTs consisting of a single graphene cylinder are characterized as single-walled CNTs (SWNTs), while multiple concentric graphene cylinders are called multi-walled CNTs (MWNTs). Typical diameters for SWNTs are approximately 1–3 nm, while MWNTs diameters range from approximately 2 nm to greater than 100 nm. The unique atomic arrangement of a SWNT dictates that each atom resides on both the interior and exterior of the structure, with the atomic orientation defined by a chiral vector relating the fully traversed perimeter of the SWNT to the unit vectors of a graphene sheet. Two thirds of SWNT chiralities are electrically semi-conducting, exhibiting an electronic band gap inversely proportional to their diameter, while the remaining third are metallic. Though the transport properties of MWNTs are degraded relative to SWNTs by the wall-to-wall interactions, they may still exceed the properties of traditional macroscale materials such as copper or aluminum. Despite the advantageous properties offered by CNTs, their integration into functional materials and devices in a manner that maximizes their benefit remains a significant technical and engineering challenge. Specific applications may demand a unique blend of characteristics such as diameter, alignment, purity, density, and chirality to maintain proper operation. *In situ* morphology and orientation control of CNTs during synthesis represents a promising path towards selectivity of these device-specific requirements, especially for applications requiring CNTs with engineered properties to be synthesized directly on a functionalized substrate. We examine the role of dc electrical bias during microwave plasma-enhanced chemical vapor deposition (MPCVD) synthesis of SWNTs using alignment, spatial density, chirality, and purity as metrics of interest. Further, we demonstrate enhanced thermal and electrical transport properties of MWNTs realized with application of substrate bias during MPCVD synthesis.

* contributed equally to this work.

2. Plasma enhanced Chemical Vapor Deposition

There is a wide range of methods for producing CNTs such as laser ablation, arc discharge, pyrolysis, and chemical vapor deposition (Huczko, 2002; Rakov, 2000). Chemical vapor deposition (CVD) has emerged as the method of choice for producing CNTs because of its simplicity, flexibility and affordability as well as the potential for scalability and the precise control of CNT properties. In a typical CVD growth process, a carbon precursor such as a hydrocarbon gas is heated to 750-900°C in the presence of a suitable catalyst (e.g., Fe, Co, and Ni), and if all the other reaction conditions such as catalyst particle size, nature of the catalyst support, and gas composition are optimized, nucleation and growth of CNTs proceeds. The CVD process is highly unique because CNTs grow from catalyst 'seeds,' and several studies have shown that there is an intimate relationship between the catalyst properties and the nanotube properties (Amama, et al., 2005a; Hofmann, et al., 2003). In other words, with proper control of the catalyst properties, it is possible to grow CNTs of controlled properties via CVD (Amama, et al., 2007; Amama, et al., 2010; Crouse, et al., 2008; Maschmann, et al., 2005; Zhu, et al., 2010). As such, an appreciable amount of research is underway to fully understand catalyst evolution during synthesis (Amama, et al., 2010; Amama, et al., 2009; Kim, et al., 2010). The CVD process typically involves either thermally driven gas phase decomposition of a hydrocarbon gas (thermal CVD) or both thermal and plasma decomposition (plasma-enhanced CVD).

MPCVD growth has received significant attention mainly because of the potential for low temperature CNT synthesis required for compatibility with standard nanofabrication and CMOS processes and the ability to produce highly graphitized, vertically aligned CNTs (Amama, et al., 2006a; Meyyappan, et al., 2003). A distinguishing feature of the MPCVD process is the presence of a highly reactive plasma environment, which enhances the decomposition of the hydrocarbon feedstock during CNT growth. The generation of highly energetic ions by the plasma and their subsequent transport to the growth surface are two critical factors that influence the growth properties (Yen, et al., 2005). Using the wide parameter space of the MPCVD, a key advantage over other CVD processes, low temperature growth (Amama, et al., 2006a; Boskovic, et al., 2002), CNT alignment (Maschmann, et al., 2006d), chiral (Li, et al., 2004) and diameter control (Amama, et al., 2006b) have been demonstrated. In many plasma-enhanced CVD studies, the plasma source used is microwave energy which is characterized by high plasma density with a resonant field that is able to concentrate the plasma, ensuring that significant electron loss to the surrounding does not occur (Yen, et al., 2005). The plasma intensity is controlled by the microwave power while the ion flux directed at the substrate may be controlled by a dc bias voltage applied to the growth substrate. These parameters operate independently in MPCVD and are capable of substantially altering the properties of CNTs.

3. Effect of DC electrical bias during SWNT synthesis

3.1 Negative polarity electrical bias

Strict vertical alignment of CNTs may be of significant advantage for many applications, including electron emitters, mechanical enhancement, and high-density electronics. Although direct synthesis of vertically aligned or vertically oriented SWNT arrays has been commonly reported in the literature (Iwasaki, et al., 2005; Maruyama, et al., 2005; Murakami, et al., 2004; Zhong, et al., 2005), most refer to a general packing of SWNTs of subsequent

density that the growth front advances and remains in plane with the originating growth substrate. Closer examination of these arrays clearly reveals that individual CNTs within the array exhibit significant waviness and inconsistent orientation with respect to the substrate. Gravity assisted thermal CVD synthesis has reportedly resulted in freestanding vertical SWNTs by orienting the growth substrate upside down such that the SWNT growth direction corresponds to the direction of the gravitation field during synthesis (Yeh, et al., 2006). The vertical orientation using the gravity assisted technique seems to diminish for growth times greater than 1.5 minutes, as the free tips of sufficiently long SWNTs contact and are retained by growth substrate due to thermal vibrations (Yeh, et al., 2007). Others have designed catalyst systems embedded within a modified porous anodic alumina (PAA) template. SWNTs originating from within isolated vertical pore will follow the pore axis toward its opening, resulting in a vertical orientation (Maschmann, et al., 2006a; Maschmann, et al., 2006b). Though this technique successfully aligns individual SWNTs within the confined vertical pores, the free ends of SWNTs emerging from the pores adhere strongly to the top horizontal PAA surface rather than maintaining vertical alignment. SWNT functionalization within the vertical pore structure has been achieved (Franklin, et al., 2009a; Franklin, et al., 2009b), though the misaligned SWNTs on the top surface of the template may be unattractive for some applications. Assembly of freestanding vertical SWNTs from solution post-synthesis is also achievable through electrophoresis into predefined vertical vias etched in a silicon nitride mask (Goyal, et al., 2008). This technique yields variable SWNT deposition with respect to overall occupancy of pores and the number of SWNTs deposited per occupied pore, and SWNTs requires magnesium nitrate hexahydrate to encourage improved substrate adhesion. Though these techniques successfully generate vertically aligned SWNTs, each requires either substrate manipulation or significant catalyst processing, which may be undesirable or impractical for practical application.

Chiral selectivity, with respect to metallic or semiconducting behaviour, is important for optimal operation of many types of devices. Metallic CNTs are obviously well suited for applications requiring high current carrying capacity, such as electrical interconnects (Close, et al., 2008; Kreupl, et al., 2002); however, they may also be advantageous in devices requiring high sensitivity to small electrical potential changes, such as electro-chemical biological sensors (Claussen, et al., 2009). Field effect transistors utilizing semiconducting SWNT channels have been extensively studied and have been found to exhibit ballistic electronic transport even at room temperature operation (Franklin and Chen, 2010). Application of SWNT transistors in electronics offer obvious dimensional and efficiency advantages, and significant research continues in this area with respect to device processing and characterization. The strong preferential growth of semiconducting (Li, et al., 2004) or metallic (Harutyunyan, et al., 2009) SWNTs to population densities greater than 90% chirality selectivity have been reported in the literature by utilizing remote RF plasma and control of gas composition during annealing, respectively. We demonstrate the preferential selectivity of both vertical alignment and semiconducting chirality through the use of negative polarity substrate bias applied during SWNT synthesis using MPCVD.

To investigate the influence of DC electrical bias on SWNT synthesis, a SEKI AX5200S MPCVD reactor with electrically grounded chamber walls, shown schematically in Fig. 1. A hollow stainless steel rod contacts the bottom surface of an otherwise electrically isolated graphite heater stage and delivers a dc potential via a voltage-controlled current source (Sorensen DCS600-1.7E). A K-type thermocouple embedded in the rod monitored the stage

temperature, while the growth substrate surface temperature was measured using a dual wavelength pyrometer (Williamson model 90). The silicon growth substrate rested on a 5.08-cm diameter, 3.30-mm thick molybdenum puck used to concentrate the plasma directly above the sample.

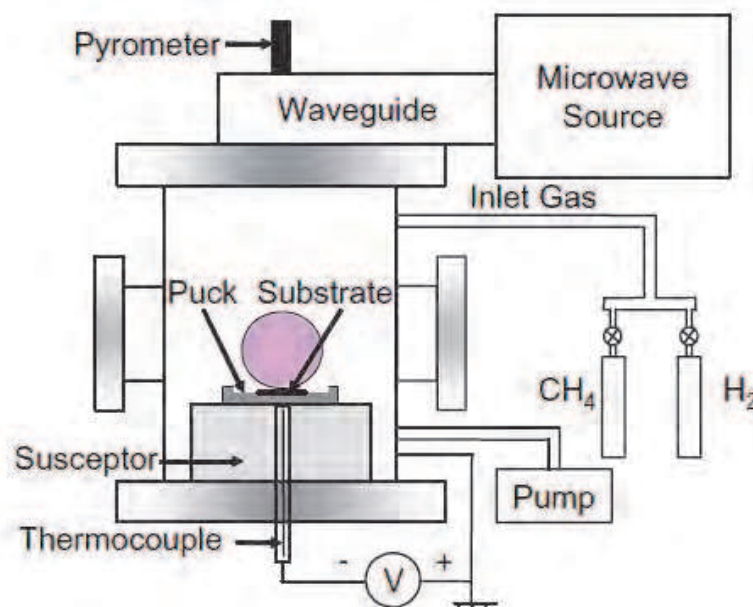


Fig. 1. Schematic of microwave plasma-enhanced chemical vapor deposition chamber.

An MgO supported Co catalyst was utilized for each SWNT synthesis. The catalyst particles were prepared by a wet mechanical mixing and combustion synthesis procedure using a solution of molybdenum, cobalt nitrate hexahydrate, and magnesium nitrate to produce bimetallic Mo/Co catalyst particles embedded in a nanoporous MgO support (Maschmann, et al., 2006d; Maschmann, et al., 2006c). The susceptor was first heated to 900°C in 50 sccm of flowing hydrogen at a pressure of 10 Torr. A dc substrate bias between 0 and -250 V was applied gradually to the substrate at a rate of approximately -25 V/second after ignition of a 200 W microwave plasma. Methane was then introduced at a flow rate of 5 sccm to initiate CNT growth. Each synthesis was 20 minutes in duration. The surface temperature of the substrate recorded by the pyrometer was approximately 770°C and relatively insensitive to the applied bias.

Characterization of the SWNT product was performed using a Hitachi S-4800 field emission scanning electron microscope (SEM) and Senterra micro-Raman spectrometer. Laser excitation wavelengths of 533 and 785 nm were selected for recording Raman spectra, with at least ten locations examined for each sample. SEM characterization was utilized to assess SWNT relative alignment with respect to the growth substrate, SWNT length, density, and diameter estimates of individual SWNTs and SWNT bundles. Multi-excitation wavelength Raman spectra analysis allowed for quantification of SWNT quality, diameter distributions, and relative trends with respect to SWNT chirality.

The application of negative bias strengthens the electric field inherently present in the plasma sheath region immediately above the substrate, thereby accelerating the impingement of positively charged ions, such as H^+ , towards the substrate. A plasma sheath is established as a result of the large mobility mismatch between ions and free electrons

generated within the plasma. The relatively low mass of electrons allows them to acquire a translational speed many times greater than that of the relatively heavy ions and accelerate away from the central concentrated plasma sphere located above the substrate.

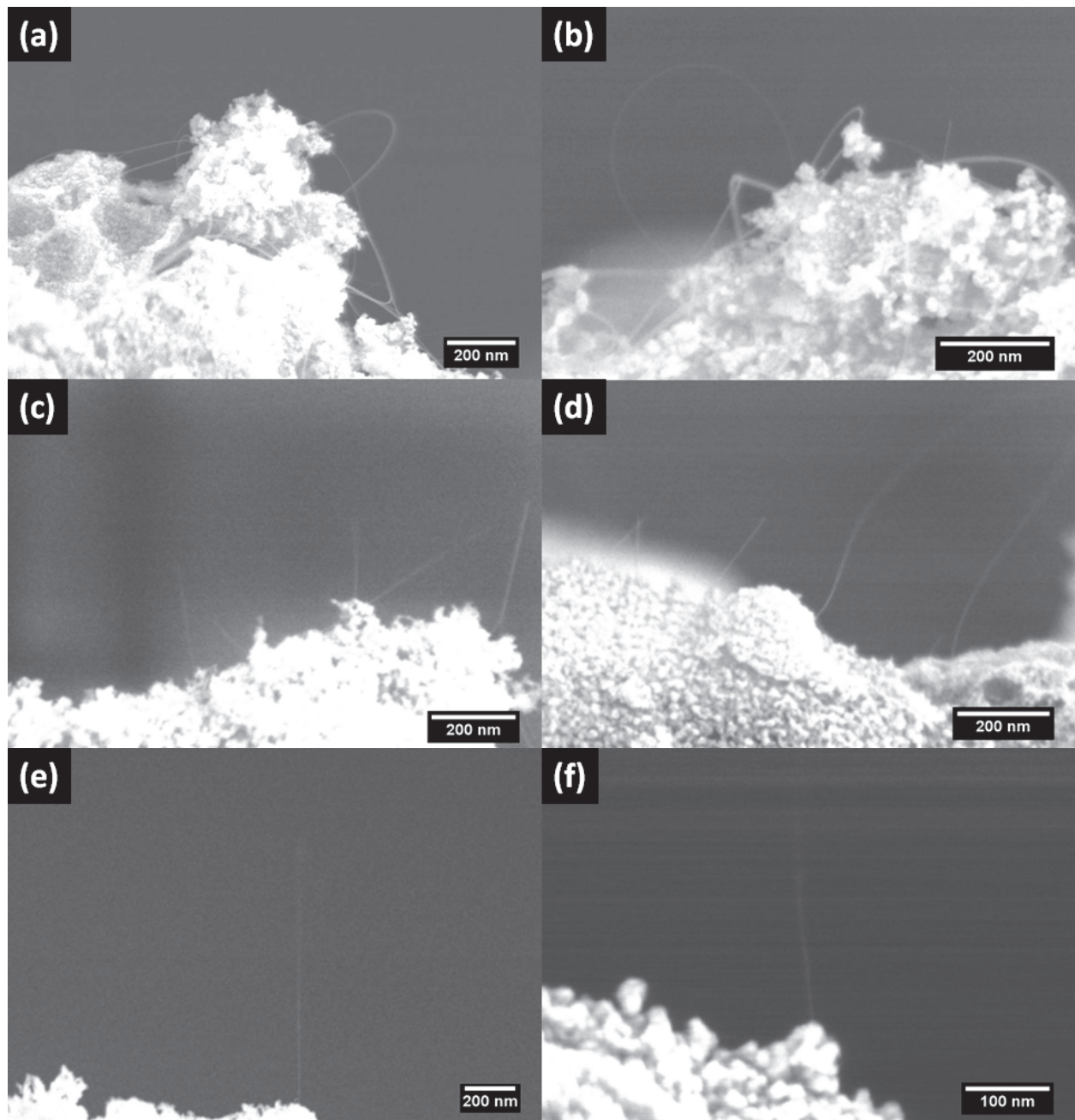


Fig. 2. Cross sectional SEM micrographs of SWNTs synthesized under negative polarity substrate bias in MPCVD at (a) 0V, (b) -50V, (c) -100V, (d) -150V, (e) -200V, and (f) -250V. From (Maschmann, et al., 2006d)

The bulk plasma is therefore electron deficient, setting up a net positive charge with respect to chamber walls, and an electric field is generated between the plasma and the surrounding surfaces. The highly anisotropic polarization of CNTs (Benedict, et al., 1995) establishes an interaction force between the CNT and the enhanced electric field near the growth substrate.

The magnitude of interaction is of sufficient magnitude to orient SWNTs (Peng, et al., 2003; Ural, et al., 2002; Zhang, et al., 2001) and multi-walled CNTs (Jang, et al., 2003; Merkulov, et al., 2001; Meyyappan, et al., 2003) along electric field lines *in situ* during CVD synthesis as well as during post-synthesis processing procedures (Kamat, et al., 2004; Yamamoto, et al., 1998).

Negative polarity substrate bias was systematically varied between 0 and -250V in 50V increments (Maschmann, et al., 2006d). Cross-sectional SEM analysis revealed distinct trends with respect to both SWNT spatial density and orientation relative to the growth substrate, as seen in Fig. 2a-f. SWNTs grown in the absence of applied bias or at -50V had a tendency to form large diameter bundles that generally followed the profile of the MgO support particles. No preferential growth perpendicular to the growth substrate was observed. The SWNTs synthesized at -100V and -150V, however, demonstrated a strong tendency to break free of the support particle in favor of a vertical orientation, normal to that of the support particles. SWNTs grown at these bias levels also tended to form bundles, with many longer SWNT bundles formed vertically oriented loops. A decrease in overall spatial density relative to the synthesis performed without bias may also be discerned. At the greater bias magnitudes of -200V and -250V, a strong preference to vertical alignment is observed, in addition to a marked decrease in SWNT spatial density. Very few SWNTs were observed along the perimeter of the catalyst support particles, as is typically observed when bias is omitted from synthesis. Freestanding SWNTs with lengths of several microns were frequently observed, though the free tips of these SWNTs were often obscured by thermal vibrations.

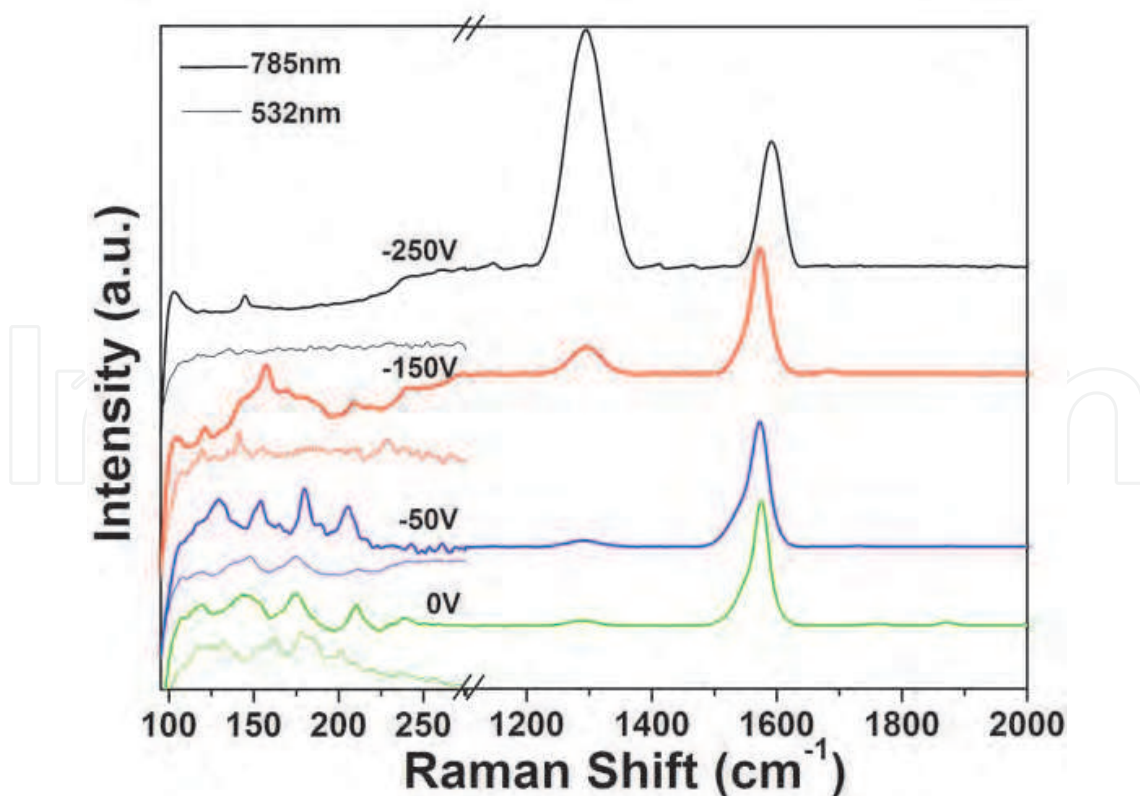


Fig. 3. Raman spectra of SWNTs synthesized using negative substrate bias in MPCVD. From (Maschmann, et al., 2006d).

Raman spectra of the SWNTs synthesized with negative polarity bias revealed another trend not readily observable from SEM observation, likely due to SWNT bundling and the inherent resolution limitations of the SEM. The radial breathing mode (RBM) distributions gradually skewed to lower frequency Raman shifts with increased levels of negative bias. Because SWNT diameter is inversely proportional to RBM frequency (Bachilo, et al., 2002; Rao, et al., 1997), the RBM distributions shift suggests a trend towards larger diameter SWNTs (up to 2.5 nm) as negative bias is increased. Locating the RBM peaks relative to excitation wavelengths on a Kataura plot (not shown) indicates that SWNTs synthesized without bias are a mix of metallic and semiconducting chiralities (Maschmann, et al., 2006d). Magnitudes of negative bias at and above -150V shift the measured RBM frequencies into bands of exclusively semiconducting chiralities. Additionally, a decreasing trend in the G- to D-band ratio is a further indication of a decreased spatial density observed by SEM and is perhaps an indication of an increased occurrence of SWNT wall defects. The Lorentzian lineshape of the G-band obtained from SWNTs under high levels of negative bias further support the RBM trend indicating a high concentration of semi-conducting SWNTs (Brown, et al., 2001; Pimenta, et al., 1998). The predominance of larger diameter SWNTs and corresponding decrease in SWNT density is thought to be a result of enhanced H^+ ion bombardment, which is known to preferentially etch small diameter SWNTs (Zhang, et al., 2005). Metallic SWNTs may have also been burned up as a result of transmitting a high current density.

3.2 Positive polarity electrical bias

Application of dc bias that is positive with respect to chamber walls is believed to decrease the magnitude of the electric field within the plasma sheath region near the growth substrate. H^+ ions, generated in abundance within the plasma, therefore attain a lower translational velocity before encountering the growth substrate. In fact, because the substrate in this configuration is the surface of greatest potential relative to the grounded chamber, the ions are instead more readily attracted toward the chamber walls. The mitigation of potentially harmful H^+ ion bombardment on the growth substrate is examined by varying the magnitude of positive polarity dc electrical bias during MPCVD SWNT synthesis, similarly to the methodology described in the previous section.

Substrate bias was varied between 0 and +200V in 50V increments while maintaining otherwise standard synthesis conditions. Bias levels of +250V or greater were attempted, but consistently led to plasma instabilities and were not further examined. Within the bias range of 0 and +100V, only incremental increases in SWNT spatial density were observed. SEM micrographs obtained from samples synthesized within this range of biases, shown in Fig. 4 (a) and (b), reveal SWNT bundles spanning tens of microns in length and tens of nanometers in diameter. No preferential vertical alignment of SWNTs is observed for these samples using cross-sectional SEM imaging (not shown). Larger biases of +150V and +200V resulted in dramatic increases in SWNT density, with a significant population of large-diameter SWNT ropes observed uniformly coating the support particle surfaces. Figures 4 (c-e) show typical SEM micrographs of SWNT products synthesized at +150 and +200V. The diameters of SWNT ropes often exceed 50 nm, with smaller feeder bundles ranging between 10-25 nm. Cross-sectional SEM analysis of these samples (Figure 4e) reveals that a small fraction of isolated SWNTs are freestanding and oriented in the direction normal to the support particle. Within the resolution limitations of the SEM, the vertical SWNTs synthesized at

+200V bias appear to be smaller in diameter than the vertical SWNTs synthesized using negative bias (Fig. 3c-f). The hypothesized weakened ion bombardment, even relative to the neutral 0V bias case, may encourage the synthesis of CNTs of all orientations that may otherwise be etched by H^+ ions, allowing these SWNTs to escape the bundling effect encountered by SWNTs that follow the profile of the support particles.

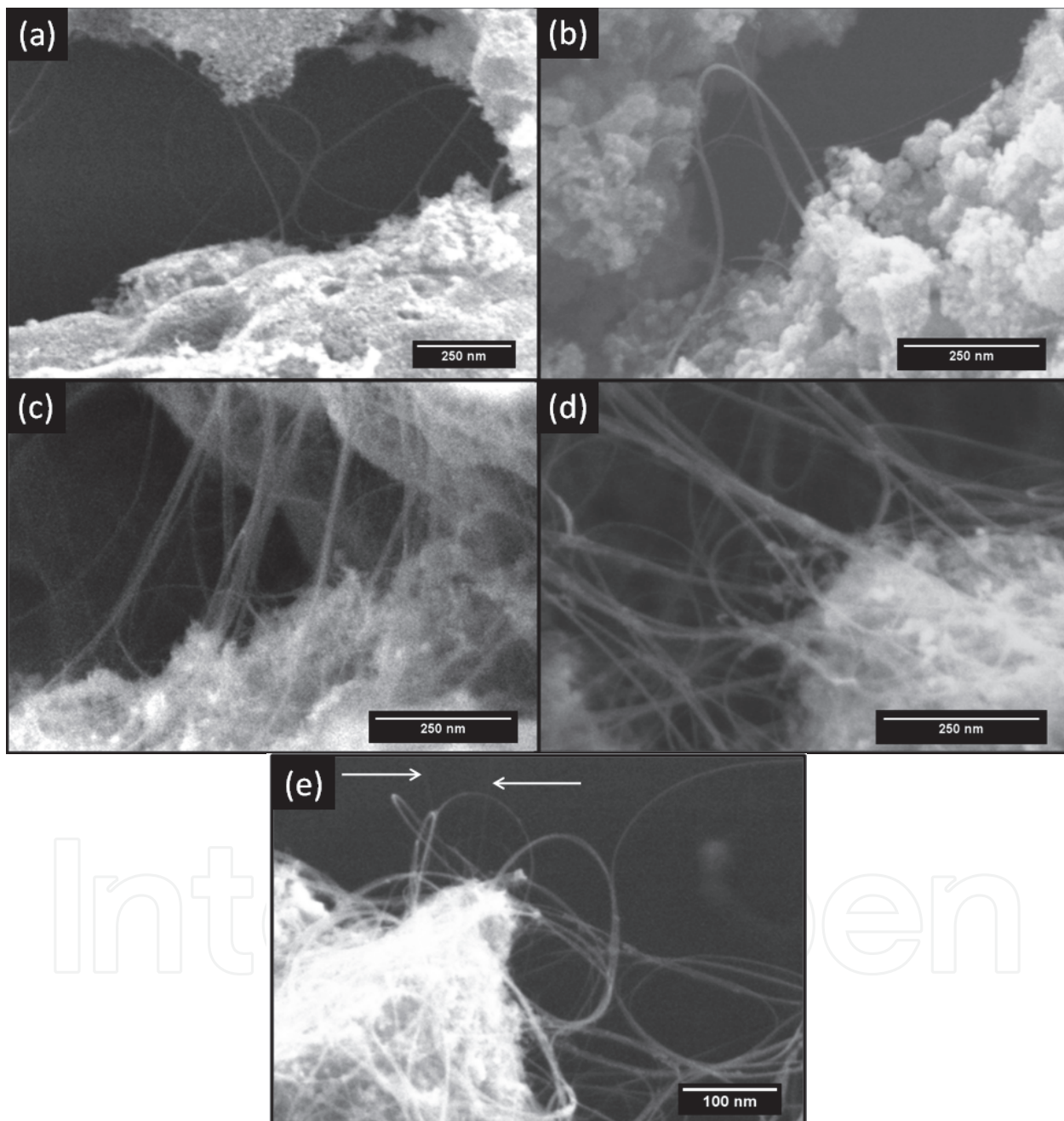


Fig. 4. SEM micrographs of SWNTs synthesized at (a) 0V, (b) +50, (c) +150, and (d-e) +200V substrate bias in MPCVD. Arrows indicate the presence of freestanding vertical SWNTs.

Raman spectroscopy yields further insights into the SWNTs produced using positive bias. While negative bias resulted in a shift in RBM peaks towards lower frequencies, the application of positive bias resulted in a shift in RBM peaks towards higher frequencies, as

shown in Fig. 5. RBMs in the range of $100 - 200 \text{ cm}^{-1}$ are present for all levels of positive bias for both 785 and 532 nm excitation wavelengths, but RBM frequencies greater than 250 cm^{-1} emerge at bias levels above +150V. Employing a 785 nm excitation wavelength, a RBM peak at 259 cm^{-1} emerges at +150V, while a peak at 261 cm^{-1} is present at +200V. Using a 532 nm excitation wavelength, a RBM peak at 251 cm^{-1} emerges at +200V. In terms of SWNT diameter distribution, the presence of these RBMs indicates the emergence of SWNTs with diameters less than 1 nm (Bachilo, et al., 2002; Rao, et al., 1997). As mentioned previously, this effect may be attributed to decreased H^+ ion bombardment which tends to preferentially etch smaller diameter SWNTs. A mixture of metallic and semiconducting chiralities exist, based on the location of RBM peaks on a Kataura plot (not shown), indicating that no chiral selectivity is attained using positive polarity bias.

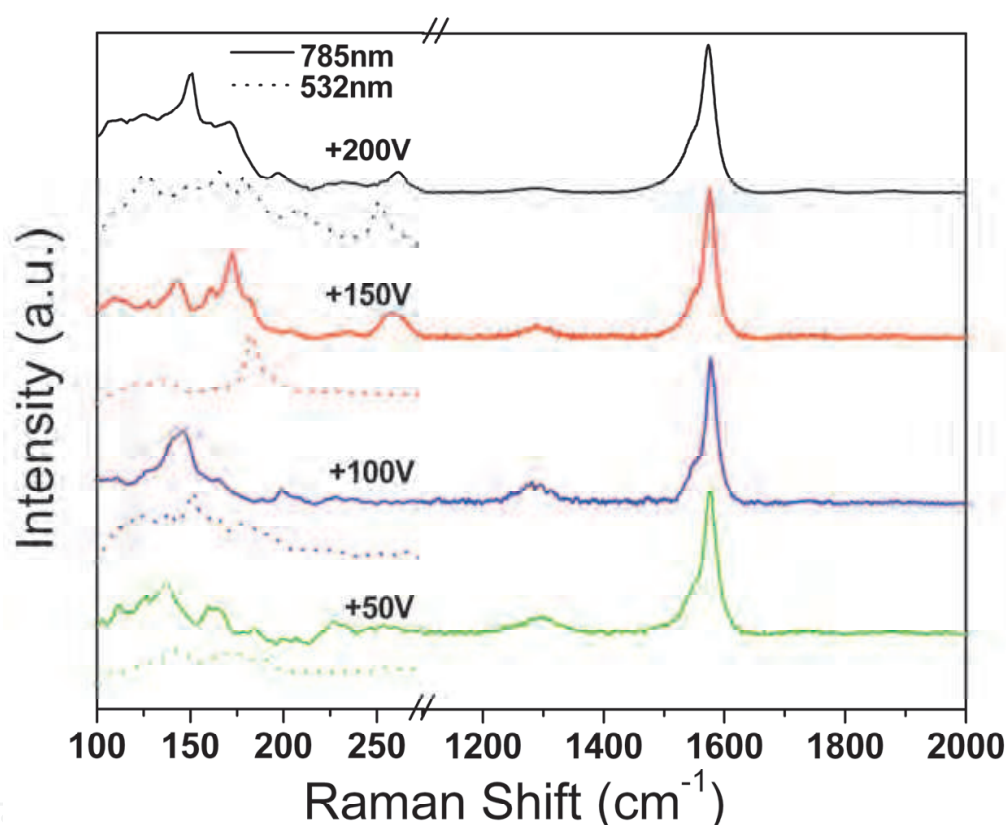


Fig. 5. Raman spectra for SWNTs synthesized using positive polarity bias in MPCVD.

Examination of the G-band further indicates a significant difference in composition of SWNTs grown under negative and positive bias. A Breit-Wigner-Fano line shape, appearing as a shoulder on the G-band at approximately 1550 cm^{-1} in Fig. 5, is indicative of metallic SWNTs (Brown, et al., 2001; Pimenta, et al., 1998) and is absent in G-bands obtained for SWNTs grown using negative bias (Fig. 3). Additionally, the G- to D-band ratios are substantially greater when utilizing positive bias. While application of negative bias attracts and accelerates H^+ ions to the growth substrate, thus damaging SWNT walls, the application of positive bias appears to adequately decrease the incoming velocity of H^+ ions to the substrate and may protect SWNTs from excessive ion bombardment. Consequently, the ratio of G- to D-band ratio for SWNTs grown using positive applied bias increased from approximately 10 for samples grown without bias to approximately 40 for those grown at

+200 V. Such a high ratio indicates a large quantity of high-quality SWNTs with little amorphous carbon.

4. Influence of DC electrical bias during MWNT synthesis

The influence of dc bias voltage during MPCVD synthesis of MWNTs from dendrimer-templated Fe_2O_3 nanoparticles will be discussed with respect to the the resulting thermal and electrical transport properties of MWNT arrays. The DC bias values examined range from -200 to +200V, in 100V increments using similar experimental techniques discussed in the previous sections. The electrical resistance of the MWNTs were measured by obtaining the slope of I-V characterization of randomly selected individual MWNTs across lithographically defined Au/Ti electrodes. Five individual MWNTs were studied for each level of dc bias. The thermal performance was assessed by utilizing the MWNT arrays as a thermal interface. Thermal resistance of the CNT interface material was determined using a photoacoustic technique (Cola, et al., 2007). The thermal resistance measurement was performed at a single interface pressure of 10 psi. Three MWNT array interfaces from each synthesis bias level were produced and characterized.

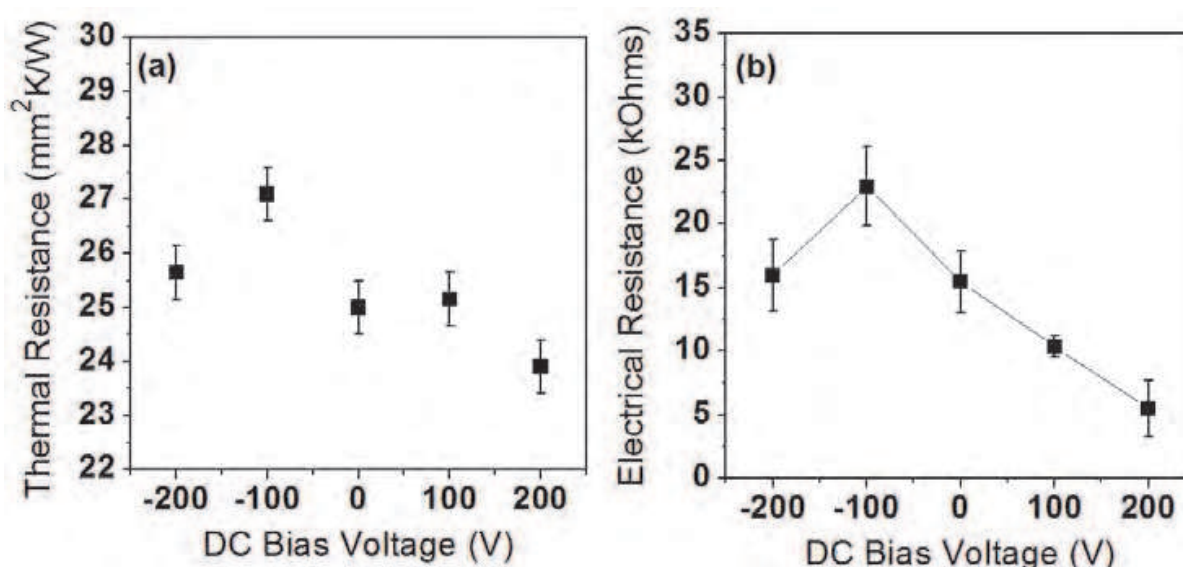


Fig. 6. Measured thermal interface resistance of MWNT arrays determined using a photoacoustic technique (a) and electrical resistance of individual MWNTs (b) as a function of dc bias voltage used during growth in the MPCVD. From (Amama, et al., 2008)

Figure 6 exhibits the electrical and thermal resistance values as a function of applied substrate bias during MPCVD synthesis. Similar trends with respect to substrate bias exist among the data sets, suggesting that similar phenomena during synthesis may be affecting both thermal and electrical transport. MWNTs grown under positive dc bias (+200V) demonstrate the lowest resistances, while the highest resistances were observed for MWNTs grown under negative dc bias voltage (-100V). The lowest thermal interface resistance (23.9 mm²/K/W) was observed for MWNT arrays grown under a dc bias voltage of +200 V while MWNT arrays grown at -100 V showed the highest thermal interface resistance (27.1 mm²/K/W). Similarly, the lowest electrical resistance (5.5 kOhms) was attained at +200V, while the greatest electrical resistance (23 kOhms). The electrical resistance data exhibits a

nearly linear decrease with respect to applied positive polarity bias, the thermal resistance observed at +100V was statistically equivalent to that observed at 0V bias. It is possible that the defect density present in MWNTs may contribute to the observed variation in electrical resistances as shown previously (Lan, et al., 2007).

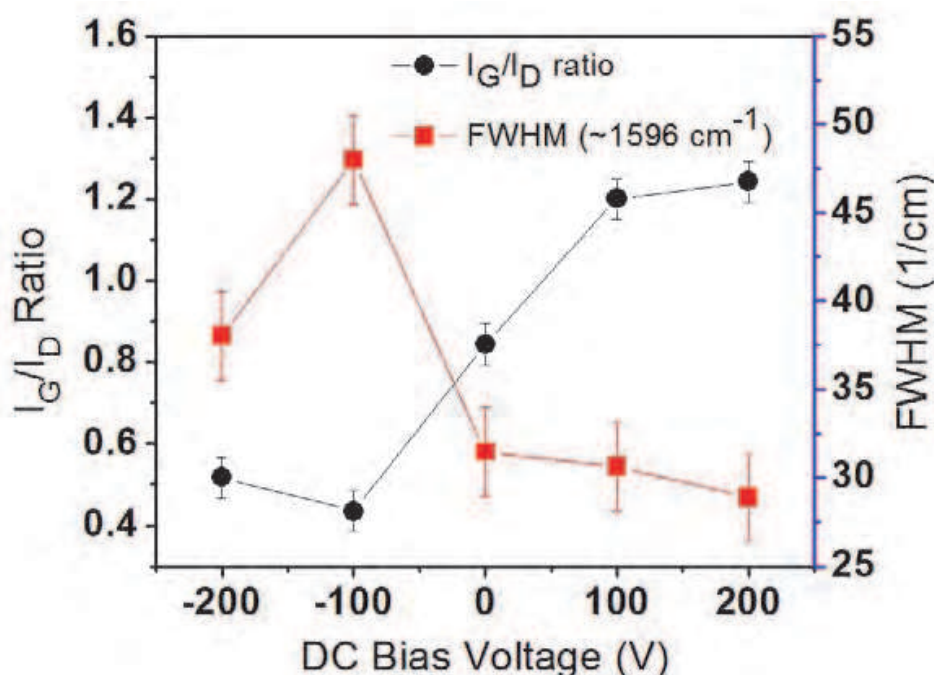


Fig. 7. Raman spectroscopy data obtained from MWNT arrays synthesized in MPCVD using dc substrate bias. I_G/I_D ratio represents the relative peak intensity ratio of the G-band to D-band, while the FWHM data is measured relative to the G-band peak. From (Amama, et al., 2008).

The thermal and relectrical resistance trends are consistent with those exhibited by the G- to D-band ratio measured via Raman spectroscopy for the MWNT samples. As seen in Fig. 7, the relative ratio of the well graphitized carbon (G-band) to disordered carbon (D-band) steadily increases as a function of positive polarity dc bias. The ratio maxima occurs at +200V, consistent with the minimal thermal and electrical resistance measurements. The minima at -100V corresponds to the maximum observed thermal and electrical resistance. The observed behavior of the I_G/I_D ratio is consistent with the full width at half maximum (FWHM) of the G-band at $\sim 1596 \text{ cm}^{-1}$. We hypothesize that negative dc bias voltage accelerated H^+ ions, introducing defects on the CNTs. This effect is most pronounced for MWNTs grown under -100 V. The relatively consistent trend between the measured resistance data and the Raman spectra data gives further evidence of this hypothesis. Biasing the substrate positively, on the other hand, reduces electric field near the substrate, reducing the bombardment of H^+ and other positively charged hydrocarbon ions generated in the plasma from the CNTs.

5. Conclusion

The parameter space for MPCVD synthesis of CNTs is vast, allowing a user a high level of fidelity with respect to control of CNT structure and morphology. The application of

substrate bias independently from plasma power and other growth parameters is a unique and robust feature of MPCVD that enables *in situ* control of CNT alignment, quality, density, and chirality and extends the potential application space for plasma-grown CNTs. We have demonstrated that both the polarity and magnitude of the applied bias dictate the resulting CNT yield. Negative polarity bias lends itself to vertical orientation and is a means to preferentially synthesize larger diameter semiconducting SWNTs. Conversely, positive polarity bias dramatically increases the SWNT quality and yield while resulting in a mix of metallic and semiconducting chiralities. To the detriment of the technique, however, the quality metrics seem exclusive to a given bias polarity. For example, the synthesis of high density, vertical freestanding SWNTs has, to date, been a challenge through variation of bias alone, and more research is required to fully optimize the capabilities of applied bias during SWNT synthesis. For MWNT synthesis, the alignment capability of negative polarity bias is well established, though the application of positive polarity bias remains relatively unexplored. We observe that positive polarity bias at levels greater than +100V during MPCVD synthesis appears to demonstrate a protective role, partially shielding CNTs from harmful ion bombardment. As a result, MWNTs exhibit enhanced thermal and electrical conductivity. The degree of freedom offered by substrate bias during MPCVD synthesis offers a tremendous extension to traditional CNT synthesis capabilities and potential inroads to myriad applications requiring strict control of SWNT or MWNT properties.

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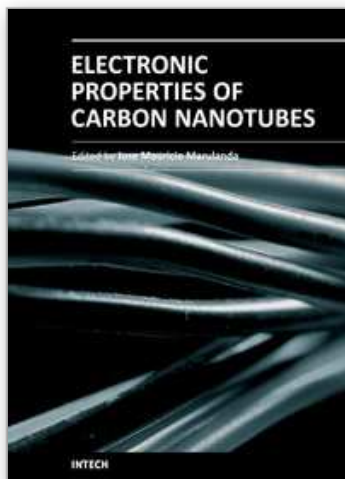
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Carbon nanotubes (CNTs), discovered in 1991, have been a subject of intensive research for a wide range of applications. These one-dimensional (1D) graphene sheets rolled into a tubular form have been the target of many researchers around the world. This book concentrates on the semiconductor physics of carbon nanotubes, it brings unique insight into the phenomena encountered in the electronic structure when operating with carbon nanotubes. This book also presents to reader useful information on the fabrication and applications of these outstanding materials. The main objective of this book is to give in-depth understanding of the physics and electronic structure of carbon nanotubes. Readers of this book should have a strong background on physical electronics and semiconductor device physics. This book first discusses fabrication techniques followed by an analysis on the physical properties of carbon nanotubes, including density of states and electronic structures. Ultimately, the book pursues a significant amount of work in the industry applications of carbon nanotubes.

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