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GaN-based Photodiodes on Silicon Substrates

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1. Introduction

GaN-based or the III-V nitrides materials are wide band gap semiconductor materials with dormant utilizations in optoelectronic as well as in electronic devices operating at high power and high temperature conditions. Silicon (Si) is one of the most common elements of the earth crust and the substrates are of very low price and are available in very large size due to its mature development and large-scale production. The thermal conductivity is higher than that of sapphire and is close to that of GaN. The crystal perfection of Si is better than that of any other substrate material and it has good thermal stability under GaN epitaxial growth condition. The growth of GaN on Si enables the possibility of integrating GaN optoelectronics devices with Si-based electronics. However, despite much effort, there is no significant breakthrough that has been obtained because of the high density of dislocations in these materials leading to a rapid degradation of all devices fabricated so far. In contrast, the performances of GaN-based devices are known to be quite acceptable despite the large density of dislocations present in the films. There are only few reports on GaN photodetectors on Si(111), including PN heterojunction photodiode, metal-semiconductor-metal (MSM) photodetectors, and Schottky photodiodes. This chapter will present the fabrication and characterization aspects of GaN-based photodiodes on Si substrates.

Photodetectors operating in the UV and with a visible blind behavior have drawn great attention in recent years, with a number of applications in both civil and military industries which include detection of missile plumes, flame sensors, engine control, solar UV monitoring, source calibration, UV astronomy, and secure space-to-space communications (Keem et. al., 2004; Hassan et. al., 2004).

Contrast to Si, gallium nitride (GaN) has wide band gap, outstanding thermal stability, small dielectric constant, chemical inertness and radiation hardness. These features make it an typical choice for use in fabricating high frequency, high power electronic devices. Furthermore, the essential preference of III-V nitrides detectors above competing devices based on semiconductors with smaller bandgaps is the long wavelength response cut-off, which is straightly associated to the bandgap of the material in the dynamic area and consequently, does not involve external filters (BenMoussa et. al., 2008). Gallium nitride (GaN) is of distinguished interest because of its great UV photoresponse, well-founded mixture techniques, and the capability of operating at high temperature and in harsh

circumstances. Dissimilar to wide band gap photodetector, the forward surface of a vacuum ultraviolet silicon photodiode is coated with filter materials to limit the sensitivity of the silicon to a much narrower band in the vacuum ultraviolet (VUV) because one significant shortcoming of Si photodiodes for certain VUV applications is the inherent broadband response extending from x-rays to the near infrared, which is undesirable.

Mainly, photodetectors can be assorted in accordance with the type of optical to electrical conversion effect. A photoelectric detector is based on the process of photon absorption by the material with the release of an electron-hole pair. In the case where the photo-generated electron is further emitted out of the material in which they become available for collection or multiplication, the device is called a photo emissive detector which is based on the external photoelectric effect. Some examples of photo emissive detectors are like photomultipliers, vacuum photodiodes, and image intensifier tubes. However, if there is no emission taking place but the photo generated electron-hole pair is available for the current circulation in the external circuit; then this is called an internal photoelectric effect detector which is the characteristic of all semiconductor photodetectors.

A p-n junction photodiode operates under reverse bias by the absorption of incident photons with energy larger than the energy gap (E_g), therefore producing electron-hole pairs in the material on both sides of the junction. The holes and electrons produced in a diffusion length from the junction get to the depletion region by diffusion. Over there, the electron-hole pairs are separated by the firm electric field and minority carriers are readily accelerated to become majority carriers on the other side, generating a photocurrent. The carrier diffusion step in this process is time consuming and should be eliminated if possible. Consequently, it is useful that the distance across of the depletion region be wide enough so that nearly all of the photons are absorbed within the depletion region rather than in the neutral p and n regions. Several types of GaN ultraviolet detectors have been reported, for example, photoconductive, p-n junction, p- π -n, Schottky barrier, MSM, and heterojunction in recent years. For metal-semiconductor contacts, the ohmic contact is needed for connections to devices because of its linear current-voltage (I-V) characteristics in both biasing directions.

Aluminium nitride, AlN (wurtzite structure type) has specific physical properties like high thermal conductivity (91-190 W/m-K), large breakdown electric field, high electrical resistivity (10^{11} - 10^{14} Ω cm), high melting point and large energy gap (Qioa et. a., 2000). Its application as a component of refractory ceramics or buffer layers for GaN epilayers grown on sapphire are widely known. In the last decade considerable attention arose in the use of thin films of AlN for various applications, from coatings for magneto-optic media, to thin films transducers and GHz-band surface acoustic wave devices. AlN is a broad band gap semiconductor; its characteristics are similar to those of an insulator with a high dielectric constant, large breakdown electric, and field good conductivity. Consequently, AlN could be the best insulator to block the leakage current. Before that, it has been demonstrated that one can decrease gate leakage current and interface state density in GaN-based effect transistor by using the AlN thin layer (Yu. et. al., 2006).

In the present work, we fabricated the p-GaN/n-Si heterojunction photodiode to observe the photoelectric effects. Besides that, the impacts of thermal annealing on GaN Schottky barrier photodiode with a thin AlN cap layer are also examined, principally because of the high thermal stability of GaN that has prompted us to bring out the best from thermal treatment to the electrical characteristics of the UV photodetectors. Thermal annealing has been effective in reducing the dark current in Schottky photodiodes which is the discussion that we are

addressing in this text. We also have investigated growth of $\text{Al}_{0.09}\text{Ga}_{0.91}\text{N}$ epilayers grown on silicon (111). Also reported is our attempt to fabricate and characterize metal-semiconductor-metal (MSM) photodiode based on these films. The effect of post annealing in oxygen ambient on the electrical properties of $\text{Ni}/\text{Al}_{0.09}\text{Ga}_{0.91}\text{N}$ is studied by I - V measurement.

2. Operation principle

Photodiodes are illuminated devices utilized to identify optical signals straight throughout electronics processes, ordinarily performance under photoconductive method. The performance contains a couple of steps: (i) charge carrier transport; (ii) carrier formation by light absorption, of which the photon need only have greater energy than that of the band gap of device materials; and (iii) the interplay of current with the external circuit to provide the output voltage signal. The supreme wavelength of the light that can generate electrons and holes is found as (Sze,1981):

$$\lambda = hc/E_g \quad (1)$$

where h is the Planck's constant; c , the velocity of light; and E_g is the bandgap of the semiconductor material. The devices function under reverse bias for the photoconductive mode for the purpose of decreasing the heterojunction capacitance and the giving up of carriers because of the larger depletion width (Luo et. al., 2006).

The I - V characteristic of a photodiode without incident light intensity is similar to a curve of a conventional diode rectification. When a reverse bias is applied, a small reverse current appears which is related to dark current. When the photodiode is irradiated with optical distribution, the I - V curve was changed by the number of photocurrent. A schematic view of the typical energy band diagram of the p -GaN/ n -Si structure and the theory of the photodiode is shown in Fig. 1. When the photon energy of the incident light is higher than the bandgap of GaN (3.4 eV), electron-holes pairs are generated inside GaN by light absorption. Simultaneously, the electron-hole pairs are separated by the electric field inside the depletion region of GaN to create photocurrent. The illumination also produces electrons and holes in the inert n -type and p -type zones, a few of which can diffuse to the depletion layer, contributing to the photocurrent (Philippe et. al., 1999). In real devices, a barrier layer at the interface of GaN might be existing due to the very thin of layer native silicon dioxide that is not drawn in Fig.1.

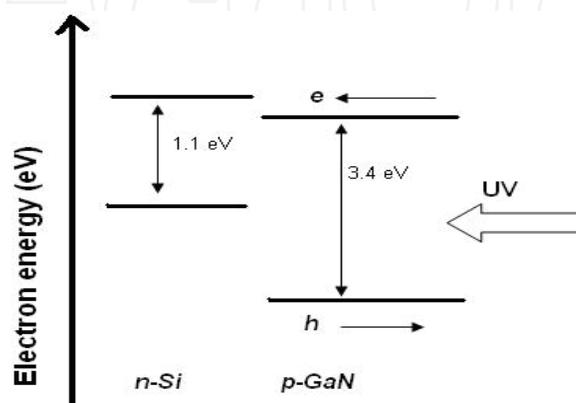


Fig. 1. Schematic energy band diagram of the p -GaN/ n -Si structure.

3. Experiment

For the first sample, a heterojunction of Mg-doped p-type crystalline gallium nitride (GaN) were grown on n-type Si(111) by radio frequency nitrogen plasma-assisted MBE (PAMBE). Sample nominally consisted of 0.10 μm AlN as a buffer layer followed by 0.23 μm Mg-doped GaN with carrier concentration of $\sim 2 \times 10^{18} \text{ cm}^{-3}$ as determined by Hall Effect measurement and the electrical conduction of all the samples was p-type.

For the wafer cleaning process prior to metallization of the contact metal, the GaN samples were dipped in a 1:20 $\text{NH}_4\text{OH}:\text{H}_2\text{O}$ solution for 15 s followed by a 10 s dip in a 1:50 $\text{HF}:\text{H}_2\text{O}$ solution. Then, it was rinsed with distilled water and blown dry with a nitrogen gas blower. Metal contact was sputtered by a sputtering system onto the backside and front-side, respectively. Here, to provide a low contact resistance of electrode, Ni/Ag (250 nm/600 nm) dots (250 μm in diameter) was deposited on the Mg-doped GaN layer at a corner, as shown in Fig. 2. In the same manner, a large area ohmic contact of Al/Ti (250 nm/600 nm) was also deposited on the backside of the Si substrate to form the second electrode of the p-n diode. A couple of contacts were patterned on the back part of an isolated piece of the sample to verify ohmic contact formation.

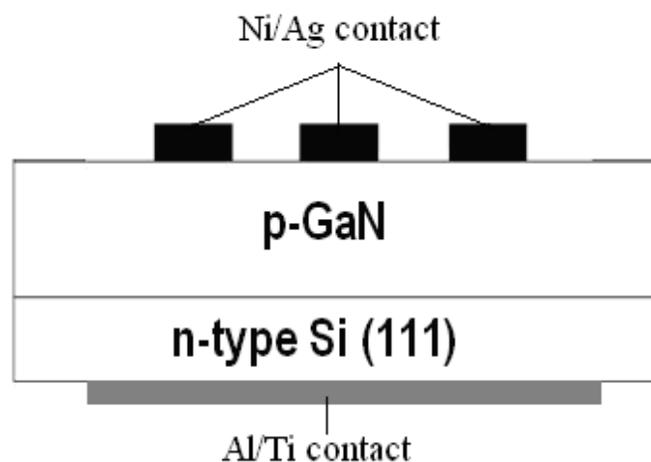


Fig. 2. Cross section view of the p-n heterojunction diode sample.

After metallization, the samples were annealed in a tube furnace at 400°C under flowing nitrogen gas environment for duration of 10 minutes. The setup of the furnace is shown in Fig. 3. Nitrogen gas was purged into the tube during annealing to displace the room ambient inside the tube. Room ambient contains nitrogen molecules that may interfere with the ohmic contact formation. The gas was purged at a mass flow rate of approximately 4 $\text{L}\cdot\text{min}^{-1}$. The electrical behaviors of the contacts were analyzed by current-voltage (I-V) measurements. For the reverse bias configuration, negative bias was applied to the p-GaN film. The measurements were performed with or without an Hg-lamp onto GaN surface. The Hg-lamp illumination was placed vertically and 3 cm away from the sample. The light beam size was large enough to barely cover the GaN surface.

For the second set sample, unintentionally doped n-type GaN (800 nm) layers were grown, followed by subsequent thin AlN cap layer (50 nm) grown on the GaN surface. GaN-based Schottky photodiodes with a thin AlN cap layer were then fabricated.

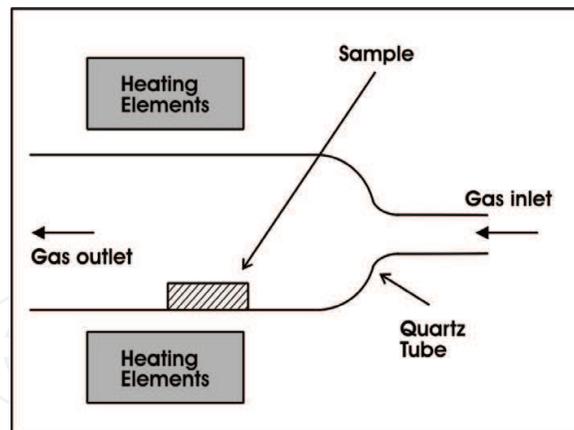


Fig. 3. Setup of tube furnace for thermal annealing.

Following surface treatment, aluminium (100 nm) metal stripes were coated on the sides of the samples as ohmic contacts by thermal evaporation technique. Contacts were then annealed in the furnace at 400 °C for 10 min under flowing nitrogen gas environment. *I-V* measurement was performed to confirm the ohmic behavior of the contacts. Consequent to the ohmic contacts deposition, Ni (150 nm) was deposited as the Schottky contact metal for all of the fabricated devices. The metal mask applied for Schottky contacts fabrication encompasses a pattern of dots with diameter of 200 μm . Both contacts were formed on the uppermost surface of the sample.

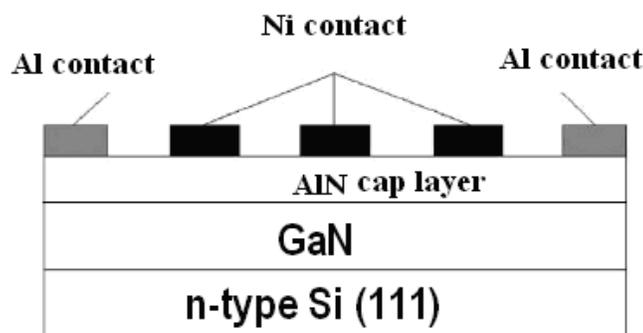


Fig. 4. Cross section view of ohmic and Schottky contacts of a typical GaN-based Schottky photodiodes using a thin AlN cap layer.

The fabricated photodiodes were then annealed in nitrogen ambient at temperatures from 500 - 700 °C in a typical tube furnace. For sample (photodiode) annealed at 500 °C, the annealing duration was 15 min, while the 600 °C sample was annealed for 5 min and 2 min for the 700 °C sample (Lee et. al., 2005). Photocurrent and dark current of the fabricated photodiodes were then measured by Keithley high-voltage-source-measure-unit model 237-semiconductor parameter analyzer. The ideality factors (n) and effective Schottky barrier heights (SBHs) are deduced from the *I-V* measurement.

For the third set sample, sample nominally consisted of 0.20 μm low temperature nucleation layer AlN followed by 0.23 μm of unintentionally doped n-type AlGaN (Fig. 5). The Al content of the sample was measured to be 9 % from high resolution XRD (PANalytical X'pert MRD) simulation. Following surface treatment, Ni Schottky contacts were deposited by thermal evaporation using a metal mask in patterning of the contact structure.

MSM photodiode is a planar device consisting of two fork-shaped interdigitated contacts on the semiconductor surface. These contacts perform as back to back Schottky contacts with finger spacing of 400 μm , fingers width of 230 μm , and the length of each electrode was about 3.3 mm (Lee et. al., 2005). It consists of 4 fingers at each electrode as shown in Fig. 6. The fabricated photodiodes were then annealed at temperatures from 400 - 700 $^{\circ}\text{C}$ in a conventional tube furnace in flowing oxygen environment. The annealing duration for the samples annealed at temperatures from 400 $^{\circ}\text{C}$ to 500 $^{\circ}\text{C}$ was 15 minutes, while the 600 $^{\circ}\text{C}$ samples were annealed for 5 minutes, and 2 minutes for the 700 $^{\circ}\text{C}$ samples. The effective Schottky barrier heights (SBHs) are deduced from I - V measurement assuming the results can be described by the standard thermionic emission equation.

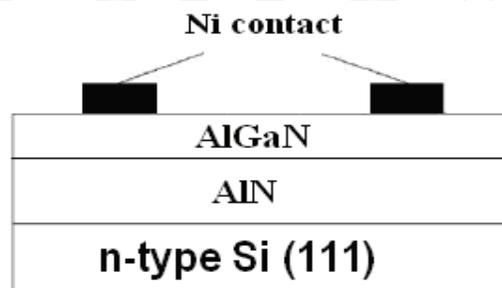


Fig. 5. Cross section view of the Ni Schottky contacts on AlGaN sample.

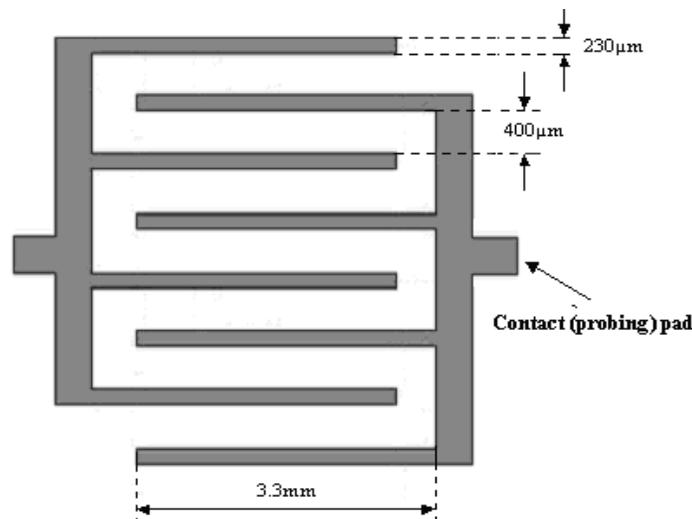


Fig. 6. Schematic diagram of the metal-semiconductor-metal (MSM) structure (Lee et. al., 2005)

4. Results and discussions

Studies of the I - V characteristics of the fabricated p-GaN/n-Si structure revealed a good p-n heterojunction as shown in Fig. 7. The I - V relationship for a heterojunction is given by (BenMoussa et. al., 2008):

$$I = I_s \left[\exp\left(\frac{qV}{K_B T}\right) - 1 \right] \quad (2)$$

where I is the current; V is the applied voltage across the heterojunction from p-side to n-side; K_B is the Boltzmann constant; I_s is the saturation current; and T is the absolute temperature. The typical I - V rectifying characteristic in the dark (without light illumination) at room temperature is observed in Fig. 7 to evaluate the turn on voltage. A pronounced rectifying diode-like behavior with turn on voltage of 0.8 V can be seen. The forward current was as high as 0.012 A at 8 V forward bias.

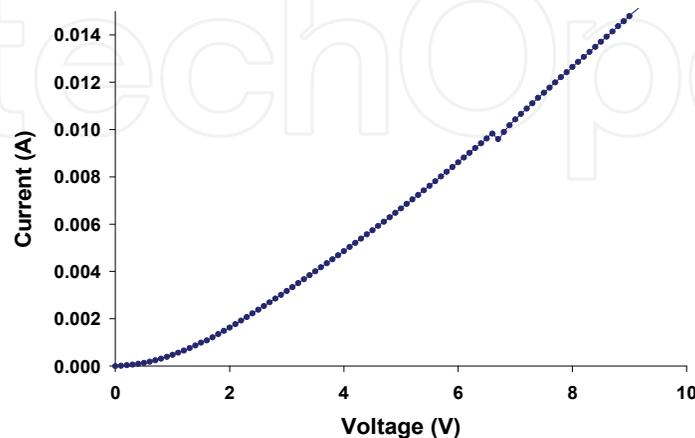


Fig. 7. I - V characteristics of the heterojunction in dark at room temperature (Chuah et. al., 2008a)

At the wavelength of 365 nm, the UV photo measurement was performed using an Hg-lamp. The mechanism of the crystalline-based p-GaN/n-Si diode is described as follows. Since the photon energy is larger than the bandgap of GaN, UV light source was absorbed by the GaN thin film producing electron-hole pairs, which were isolated by the electric field inside the GaN thin film contributing to the raise of the external current. Before p-GaN an n-Si are in contact, p-GaN has high concentration of holes and few electrons compared to that of n-Si. On contacting, diffusion of carriers will take place at concentration gradient near to the junction. The holes will diffuse from p-GaN to n-Si and negative space charge will remain behind in p-GaN near the junction.

From the p-GaN side, holes diffuse into the n-Si side, and electrons diffuse from n-Si into p-GaN. Although the electrons and holes can move to the opposite side of the junction, the donors and acceptors are fixed in space. When electrons diffuse from the n-Si to the p-GaN side, they drop behind uncompensated donor ions in the n-Si material, and holes leaving the p-GaN region produce uncompensated acceptors. The diffusion of electrons and holes from the vicinity of the junction establishes a region of positive space charge near the n side of the junction and negative charge near the p side. An electric field is constructed at the interface. Photocurrents are directly gained (An Doan et. al., 2002).

At zero voltage bias, the energy gaps (E_g) for GaN and Si are 3.4 eV (Miskys et. al., 2003) and 1.12 eV, respectively. The electron affinity for GaN is approximated as 4.10 eV (Yeh et. al., 2007). However the electron affinity for Si is 3.95 eV (Edgar et. al., 1999). The energy barrier ΔE_c for electrons is $\Delta E_c = \chi_{\text{GaN}} - \chi_{\text{Si}} = (4.10 - 3.95) \text{ eV} = 0.15 \text{ eV}$, while the energy barrier ΔE_v for holes is $\Delta E_v = E_{g,\text{GaN}} + \Delta E_c - E_{g,\text{Si}} = (3.4 + 0.15 - 1.12) \text{ eV} = 2.43 \text{ eV}$. Consequently, the energy barrier for holes (ΔE_v) is 16 times more than the barrier for electrons (ΔE_c) (He et. al., 2007).

When a reverse bias is used, holes confront ΔE_v , producing a low current. In adverse, when a forward bias is applied, the electron only require to surmount a much smaller potential

barrier (ΔE_c), hence presenting rise to the rectifying effect. These arguments are for the ideal case, and direct measurements are required to ascertain the exact band structure of the heterojunction (He et. al., 2007).

In addition, crystalline GaN photodiodes were designed to be responsive to optical source. The UV photocurrent measurement was performed using an Hg-lamp. Because the photon energy is higher than the bandgap of GaN (~ 364 nm wavelength), UV light was absorbed by the crystalline GaN creating electron-hole pairs, which were further separated by the electric field inside GaN contributing to the increase of the external current. Since the bandgap of p-GaN layer is larger than that of the substrate, band-to-band photo excitation cannot take place in this layer (Luo et. al., 2006; Soci et. al., 2010).

In this work, reverse bias was used for the photodiode operation to maximize the depletion width, reduce the transit time, and the carrier loss because of recombination process in diffusion area. Photodiodes typically have high resistance under reverse bias. This resistance is reduced when light of an appropriate frequency illuminates on the junction to create additional free electron-hole pairs. Consequently, a reverse biased diode may be applied as a detector by observing the current running through it (He et. al., 2007).

The I-V characteristics are shown in Fig. 8, where clear rectifying behavior can be seen both in the dark and under UV illumination conditions. Clear response to UV illumination can be seen from Fig. 7 in the reverse-biased condition because of the photogeneration of additional electron-hole pairs. The magnitude of photocurrent improves with the increase of applied reverse bias because of enhanced carrier collection. The photocurrent is 2.0 mA with -3 V reverse bias. On the other hand, the dark leakage current for the GaN photodiodes is weak (0.8 mA in the -3 V reverse bias). This behavior shows that GaN photodiodes can sensitively distinguish UV light to produce the measurable photocurrent response. The capability of the device is influenced by a few factors, including the presence of a thin native silicon dioxide barrier layer at the interface of the heterojunction that will decrease the photocurrent; and light loss by reflection and absorption of the top metal contact (Fahrettin et. al., 2008).

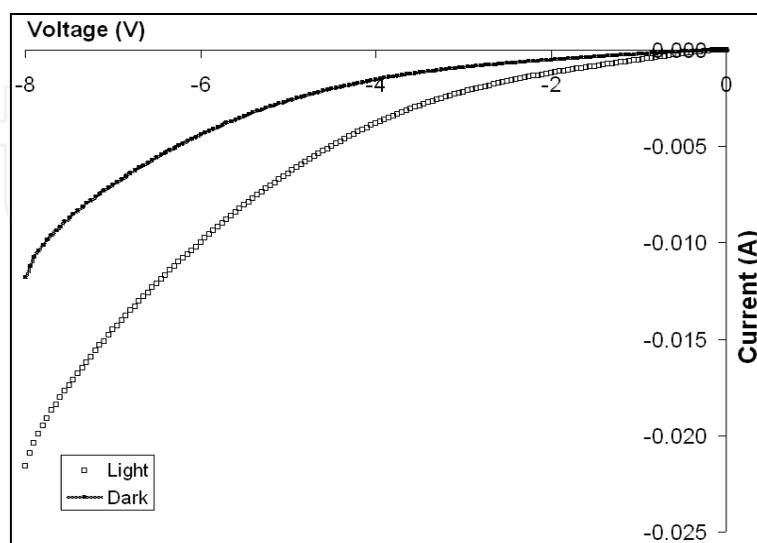


Fig. 8. The I-V characteristics of p-GaN/n-Si photodiodes. Photo-currents under illumination (light) are shown from the diodes (Chuah et. al., 2008a)

For the second set sample, Fig. 9 show the I - V characteristics of the Ni/AlN/GaN/AlN Schottky barrier photodiodes annealed at different temperatures. The higher turn-on voltage observed for all measurements under dark currents could be attributed to the highly resistive nature of the AlN cap layer. The dark current was 2.37×10^{-3} A under 10 V applied bias for as deposited Ni/AlN/GaN/AlN Schottky diode. On the other hand, for Schottky diodes annealed at 500 °C, 600 °C, and 700 °C, the dark currents were 3.25×10^{-4} , 4.97×10^{-5} , and 5.05×10^{-5} A, respectively, under 10 V applied bias.

By referring to Table 1, annealed samples exhibited more significant changes to the dark current characteristics compared to the as deposited Ni/AlN/GaN/AlN Schottky diode. This finding was further confirmed by measuring the contrast ratio of photo-current and dark current at 10 V. The contrast ratio for annealed sample (at 700 °C) and as deposited Schottky diode were found to be 25 and 2, respectively. High temperature (600 °C and 700 °C) annealing treatment increased the barrier height as well as reduced the dark current as compared to the low temperature annealing (500 °C).

The I - V characteristics of the Schottky diode, Φ_B and n , were determined assuming thermionic emission (Monroy et. al., 1998):

$$I = I_0 [\exp \{qV/nkT\} - 1] \quad (3)$$

$$I_0 = A^*AT^2 \exp\{-q \Phi_B/(kT)\} \quad (4)$$

where I_0 is the saturation current, n is the ideality factor, k is the Boltzmann's constant, T is the absolute temperature, Φ_B is the barrier height, A is area of the Schottky contact and A^* is the effective Richardson coefficient. The theoretical value of A^* can be calculated using

$$A^* = 4\pi m^* q k^2 / h^3 \quad (5)$$

where h is Planck's constant and $m^* = 0.27m_0$ is the effective electron mass for AlN (Chen et. al., 2004). The value of A^* is determined to be $32.4 \text{ Acm}^{-2}\text{K}^{-2}$.

Using equation (3) and the theoretical value of A^* , under dark condition, the Schottky barrier height derived by the I - V method is 0.48 eV for as deposited Ni/AlN/GaN/AlN Schottky diode. On the other hand, the effective barrier heights of 0.52 eV, 0.55 eV, and 0.57 eV were obtained for Schottky diodes annealed at 500 °C, 600 °C, and 700 °C, respectively.

In our earlier study, a thin AlN cap layer was incorporated in GaN Schottky diode to improve the effective Schottky barrier height and decreases the dark current. A barrier height of 0.52 eV for typical Ni/GaN Schottky diode was increased to the effective barrier height of 0.63 eV for Ni/GaN Schottky diode with thin AlN cap layer. The low dark currents in GaN Schottky barriers with the thin AlN cap layer might be attributed to the AlN cap layer, resulting in a higher potential barrier, compared to standard sample. The deleterious effect of the interface states near the metal/semiconductor interface may be reduced for the sample owing to the insertion of the AlN cap layer. It was established that AlN cap layers would completely suppress the dark current of the GaN Schottky diodes and resulted in improved device characteristics (Chuah et. al., 2008b).

In the last few years, various kinds of nitride-based photodetectors have been reported (Vivian et. al., 2010). Nevertheless, an opposing condition usually occurs, when large differences in the lattice constant and thermal expansion coefficient of GaN and silicon inevitably lead to high dislocation density in the GaN epitaxial layer. Such a result contributes to a large dark current and smaller photocurrent/dark current ratio for nitride-

based Schottky barrier photodetectors. Despite, we attained low dark current from the GaN metal-semiconductor-metal photodiode with a 50-nm thick low temperature GaN barrier enhancement layer (Chuah et. al., 2008c).

With a panorama to decreasing the dark current of Schottky barrier photodetectors, metal-insulator-semiconductor (MIS) structures are for the present deeply investigated. In past reports, a number of gate dielectrics for example SiO_2 have been used in MIS structures (Casey et. al., 1996; Gaffey et. al., 2001). Nevertheless, these insulators were all ex-situ deposited and the contamination might occur at the insulator/semiconductor interface. Consequently, we deposited an in-situ AlN cap layer by MBE in our study. Nevertheless, not much report on GaN-based Schottky barrier photodiodes capped with an AlN layer can be detected in the literature, to our knowledge.

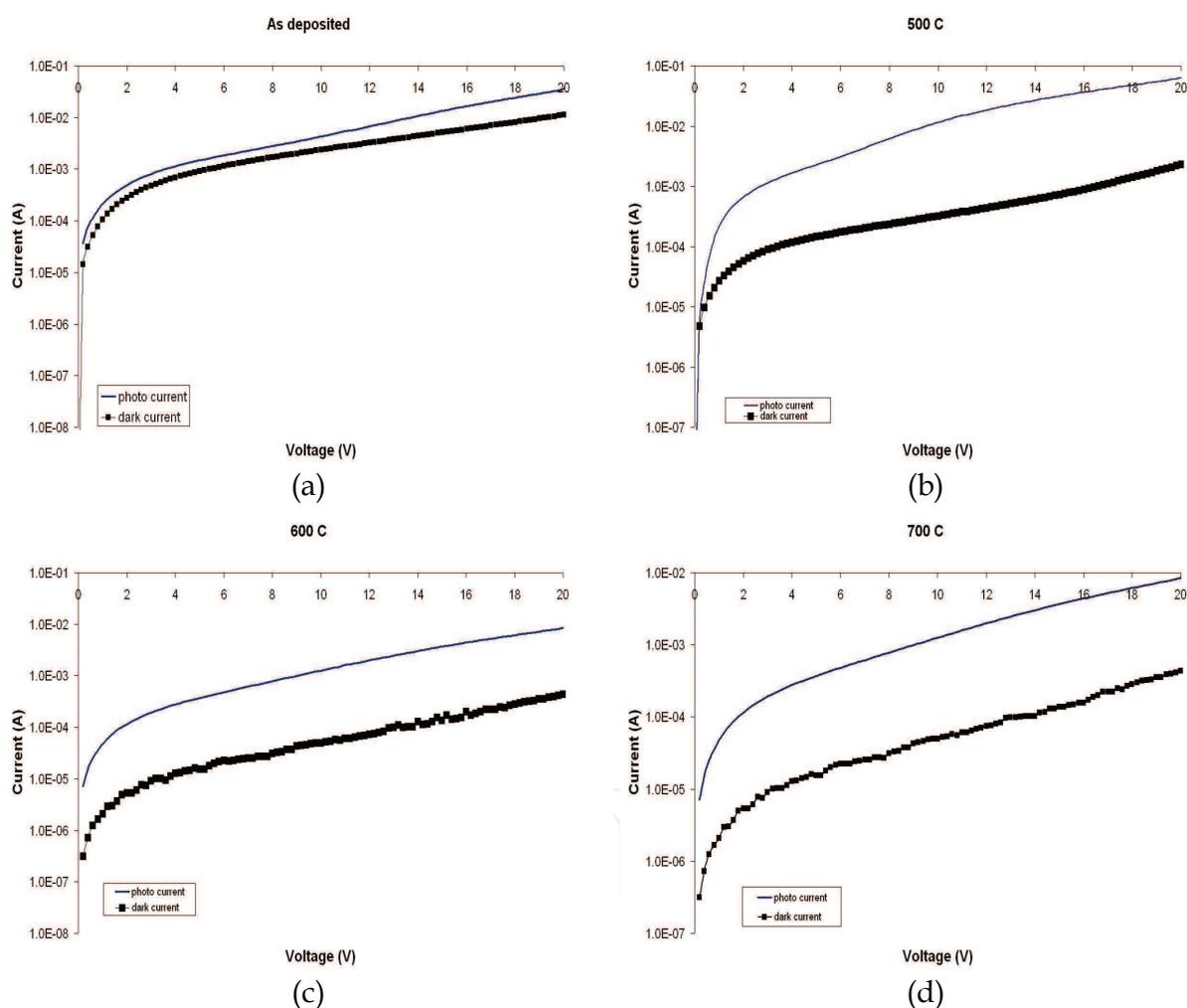


Fig. 9. I-V characteristics of the fabricated photodiodes annealed at different temperatures (a) as deposited, (b) 500°C, (c) 600°C, and (d) 700°C (Chuah et. al., 2009a)

According to Reddy (Reddy et. al., 2007), they hypothesized that the temperature dependence of Schottky barrier height maybe due to the changes of surface morphology of Pt films on the n-GaN and variation of nonstoichiometric defects at the interface. Khanna et al. (Khanna et. al., 2006) investigated temperature dependence of W_2B_5 -based rectifying contacts to n-GaN and showed that the Schottky barrier height (0.65 eV) increased with annealing temperature up to

200 °C. Wang et al. (Wang et. al., 2003) investigated that the variation of barrier height upon annealing could be ascribed to variation of surface morphology and inequality of nonstoichiometric defects at the interface vicinity. In the current work, the increase of the Schottky barrier height could be because of the decrease of nonstoichiometric defects in the metallurgical interface (Wang et. al., 2003). The region involving the defects could be decreased because of the interdiffusion of Ni and Al. Hence, the consumption of the defect region is followed by a raise in the value of the Schottky barrier height extracted from the I-V characteristics for the samples annealed from 500 °C to 700 °C.

Temperature (°C)	Samples (photodiodes)	Ideality factor, n	Barrier height, Φ_b (eV)	Current at 10V (A)
As deposited	dark	1.08	0.48	2.37×10^{-3}
	illumination	1.10	0.46	4.29×10^{-3}
500°C	dark	1.07	0.52	3.25×10^{-4}
	illumination	1.08	0.50	1.15×10^{-2}
600°C	dark	1.03	0.55	4.97×10^{-5}
	illumination	1.04	0.53	1.20×10^{-3}
700°C	dark	1.05	0.57	5.05×10^{-5}
	illumination	1.04	0.53	2.00×10^{-3}

Table 1. Summary of the dark and photo-current (I-V) characteristics of the samples annealed at different temperatures.

As generally known, chemical reaction between the metal and the semiconductor interfaces can play an important role in the electrical properties of metal/semiconductor contact. The change in the barrier height of Ni/AlN/GaN/AlN Schottky contact with annealing temperature may also be ascribed to the interfacial reaction occurring between metals and AlN and their alloys which extend to AlN films. These interfacial layers might have dissimilar work functions than the Ni/AlN contact layers, which is responsible for the observed increase of barrier height (Lee et. al., 2006).

From the value of the ideality factors that we have obtained, the deviation of the diodes' ideality factor from unity may be due to tunneling effects, image force, and edge leakage. Furthermore, a unique value of ideality factor could be associated with a given set of diode conditions like temperature, bias, and doping. From the literature, the Schottky barrier heights varied widely from 0.50 to 1.15 eV for Ni, depending on the measuring methods, doping concentration and quality of the GaN (Schmitz et. al., 1996; Kalinina et. al., 1997). This also suggests that the metal work function should not be the only factor affecting the Schottky characteristics of the diodes (Kalinina et. al., 1997).

For the third set sample, a Schottky contact behaviour could be more closely described by the equation which takes into account the barrier height lowering because of electric field, tunneling effects, the presence of an interfacial layer, and carrier recombination in the space charge region of the metal-semiconductor contact as given by (Rideout et. al., 1975; Abdulmecit et. al., 1992)

$$I = I_0 \exp\left(\frac{eV}{nkT}\right) \left[1 - \exp\left(\frac{-eV}{kT}\right)\right] \quad (6)$$

where I is the current, I_0 is the saturation current, V is the bias voltage, and n is the ideality factor. The expression for the saturation current, I_0 is

$$I_0 = AA^*T^2 \exp\left(\frac{-q\Phi_b}{kT}\right) \quad (7)$$

where A is the Schottky contact area, and the theoretical value (Hacke et. al., 1993) of A^* for GaN is $\sim 26.4 \text{ Acm}^{-2}\text{K}^{-2}$. Here, the theoretical value of A^* for $\text{Al}_{0.09}\text{Ga}_{0.91}\text{N}$ can be estimated by using the relation $A^* = 4\pi q m^* k^2 / h^3$ where h is Planck's constant and m^* is the effective electron mass for AlGaN. As there has not been any references or reported experimental results of the effective mass of electron in AlGaN, therefore m^* for $\text{Al}_x\text{Ga}_{1-x}\text{N}$ with different x is estimated by a linear interpolation from the theoretical value of $m^* = 0.35 m_0$ for AlN (Kim et. al., 1997) and $m^* = 0.22 m_0$ for GaN (Piotr et. al., 1997). Here, m_0 is the free electron mass. From the equation as being mentioned above, A^* for $\text{Al}_{0.09}\text{Ga}_{0.91}\text{N}$ is estimated to be $\sim 27.9 \text{ Acm}^{-2}\text{K}^{-2}$. However, it should be noted that a large variation in A^* does not have a significant influence on the Φ_B value that is to be determined (Liu et. al., 1998). Equation (6) can be written in the form of

$$\frac{I \exp(eV/kT)}{\exp(eV/kT) - 1} = I_0 \exp(eV/nkT) \quad (8)$$

At $T \leq 370 \text{ K}$ and when $V \leq -0.5 \text{ V}$, equation (8) can be simplified to (Averine et. al., 2000)

$$I \exp\left(\frac{eV}{kT}\right) = I_0 \exp\left(\frac{eV}{nkT}\right) \quad (9)$$

$$\ln \left[I \exp\left(\frac{eV}{kT}\right) \right] = \ln I_0 + \frac{eV}{nkT} \quad (10)$$

The plot of $\ln [I \exp(eV/kT)]$ vs V should give a straight line with the slope = e/nkT and y-intercept at $\ln I_0$. Using equation (7), the ideality factor (n) and Schottky barrier height (SBH) were determined by the I - V method.

Figures 10 show the I - V characteristics of Ni/AlGaIn photodetectors under dark and illumination conditions. Table 2 summarizes dark and illumination current measured at 10V, as well as the ideality factor and SBH of the samples determined from the I - V measurements. By referring to Table 2, it is found that high temperature annealing in oxygen ambient (600 °C and 700 °C) resulted in more significant changes to the dark current characteristics compared to the lower temperature annealing treatment. High temperature annealing treatment increased the barrier height as well as reduced the dark current level. For lower annealing temperature (400 °C and 500 °C), the barrier height for annealed samples (photodiodes) increased. As shown in Fig. 10, it can be seen that dark currents became significantly smaller after annealing. Such a reduction can again be attributed to the formation of NiO layers since NiO is p-type in nature (Averine et. al., 2000). As a result, we achieved larger Schottky barrier heights from the thermally annealed samples.

In the literatures, it can be seen that the bi-layer of Ni/Au film annealed in oxygen by using photo-CVD would transform the metallic Ni into NiO along with Au grains and amorphous Ni-Ga-O phases (Chen et. al., 1999). Hence, the Ni/Au semitransparent contact

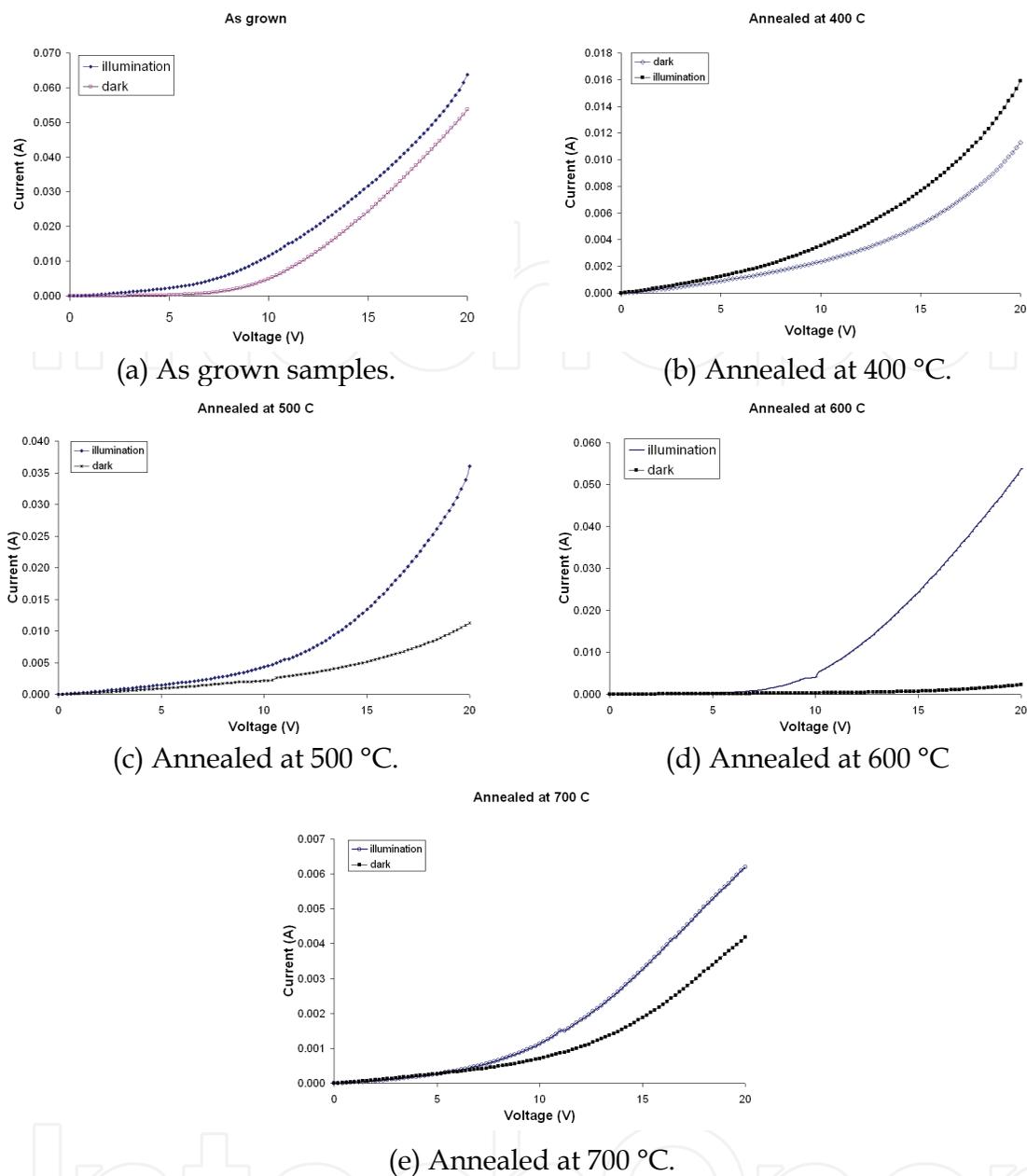


Fig. 10. Current-voltage (I-V) characteristics of Ni/AlGaIn photodetectors under dark and illumination conditions (Chuah et. al., 2009b)

can be used to form good Schottky contacts with superior Schottky barrier height. In this study, the Ni/Al_{0.09}Ga_{0.91}N samples were annealed at a range of temperatures in flowing oxygen by using a tube furnace. NiO, which is reported to behave as a p-type semiconductor with nickel vacancies and/or oxygen interstitials, has also been reported as a kind of passivation layer on the AlGaIn interface (Kim et. al., 2006). As the annealed temperature increases, the transmittance increases. The increase of transmittance also indicates the formation of NiO. We are able to achieve excellent photo-current to dark-current contrast ratio from the AlGaIn MSM photodetectors, which can be attributed to the formation of the transparent NiO in the Ni contacts. It was found that dark current of the detector became significantly smaller after annealing.

With a 10V applied bias, it was found that we can achieve a photocurrent to dark current contrast ratio of 12 from the photodetectors with 600 °C annealed Ni contacts. This could be attributed to the more transparent nature of NiO formed after annealing so that more photons can be absorbed by the underneath AlGaIn epitaxial layer. Such an effect should compensate the effect of larger Schottky barrier height. As the annealing temperature was increased to 700 °C, it was found that we can achieve a photocurrent to dark current contrast ratio of 2, which may be due to the formation of a rough Ni layer surface. Such a rough surface could increase light scattering losses and thus degraded the film transparency.

On the other hand, for Ni/AlGaIn Schottky contact, the effect of "surface patches" which originates from surface defects such as dislocations and micropipes with residual oxides may not be neglectable (Sawada et. al., 2000). Postannealing is an effective method to repair the surface patches. However, considering that the annealing temperature is relatively low and the time is short, the surface patches effect could not be regarded as the main factor to the enhancement of the barrier height. When the sample was under illumination condition, the change of current was significant for the annealed sample as compared to the as grown sample. Both as grown and annealed samples originated from the same AlGaIn wafer, and both of the devices were fabricated using same processing tools and under identical parameters, however, significant difference in dark current was observed. High dark current in the as grown sample could be attributed to the low barrier height of the metal contact with the AlGaIn thin film.

Since the Ni film became more transparent after annealing in O₂, more photons should be absorbed by the underneath semiconductor. The oxidation layer near the interface provides an increasing energy barrier for carrier injection into AlGaIn. As the oxidation temperature increases (annealed at 800 °C), the rectifying characteristics is deteriorated by excess oxidation. There may be some interfacial reaction between oxygen and AlGaIn. The real interaction is still under investigation.

The effect of the reduction of leakage current or dark current, barrier height or ideality factor enhancement in the MSM photodiodes after heat treatment can mainly be explained by the chemical reaction of the metal with the interfacial oxide layer of the semiconductor

Temperature (°C)	Samples (MSM photodiodes)	Ideality factor, n	Barrier height, Φ_b (eV)	Current at 10V (A)
As grown	illumination	1.23	0.51	1.15×10^{-2}
	dark	1.20	0.53	4.94×10^{-3}
400°C	illumination	1.15	0.55	3.54×10^{-3}
	dark	1.10	0.57	1.37×10^{-3}
500°C	illumination	1.09	0.60	6.25×10^{-3}
	dark	1.08	0.61	2.15×10^{-3}
600°C	illumination	1.05	0.66	4.04×10^{-3}
	dark	1.04	0.69	2.20×10^{-4}
700°C	illumination	1.02	0.67	1.01×10^{-3}
	dark	1.01	0.70	7.09×10^{-4}

Table 2. Summary of the dark current characteristics of the samples annealed in oxygen ambient at different temperatures.

(Abdulmecit et. al., 1992) or macroscopic interaction between the metal and the semiconductor (Liu et. al, 1998). Experimental results also revealed that reactive contact metals decrease the interfacial layer and react with the semiconductor during annealing and hence it can cause a low or high barrier height for the MSM photodiodes.

It can be viewed that dark current increased gradually with the applied reverse bias and does not exhibit any impact of saturation. The absence of saturation for a Schottky contact under reverse bias could be clarified in terms of barrier height which is dependent on the electric field strength in the barrier as a result of the existence of an interfacial layer between the metal and the semiconductor. Besides that, the lack of saturation might be caused by image force lowering of the barrier height and because of the generation of electron-hole pairs in the depletion region as generation current is more pronounced at low temperatures than high temperatures because it has lower activation energy than the thermionic emission component (Rhoderick et. al., 1988).

$\text{Al}_{0.09}\text{Ga}_{0.91}\text{N}$ is known to experience from a high volume of defect densities due to the reasons like the difference between the thermal expansion coefficient and large lattice mismatch between the silicon substrates and the $\text{Al}_{0.09}\text{Ga}_{0.91}\text{N}$ material. During the reverse bias of the Schottky contact, the effect of the applied bias can be much greater when compared to the forward bias. Thus, tunneling current can also be observed in a semiconductor with a lower doping concentration during reverse biases which mean that the tunnel current cannot be left out when investigating Schottky contacts under reverse bias voltage.

Consequently, apart from the usual thermionic emission current that survive within a metal-semiconductor contact, the high amount of dark current observed can be attributed to the tunneling of carriers across the barrier. This effect can be assisted by traps generated by defects (threading dislocations that reach the surface) and the interfacial layer to produce trap assisted tunnel currents. Deep level bulk states that are within a tunneling distance of the interface can be another kind of traps. Therefore, thermionic emission is the primary transport mechanism in these MSM photodiodes.

Another contribution to the high dark current in $\text{Al}_{0.09}\text{Ga}_{0.91}\text{N}$ is dominated by other current mechanism like leakage current generated by a high defect density present in $\text{Al}_{0.09}\text{Ga}_{0.91}\text{N}$ as well as the traps assisted tunnel current. The high defect density in $\text{Al}_{0.09}\text{Ga}_{0.91}\text{N}$ justifies the presence of inhomogeneity at Schottky contacts, which causes a local enhancement of the tunnel current. The rotation of the layer during the growth and the different positions of the III-element sources could produce alloy inhomogeneities, as already reported.

From the results, the value of the ideality factor is quite near to unity, thus indicating the high quality Schottky contact under investigation and the absence of a thick interfacial layer. However, the existence of a thin interfacial layer cannot be ruled out unless the semiconductor is cleaved in an ultra high vacuum (UHV) condition (Guo et. al., 1996). The variation in barrier height values may be due to formation of alloy, different thickness of interfacial layer present on the film, variation in surface roughness of samples used, defects present in films, presence of several transport mechanism, and the variations in the local stoichiometry (Pearson et. al., 1999).

5. Conclusions

In the previous years, GaN growth on Si has expanded fastly and a few solutions are emerging to adjust the stress because of the thermal mismatch. In recent years, many groups

have obtained thick, crack-free GaN on Si (111) by molecular beam epitaxy (MBE) or metalorganic chemical vapor deposition (MOCVD). There are also some reports about devices fabricated on GaN/Si (111) such as light emitting diodes (LEDs) and high electron mobility transistors (HEMTs). PN heterojunction photodiode, metal-semiconductor-metal (MSM) photodetectors, and Schottky photodiodes are now well developed and expected to be commercialized soon. However, reports of photodetectors based on crack-free GaN on Si (111) are limited. In the near future, Si may be the predominant substrate material for GaN growth because of its low price and availability in large diameters. The further interest is that the Si substrate can be etched away for laser or LEDs and for development of GaN substrates.

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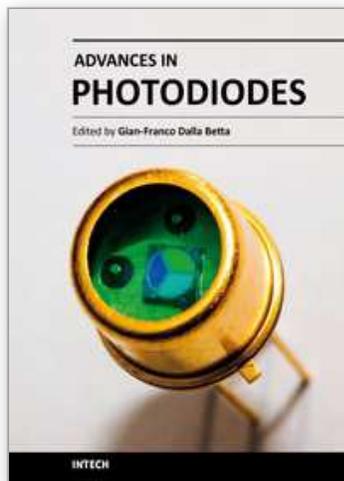
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