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Carbon Nanotubes Interconnects for Nanoelectronics Circuits

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1. Introduction

Future nanoelectronics will be enabled only by providing the effective capability of connecting the nanometric devices to the circuit boards, therefore a major challenge is the design and fabrication of the *nano-interconnects*. For nanotechnology applications the limits imposed by physics, materials, assembly and design could not be overcome by simply scaling the conventional metal/dielectric systems: innovation in new materials, new technology and new system integration techniques is required.

Carbon nanotubes (CNTs) are recently discovered carbon structures (Iijima, 1991), which have been soon considered as *emerging research materials* (ITRS, 2007) for nanoelectronics applications (Fig.1), because of their unique properties (Avouris et al., 2003; Saito et al., 2004; Anantram & Léonard, 2006). CNT interconnects are expected to meet many of the requirements for technologies below the 22nm node (ITRS, 2007) in terms of mechanical strength, thermal conductivity and electrical performances.

This Chapter is devoted to CNT interconnects, proposed for wiring and for packaging nanotechnology ICs, with the aim of presenting the state-of-the-art of electrodynamics and circuit modelling of CNTs and to provide performance comparisons between conventional and CNT interconnects. Some case-studies of practical application are carried-out, referring to real-world wiring and packaging problems for future nanoelectronics.
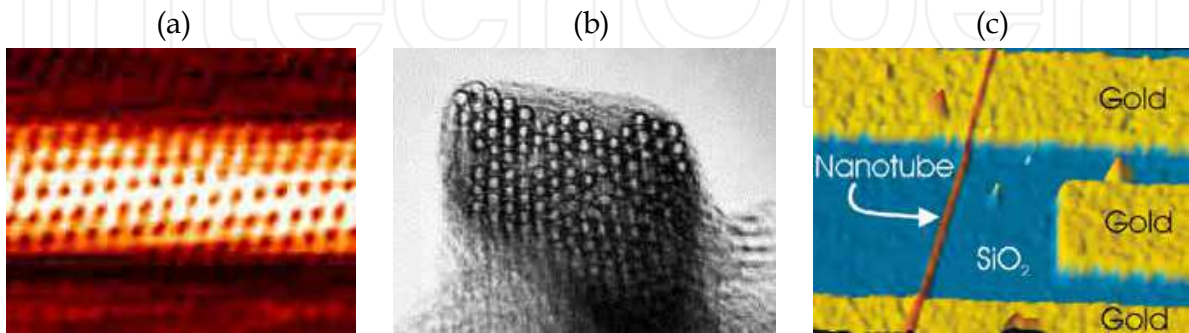
(a)   (b)   (c)

Fig. 1. Some real-world nanotubes: (a) AFM image of chiral tube of 1.3 nm diameter (*Technical University, Delft*); (b) TEM image of a crystalline nanotube bundle (*Rice University*); (c) a single nanotube as interconnect between gold electrodes (*IBM*).

Due to the potential applications of CNTs in nanoelectronics, many efforts have been made in literature to derive models able to describe the electrical propagation. Examples of phenomenological models are found in (Burke, 2002; Burke, 2003), based on the Luttinger liquid theory. Semi-classical approaches describing the CNTs as electron waveguides may be found in (Wesström, 1996; Sarto, 2009), and in (Salahuddin, 2005), where a general model for a quantum wire is derived from the transport theory based on the Boltzmann equation. In this Chapter we adopt the semi-classical model first proposed in (Miano & Villone, 2006), based on a fluid description of the motion of the CNT free electrons. This model has been used to derive transmission line equivalent circuits for metallic CNT interconnects in (Maffucci et al, 2008a; Maffucci et al., 2009), and has been recently extended to CNTs of arbitrary chirality (Forestiere et al., 2009a and 2009b). Note that the same model may be extended to take into account also the effect of CNT curvature (Forestiere et al., 2009b)

Using any of the quoted models, the comparative analysis between CNT and conventional Cu interconnects leads to a quite general result: nano-interconnects made by good quality CNT bundles outperform Cu ones at intermediate and global level, whereas at local level the behaviour is comparable (Li et al., 2009; Maffucci et al., 2008b; Naeemi, 2008). Therefore, the focus is now on the availability of a low-cost fabrication process providing CNT bundles with low contact resistance, good densities, good direction control and compatibility with CMOS technology. Satisfactorily results have been achieved for vertical CNT bundles (ITRS, 2007; Li et al., 2009; Liu, 2009). The same good results, however, have not yet been obtained in the fabrication of CNT interconnects parallel to the substrate, which remains a challenging task. On the other hand, many of the problems related to the integration between the CNT technology and the CMOS one are still open. However, the gap between theoretical predictions and practical applications is reducing faster and faster, and many examples have been recently given of the first real-world applications of CNT interconnects. In the nanopackaging area CNT bumps for flip-chip interconnects have been fabricated, replacing the conventional solder bumps, Fig.2a (Soga, 2008). Recently, a 1.02 GHz oscillator ring circuit using CNT wiring for a conventional chip has been presented, see Fig.2b (Close et al., 2008). These are only few of the many examples of successful integration between CNTs and ICs, testimonials of this technological trend.

<center>(a)   (b)</center>
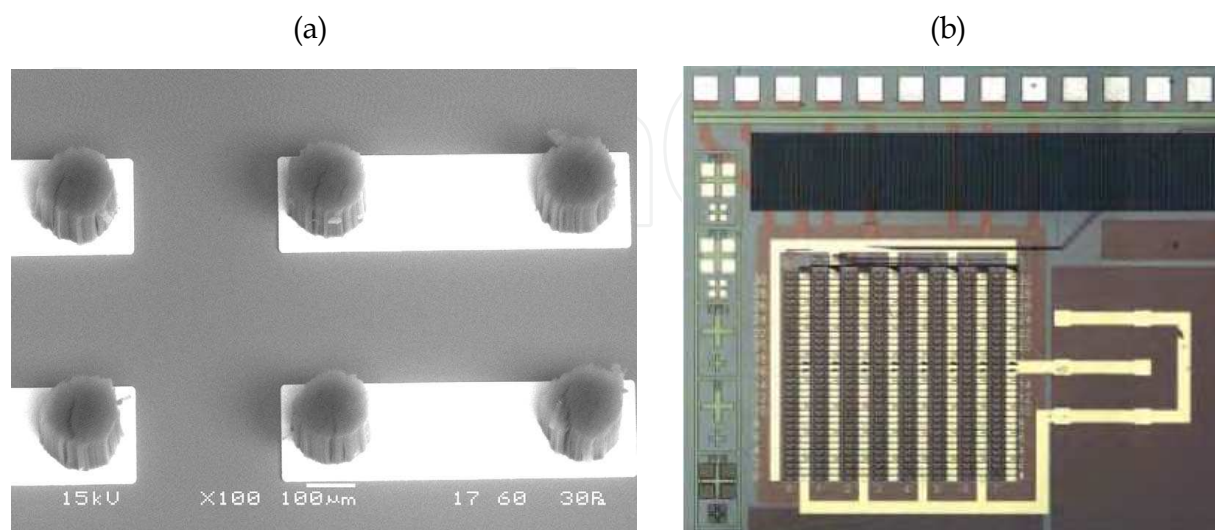

Fig. 2. (a) Vertical CNT bundles as flip-chip bumps (*Fujitsu Labs.*); (b) carbon nanotubes wiring in an oscillator circuit (*Stanford University & Toshiba*).

2. Electromagnetic propagation models for CNTs

A carbon nanotube is realized by rolling-up a sheet of a mono-atomic layer of graphite (*graphene*), see Fig.3a. The rolled graphene may become either metallic or semiconducting, depending on its *chirality*, i.e. the way in which it is rolled up. A single-wall carbon nanotube (SWCNT) is made by a single sheet of graphene (Fig.3b), whereas a multi-wall carbon nanotube (MWCNT) is made by nested sheets (Fig.3c). In the graphene the conduction phenomenon is related to the motion of the so-called π-electron, i.e. the one out of the four valence electrons which is free to move between the positive ion lattice. Both semiclassical as well as quantum-mechanical approaches have been used to describe the CNT electron dynamics.

We study the electrodynamics of a CNT shell, which may either describe an isolated SWCNT or a shell of a MWCNT, in which the direct coupling of electronic states of adjacent shells may be disregarded. The electrical signal propagation along the CNT shell is analyzed assuming a low frequency regime, where only intraband transitions of π-electrons with unchanged transverse quasimomentum occur. This allows using the semi-classical transport theory expounded, for instance, in (Slepyan et al., 1999; Maksimenko et al., 2000; Miano & Villone, 2006: Maffucci et al., 2008a; Forestiere et al., 2009a and 2009b). Using this approach, the longitudinal dynamic conductivity of a CNT shell of arbitrary chirality may be obtained, expressed in terms of the number of effective conducting channels. This leads to the constitutive equation of this innovative material, which could be coupled to Maxwell equations in order to derive electromagnetic and/or circuital models, following the stream of what done in (Miano & Villone, 2006: Maffucci et al., 2008a; Forestiere et al., 2009).

2.1 Constitutive relation of a CNT shell

The chirality of a CNT is described by two integers n and m, which are the coefficients of he basis vectors $\mathbf{a}_1$ and $\mathbf{a}_2$ depicted in Fig.3a. The CNT is metallic if $|n-m|=3q$, where q=0,1,2… A zig-zag CNT is obtained for $n=0$ or $m=0$, whereas an armchair CNT is given by $n=m$, therefore, armchair CNTs are always metallic, whereas zig-zag ones are metallic only if $m=3q$, with q=0,1,2… For $m \to \infty$ or $n \to \infty$ any CNT tends to the graphene sheet. The CNT diameter D is given by:

$$D = 0.0782\sqrt{n^2 + nm + m^2} \quad [nm] .$$ (1)

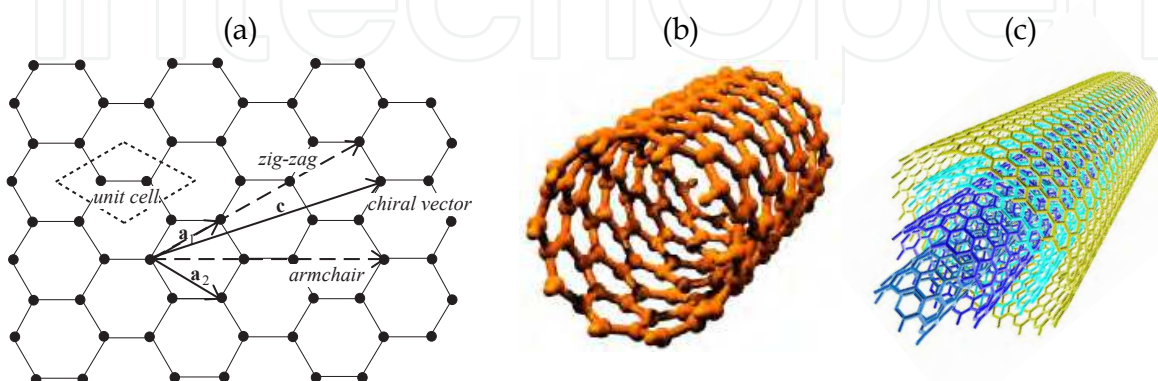(a)   (b)   (c)

Fig. 3. (a) the graphene lattice; (b) single-wall CNT; (c) multi-wall CNT.

For each segment of the first Brillouin zone of the nanotube we introduce a distribution function for the π electrons in the valence and conduction bands:

$$f_\mu^{(\pm)} = f_\mu^{(\pm)}(z, k, t), \quad \mu = 0, 1, \ldots N, \quad -\frac{\pi}{T} < k \le \frac{\pi}{T}, \tag{2}$$

where k is the longitudinal wavenumber, t is the time, N is the number of graphene exagons in the primitive cell of the CNT, and T is the translational length. By assuming that the electric field is transversally uniform, $f_\mu^{(\pm)}$ satisfy the quasi-classic Boltzmann equation

$$\frac{\partial f_\mu^{(\pm)}}{\partial t} + v_\mu^{(\pm)} \frac{\partial f_\mu^{(\pm)}}{\partial z} + \frac{2\pi e}{h} E_z \frac{\partial f_\mu^{(\pm)}}{\partial k} = -\nu(f_\mu^{(\pm)} - f_{0,\mu}^{(\pm)}), \tag{3}$$

where e is the electron charge, h is the Planck constant, $E_z(z, t)$ is the component of the electric field tangent to the CNT surface, ν is the frequency of collision of the electrons with the ion lattice, $v_\mu^{(\pm)}$ are the longitudinal velocities of the electrons in the conduction/valence subbands and $f_{0,\mu}^{(\pm)}$ are the distribution functions at equilibrium. The latter are given by:

$$f_{0,\mu}^{(\pm)}(k) = \frac{1}{\pi^2 D} F\left[E_\mu^{(\pm)}(k)\right], \qquad F[E] = \frac{1}{e^{E/k_B T_0} + 1}, \tag{4}$$

being $E_\mu^{(\pm)}$ the energy of the generic subband, $F[E]$ the Dirac-Fermi function, k_B the Boltzmann constant and T_0 the CNT absolute temperature. Let us assume time-harmonic electric field and surface current density:

$$E_z(z, t) = \text{Re}\left\{\hat{E}_z e^{i(\omega t - \beta z)}\right\}, \qquad J_z(z, t) = \text{Re}\left\{\hat{J}_z e^{i(\omega t - \beta z)}\right\}. \tag{5}$$

For small perturbations of functions (2) around the equilibrium values (4) we get

$$\hat{E}_z = \hat{\sigma}_{zz}(\beta, \omega)\hat{J}_z, \tag{6}$$

where the spectral domain conductivity $\hat{\sigma}_{zz}(\beta, \omega)$ is given by

$$\hat{\sigma}_{zz}(\beta, \omega) = \frac{2ie^2}{\pi^2 D} \sum_{\mu=0}^{N-1} \int_0^{\pi/T} \left(\frac{1}{\omega - v_\mu^{(+)}\beta - i\nu} + \frac{1}{\omega + v_\mu^{(+)}\beta - i\nu}\right) v_\mu^{(+)2} \frac{dF}{dE_\mu^{(+)}} dk. \tag{7}$$

For all the subbands that give a meaningful contribution to the conductivity we may assume $v_\mu^{(\pm)} \cong v_F$ in (7), being v_F the Fermi velocity. This leads to

$$\hat{\sigma}_{zz}(\beta, \omega) = \frac{4e^2 v_F}{\pi h D} \frac{1}{i\omega + \nu} \frac{1}{1 + v_F^2 \beta^2 /(i\omega + \nu)^2} M(n, m), \tag{8}$$

where M may be regarded as the *equivalent number of conducting channels*:

$$M(n,m) = \frac{h}{\pi v_F} \sum_{\mu=0}^{N-1} \int_0^{\pi/T} v_F^2 \left(\frac{dF}{dE_\mu^+} \right) dk. \tag{9}$$

The *constitutive relation* of the CNT in the frequency domain comes from (6), (8) and (9):

$$(1 + i\omega/v)J_z + \frac{v_F^2}{v(1 + v/i\omega)} \frac{\partial \rho_s}{\partial z} = \sigma_c E_z, \tag{10}$$

where ρ_s is the surface charge density and σ_c is the static limit for the axial conductivity:

$$\sigma_c = \frac{4e^2 v_F}{v\pi hD} M(n,m). \tag{11}$$

In the limit $\omega/v \gg 1$, (11) reduces to the simple fluid model in (Miano & Villone, 2006; Maffucci et al., 2008a) and for $\omega/v \ll 1$ the electrons behave as a cold fluid.

2.2 Approximation of the equivalent number of conducting channels

The number of conducting channels M is a function of the parameters affecting the number of subbands around the CNT Fermi level: CNT chirality, diameter and temperature. Figure 4a shows M for increasing CNT diameter, evaluated at $T = 273$ K. For small diameters it is $M = 2$ for metallic CNTs and $M = 0$ for semiconducting ones, i.e. only metallic CNTs contribute the conduction. For larger diameters the channels of a metallic CNT increase and those associated to semiconducting ones are no longer negligible.

Let us now compute the axial conductivity for the two cases: Fig.4b shows these values, normalized to the axial conductivity for a graphene sheet, given by (Slepyan, 1999):

$$\sigma_\infty = \frac{8\pi \ln 2}{h^2} \frac{e^2 k_B T}{v + i\omega}. \tag{12}$$

Figure 4b agrees with the condition that for $D \to \infty$ a CNT tends to the graphene.

To obtain a simple analysis tool for studying MWCNTs or bundles of CNTs, it is useful to fit the behavior of M through a simple approximated function of the diameter D and temperature T. A piecewise linear function able to fit the asymptotic behaviors for $D \to \infty$ and $D \to 0$ may be given by the following expression (the parameters are given in Table 1):

$$M \approx \begin{cases} M_0 & \text{for } D < d_0/T \\ a_1 DT + a_2 & \text{for } D \geq d_0/T \end{cases}. \tag{13}$$

Figure 5 shows the fitting results computed at $T = 273$ K and $T = 373$ K.

This result is consistent with similar approximations proposed in literature (Naeemi, 2008), where however M is sligthly overestimated, due to the approximations used in evaluating the number of subbands around the Fermi level.

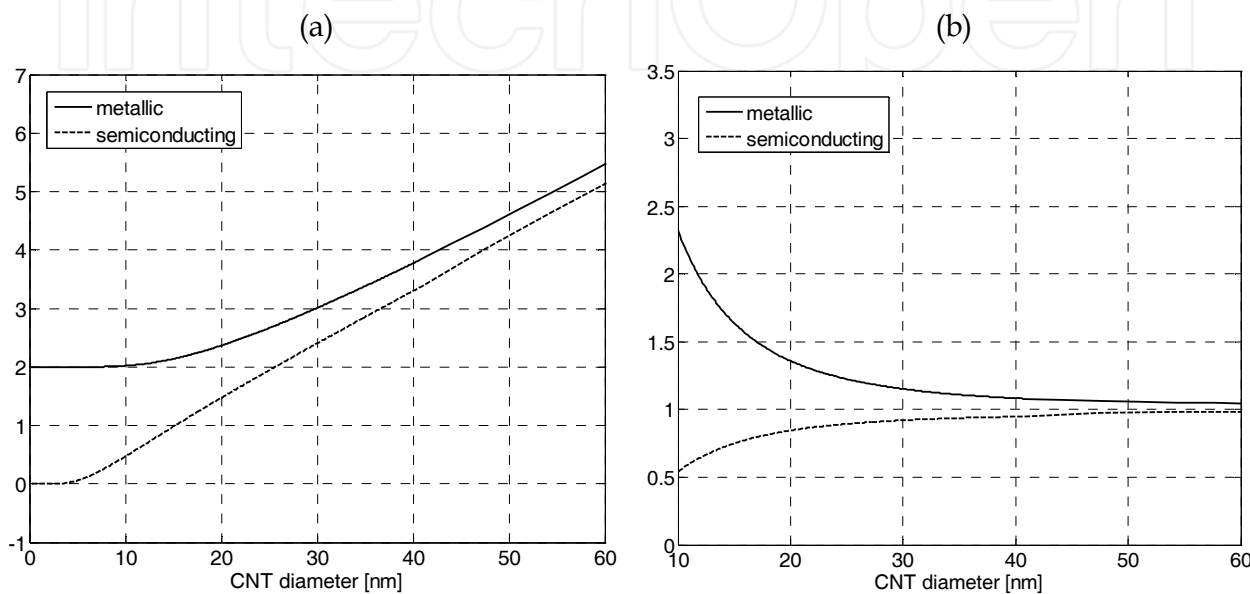|	*Metallic*	*Semiconducting*
M_0 [adim.]	2	0
a_1 [1/nmK]	$3.3 \cdot 10^{-4}$	$3.3 \cdot 10^{-4}$
a_2 [adim.]	0.15	-0.20
d_0 [nm·K]	$5.6 \cdot 10^3$	$0.6 \cdot 10^3$

Table 1. Parameters for the fitting formulas (13).

Fig. 4. (a) Number of effective conducting channels versus CNT diameter, at T=273 K; (b) axial conductivity normalized to the graphene value.
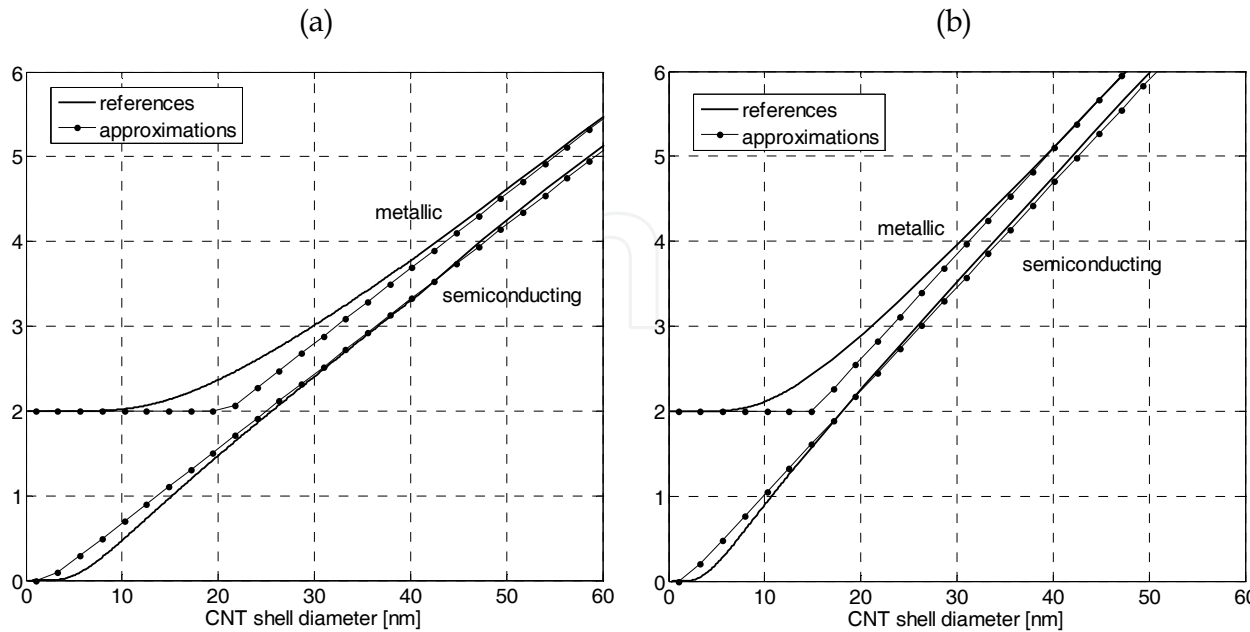

Fig. 5. Piecewise linear fitting (13) for the number of effective conducting channels evaluated at: (a) $T = 273$ K ; and (b) $T = 373$ K .

3. Circuit models for CNT bundle interconnects

3.1 Circuit model for a single CNT above a ground plane

A circuit model for CNT interconnects may be derived in the framework of the transmission line (TL) theory. The problem may be formulated in frequency domain by coupling Maxwell equations to the CNT constitutive relation (10). Then, assuming the propagation to be of quasi-TEM type, a distributed RLC circuit may be derived.

Let us first consider the simple case of a CNT shell of diameter D, located at a distance t above a PEC ground, Fig. 6a. As shown in paragraph 4.3, the distribution of surface currents and charges along the contour of a CNT section may be assumed to be uniform, since the proximity effect is negligible, hence the current and charge at any position along the CNT axis are given by:

$$Q(\omega, z) = \pi D \rho_s(\omega, z), \quad I(\omega, z) = \pi D J_z(\omega, z). \tag{14}$$

Using (14) in (10) we get:

$$(\nu + i\omega)I(\omega, z) + \frac{v_F^2}{(1 + \nu/i\omega)} \frac{\partial Q(\omega, z)}{\partial z} = 2v_F \frac{M}{R_0} E_z(\omega, z), \tag{15}$$

being $R_0 = h/2e^2 = 12.9\,\mathrm{k\Omega}$ the so-called *quantum resistance*. The longitudinal component of the electric field E_z may be derived from the scalar electric and vector magnetic potentials:

$$E_z(\omega, z) = -\frac{\partial V(\omega, z)}{\partial z} - i\omega A_z, \tag{16}$$

which may be related to charge and current through the classical expressions:

$$V(\omega, z) = \frac{Q(\omega, z)}{C_e}, \quad A_z(\omega, z) = L_m I(\omega, z), \tag{17}$$

being C_e, L_m the per-unit-length (pul) electrostatic capacitance and magnetic inductance.
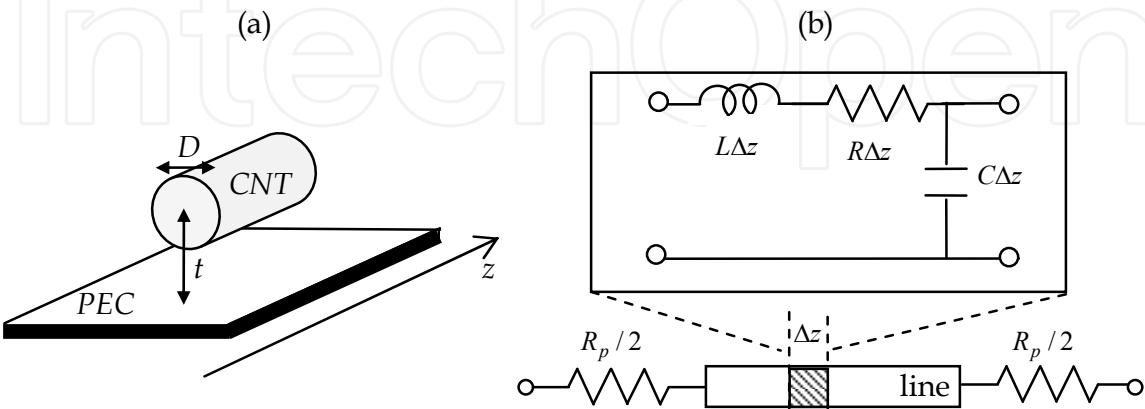

Fig. 6. (a) A CNT shell above a PEC; (b) equivalent circuit: elementary cell (inset) and lumped contact resistances.

Combining (15)-(17) we get the first Telegraphers' equation:

$$-\frac{dV(\omega, z)}{dz} = (R(\omega) + i\omega L(\omega))I(\omega, z), \tag{18}$$

with the pul parameters R and L defined as:

$$L = (L_m + L_k)/a_C, \quad R = \nu L_k / a_C, \quad C = C_e, \tag{19}$$

where the parameter α_C is given by:

$$a_C = 1 + \frac{C_e}{C_q} \frac{1}{1 + \nu/i\omega}. \tag{20}$$

In (19) we have introduced the *p.u.l. kinetic inductance* L_k and *p.u.l. quantum capacitance* C_q:

$$L_k = \frac{R_0}{2v_F M} = \frac{h}{4e^2 v_F M}, \quad C_q = \frac{1}{L_k v_F^2} = \frac{2M}{R_0 v_F} = \frac{4e^2 M}{h v_F}. \tag{21}$$

The second Telegraphers' equation is obtained by imposing the charge conservation law:

$$-\frac{dI(\omega, z)}{dz} = i\omega C V(\omega, z), \tag{22}$$

with the pul capacitance C defined as $C = C_e$.

Equations (18) and (22) describe a lossy TL where the quantum effects are combined to the classical electrical and magnetic ones in the definition of the *p.u.l.* parameters R, L, and C. Assuming a frequency operating condition such as $\nu/\omega \ll 1$, the parameters R and L do not depend on frequency, since (20) becomes:

$$a_C \approx 1 + C_e / C_q. \tag{23}$$

Typical orders of magnitude for the collision frequency ν are $10^{11} \div 10^{12}$ Hz, hence the above condition is satisfied for frequencies up to the order of hundrends of GHz. Assuming this limit, the propagation along a CNT is then described by a simple lossy TL with constant parameters R, L, and C. The elementary cell is depicted in the inset of Fig.6b. The circuit model obtained here is a generalization of other models proposed in literature: for instance in (Burke, 2003; Salahuddin, 2005; Maffucci et al., 2008a) TL models for metallic SWCNTs are obtained. The elementary cell is however slightly different from those used in (Burke, 2003; Salahuddin, 2005), where the voltage variable is derived from the *electrochemical* potential rather than from the electrostatic one. A detailed discussion may be found in (Maffucci et al., 2009). The TL models in the quoted references describe metallic CNTs, where the model presented here is more general, since it deals with both metallic and semiconducting CNTs of any diameter. For instance the values of L_k and C_q used in literature may be obtained using *M*=2 in (21), i.e. assuming metallic CNT with small radius:

$$L_{k0} = \frac{h}{8e^2 v_F} = \frac{R_0}{4v_F} \, , \quad C_{q0} = \frac{8e^2}{hv_F} = \frac{4}{R_0 v_F} \, . \tag{24}$$

Assuming in (24) $c_s \approx v_F \approx 8 \cdot 10^5 \, m/s$, we get $L_{k0} \approx 4 \, \text{nH}/\mu\text{m}$ and $C_{q0} \approx 400 \, \text{aF}/\mu\text{m}$. However the actual values deviate from this bulk values as an effect of the increase of the number of channels with the CNT diameter, as shown in Figure 7.

Let us now investigate the influence of the quantistic capacitance and kinetic inductance on the pul parameters (19). Assuming a homogeneous dielectric the classical parameters are:

$$L_m = \frac{\mu}{2\pi} \cosh^{-1}\left(\frac{2t}{D}\right), \quad C_e = 2\pi\varepsilon \Big/ \cosh^{-1}\left(\frac{2t}{D}\right). \tag{25}$$

Let us consider the case of a metallic SWCNT with $D = 4 \, \text{nm}$, $\varepsilon_r = 2.2$ and $\mu = \mu_0$. For such a condition $L_k \approx L_{k0}$ and $C_q \approx C_{q0}$. Table 2 shows the ratio between classical and quantistic parameters for different aspect ratios t/D. The kinetic inductance dominates the magnetic one, being 3-4 order of
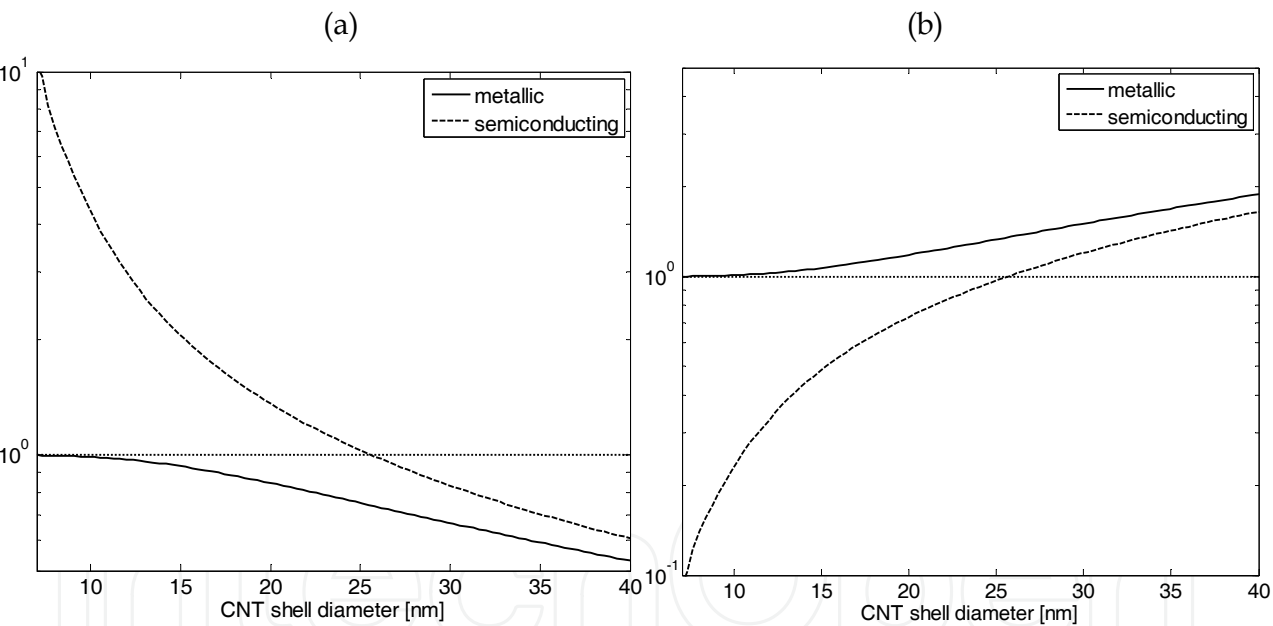

Fig. 7. (a) kinetic inductance, and (b) quantum capacitance for a CNT shell vs diameter, normalized to the bulk values (24).

t/D	L_k/L_m	C_e/C_q	α_C
1.5	$8.1 \cdot 10^3$	0.13	1.13
3	$7.5 \cdot 10^3$	0.14	1.14
5	$5.5 \cdot 10^3$	0.09	1.09
10	$4.6 \cdot 10^3$	0.07	1.07

Table 2. Comparison between classical and quantistic pul TL parameters for a SWCNT.

magnitude higher. As for the capacitance, the quantum capacitance is approximately one order of magnitude smaller than the electrostatic one, hence the parameter α_C is close to 1. A consequence of the huge value of the kinetic inductance is a low propagation velocity and an high characteristic impedance: the values of these two parameters normalized to an ideally scaled PEC interconnect of same dimensions would be

$$\frac{c_{CNT}}{c} \approx \sqrt{\frac{L_m}{L_k}}, \qquad \frac{Z_{CNT}}{Z_C} \approx \sqrt{\frac{L_k}{L_m}}. \tag{26}$$

Indeed the lower propagation velocity is not a real limit for the practical application of CNT interconnects. Assuming the same conditions as those used for Table 2, for $t = 3D$ it is $c_{CNT} \approx 2.5 \cdot 10^6 \text{ m/s}$, instead of $c \approx 2 \cdot 10^8 \text{ m/s}$, hence at $f = 10 \text{ GHz}$ we have a wavelength $\lambda = 0.25 \text{ mm}$. This means that at local and semilocal level the line is electrically short and the propagation delay, although higher than that introduced by an ideally scaled conventional interconnect, is negligible compared to the delay introduced by the *RC* or *RLC* behavior of the line (e.g., Banerjee, 2008; Maffucci et al., 2008b).

Let us now analyze the p.u.l. resistance, using (19) with $\alpha_C \approx 1$ and assumimg $L_K \approx L_{k0}$:

$$R = \nu L_K = \frac{R_0}{2M} \frac{1}{l_{mfp}}, \tag{27}$$

where the collision frequency ν has been defined as

$$\nu = v_F / l_{mfp}, \tag{28}$$

being l_{mfp} the mean-free-path. The values of l_{mfp} may vary significantly if low or high bias conditions are considered. In the low bias condition l_{mfp} may be assumed constant, whereas for higher bias conditions, l_{mfp} depends on the longitudinal voltage too, hence the parameter R is a non-linear function of the longitudinal voltage, usually described as a piecewise linear function with only two asymptotic values (Raychowdhury & Roy, 2006). A detailed discussion on this point may be found in (Park et al., 2004; Maffucci et al., 2009). In addition, assuming a CNT length greater than the *ballistic* length, a simple linear dependence of l_{mfp} on the diameter D may be considered (Nieuwoudt & Massoud, (2006).

As a final remark we note that experimental evidences and theoretical limits show a bulk value of $R_0 / 2 \approx 6.45 \text{ k}\Omega$ for the CNT resistance, which is a lower bound even when ballistic transport is considered (*e.g.*, Park et al., 2004). Furthermore, if the contacts between the CNT and the metallic electrodes at its terminations are not ideal, we have to consider an additional parasitic lumped resistance, whose value is strongly dependent on the quality of the contacts. For poor contacts this value may easily rise up to $100 \text{ k}\Omega$, even if recently the fabrication techinques are demonstrated very good quality contacts (Li et al, 2009). We include these phenomena by adding two lumped resistors $R_P / 2$ in series to the line (see Fig.6b), taking into account both the bulk resistance and the imperfect contact resistance.

3.2 Circuit model for bundles of CNTs

The huge value of the resistance makes useless an interconnect made by a single CNT shell. For practical application purposes, bundles of SWCNTs or MWCNTs are proposed as material to be used in fabricating interconnect traces (ITRS, 2007): all the CNT shells are fed in parallel, so lowering the total resistance. Following the stream of what done in paragraph 3.1, we can model the propagation along a CNT bundle in the frame of the multiconductor TL theory, starting from the scheme in Fig.8.

Let $\mathbf{V}(\omega,z) = [V_1(\omega,z)....V_N(\omega,z)]^T$ and $\mathbf{I}(\omega,z) = [I_1(\omega,z)....I_N(\omega,z)]^T$ be the vectors of the N voltages and currents at given z, in frequency domain. On the k-th CNT eq. (15) reads:

$$(v_k + i\omega)I_k(\omega,z) + \frac{v_F^2}{(1+v_k/i\omega)}\frac{\partial Q_k(\omega,z)}{\partial z} = 2v_F\frac{M_k}{R_0}E_{zk}(\omega,z). \tag{29}$$

Let us assume that in evaluating the low-energy band structure we can neglect the interactions between the CNT shells, as shown by experimental evidence (Maarouf et al., 2000). This assumption allows using the results obtained for isolated CNT shells in evaluating the parameters M_k and v_k in (29). Using again (16)-(17) to derive $E_{zk}(\omega,z)$ and imposing the charge conservation law, we get the MTL equations:

$$-\frac{d\mathbf{V}(\omega,z)}{dz} = (R + i\omega L)\mathbf{I}(\omega,z), \quad -\frac{d\mathbf{I}(\omega,z)}{dz} = i\omega C\mathbf{V}(\omega,z), \tag{30}$$

with the pul parameter matrices given by:

$$L = \alpha_C^{-1}(L_M + L_k), \quad C = C_E, \quad R = \alpha_c^{-1}L_k v, \quad \alpha_c = I + C_q^{-1}C_e, \tag{31}$$

where I is the identity matrix, and the other matrices are given by:

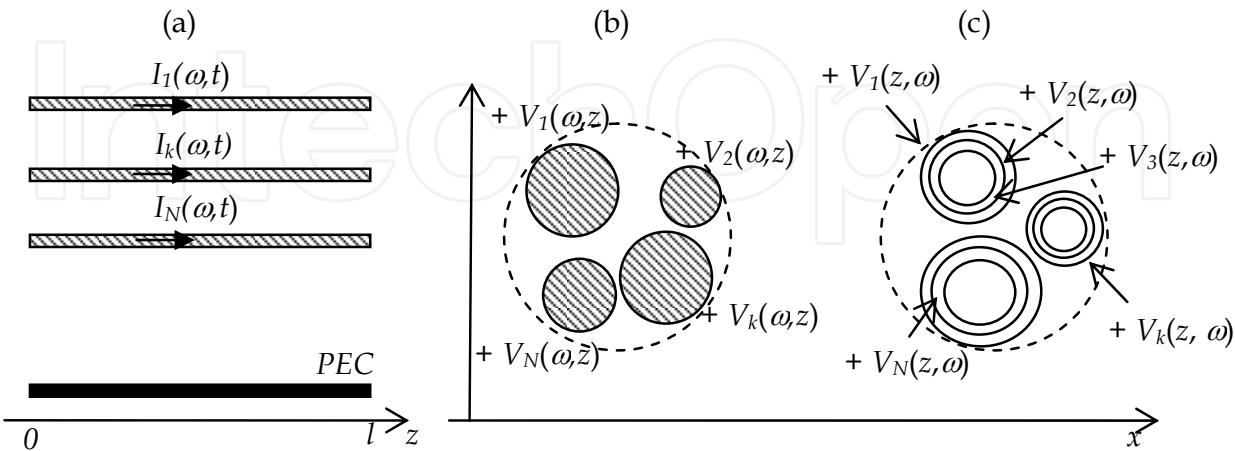$$v = diag(v_k), \quad L_k = diag(L_{kk}), \quad C_q = diag(C_{qk}). \tag{32}$$

Fig. 8. A bundle of CNTs modeled as a multiconductor interconnect: (a) longitudinal view; transverse section of (b) SWCNTs and (c) MWCNTs bundle.

In practical applications any CNT bundle is used to carry a single signal, hence a CNT bundle above a ground may be described by a two-conductor TL model, which could be rigorously derived from the MTL one (30)-(32) assuming all the CNTs in parallel. Alternatively, we can use approximated expressions for its pul parameters. It is important to stress that statistically a low-cost fabrication process for CNT bundles ends up with 1/3 of metallic CNTs and 2/3 semiconducting ones (Li et al., 2005; Naeemi & Meindl, 2006). In addition we have to recall that typical values of SWCNTs diameters fall in the range $2 \div 10$ nm (Cheung et al., 2002), whereas the external shells of MWCNTs have typically diameters greater than 20 nm (Li, 2005; Naeemi, 2006). Recalling the results of Section 2, we can state that in a bundle of SWCNTs we may assume 1/3 conducting (*M*=2) and 2/3 non conducting (*M*=0), whereas all the MWCNTs shells may assumed to be conducting.

Let us first focus on the bundle capacitance. Figure 9 shows two circular bundles made by MWCNTs of external diameter $D_{out} = 20$ nm , embbedded in a homogeneous dielectric matrix of SiO₂ with $\varepsilon_r = 4.5$. The bundle diameter is $D_b = 146$ nm and the center-center distance is $s = 300$ nm . Figure 9 shows the distribution of the electrostatic potential, assuming a differential mode on the pair, which leads to a computed pul capacitance of 88.7 pF/m. The classical pul capacitance of a pair of solid cylindrical conductors would be:

$$C_e = \pi \varepsilon \left/ \cosh^{-1}\left(\frac{2s}{D_b} \right) \right. . \tag{33}$$

Using (33) would give a value of 92.8 pF/m, with an error of less of 5%. In a SWCNTs only 1/3 of the SWCNTs are metallic, hence the distribution of the potential may sensibly vary assuming random distribution of the fraction of metallic CNTs in the bundle. Figure 10 shows two possible cases, referred to a pair made by two bundles of SWCNTs with CNT diameter $D = 2$ nm , bundle diameter $D_b = 18.8$ nm , center-center distance $s = 38.2$ nm , embedded in a SiO₂ matrix ($\varepsilon_r = 4.5$). Table 3 shows the results obtained for the cases of Fig.10 and assuming all the CNTs metallic (case 3). In this condition approximation (33) would give an error of 20%. This approximation is acceptable or not depending on the values of the load capacitance in practical applications, as will be shown in Section 4.
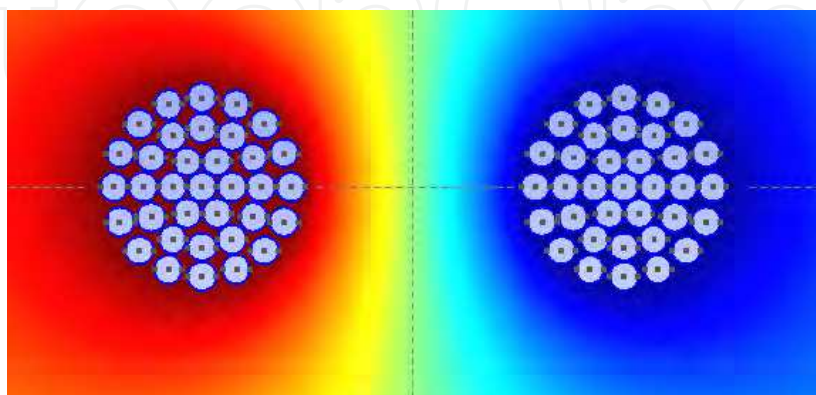

Fig. 9. Distribution of electrostatic potential for a differentially-fed pair of MWCNT bundles.

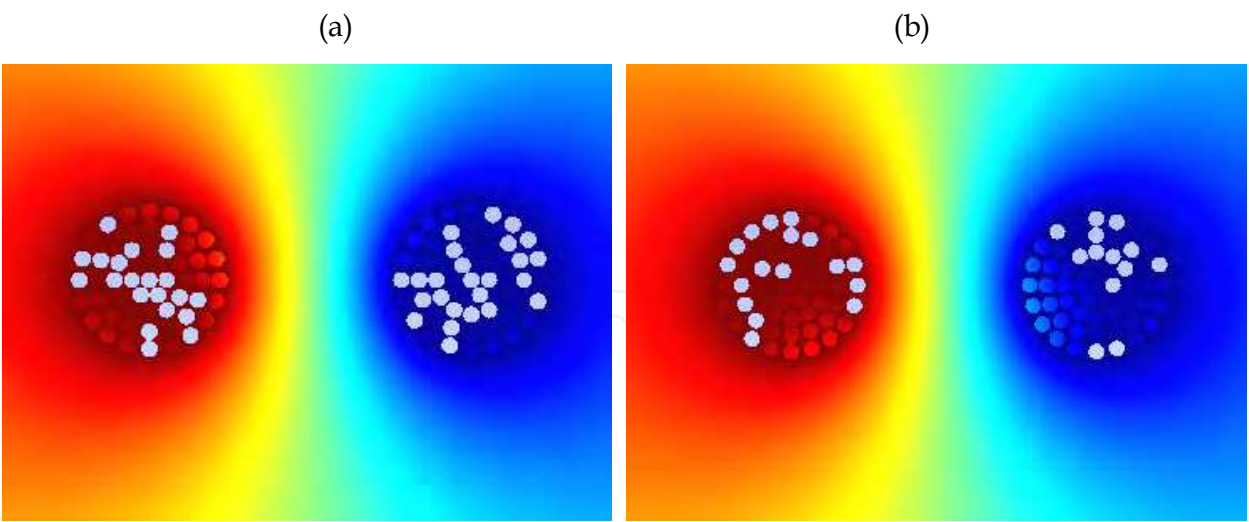(a)   (b)

Fig. 10. Distribution of the electrostatic potential for a differentially-fed pair of SWCNT bundles: two random cases obtained assuming that 1/3 of CNTs are metallic (highlighted).

Case	C_b (exact) [pF/m]	C_b (33) [pF/m]	% err
1	76.5	92.8	17.6
2	74.3	92.8	19.9
3	87.9	92.8	5.4

Table 3. Evaluation of the bundle pul capacitance for the SWCNT cases depicted in Fig.10.

Let us now investigate the behavior of the bundle pul inductance and resistance. The magnetic inductance of the bundle may be computed from the vacuum space electrostatic capacitance, since it is $L_m = \mu_0 \varepsilon_0 C_{e0}^{-1}$, where C_{e0} may be computed exactly or may be approximated by (33) if the introduced error is acceptable (which is the case of MWCNT bundles). When considering the kinetic inductance L_{kb} of a bundle of N SWCNTs, we may assume that only 1/3 are metallic, each of them contributing with $L_k = L_{k0}$. In addition, if we consider CNTs with the same diameters, we have a unique value for ν, hence:

$$L_{kb} = \frac{L_{k0}}{N/3}, \qquad R_b = \frac{\nu L_{k0}}{N/3} = \frac{3}{4} \frac{R_0}{l_{mfp} N}. \tag{34}$$

Recalling the results in Table 2, for N up to the order of 10^3 we may still assume that $L_{kb} \gg L_m$, and since it is $\alpha_C \approx 1$, we may approximate the total bundle inductance with the first of (34), hence following the simple scaling law obtained in literature (Maffucci et al., 2009). Let us consider the case of a microstrip with effective dielectric constant ε_{reff} = 2.2, where the signal trace of width $w = 27$ nm, thickness $t = 2w$ and distance to ground $2t$ is made by a bundle of N SWCNTs of diameter $D = 2.2$ nm. Figure 11 shows the self admittance of the line computed with the rigorous approach and with the proposed approximation, for N=100 and N=200. Here we have assumed $v_F = 8.7 \cdot 10^5$ m/s and $l_{mfp} = 1 \mu$m.
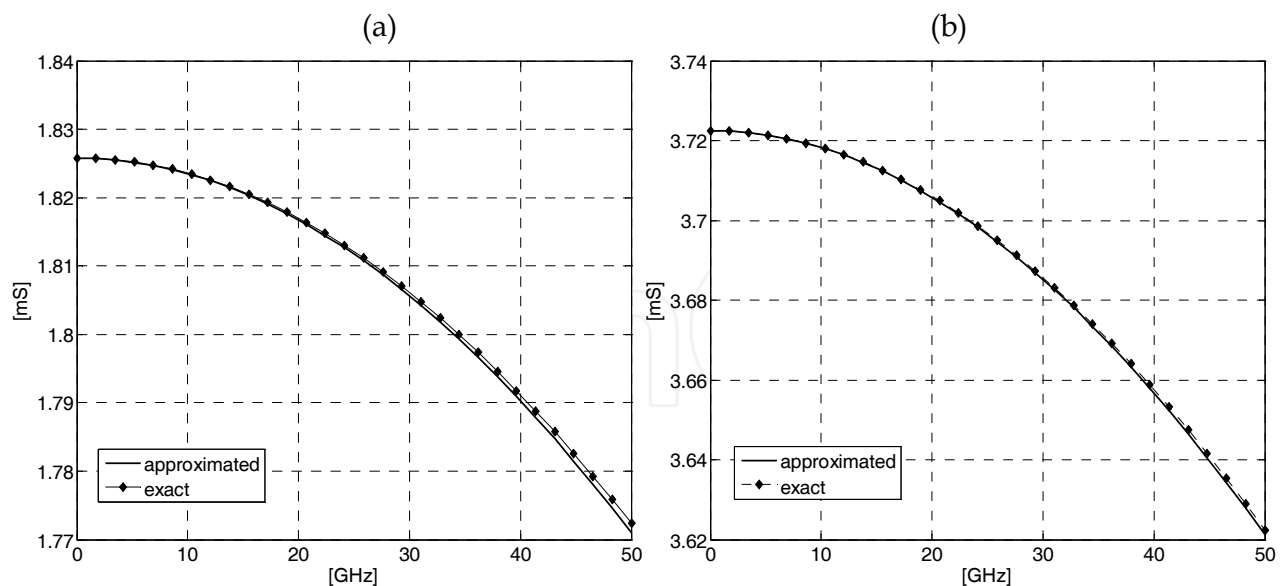
Fig. 11. Self admittance for a bundle of (a) 100 and (b) 200 SWCNTs.

Let us consider MWCNTs bundles. For these structures, the electrostatic capacitance and hence the magnetic inductance may be computed from (33). As for the kinetic inductance, we may introduce a simple approximation based on (13). Let D_{in} and D_{out} be the outer and inner shells diameters, respectively. The inter-shell spacing is the Van-der Waals distance $\delta = 0.34$ nm (Li et al., 2005), hence the number of shells is $N_{shell} = 1 + (D_{out} - D_{in})/2\delta$. Once again we assume 1/3 of the shells to be metallic and 2/3 semiconducting. Assuming the above distribution and using approximation (13) we can compute the equivalent number of channels for a single shell of diameter D_s in a MWCNTs through the simple law:

$$M_{shell} \approx \begin{cases} 2/3 & \text{for } D < d_s/T \\ a_{1s}D_s T + a_{2s} & \text{for } D \geq d_s/T \end{cases} \tag{35}$$

where $a_{1s} = 3.3 \cdot 10^{-4}$ 1/nmK, $a_{2s} = -0.08$ and $d_s = 1.9 \cdot 10^3$ nm·K. The kinetic inductance of a bundle of N_{CNT} MWCNTs is then given by:

$$L_{kb} \approx \frac{2L_{k0}}{N_{CNT}N_{shell}M_{shell}}. \tag{36}$$

where L_{k0} is the bulk value defined in (24). Using (36) in (21) we can derive the bundle quantum capacitance. Using $\alpha_C \approx 1$, from (19), (21) and (28) we may derive the bundle resistance:

$$R_b \approx \nu L_{kb} \approx \frac{2\nu L_{k0}}{N_{CNT}N_{shell}M_{shell}} = \frac{1}{2l_{mfp}}\frac{R_0}{N_{CNT}N_{shell}M_{shell}}. \tag{37}$$

Note that the mean-free-path l_{mfp} may be a function of the diameter D. A simple linear dependence is proposed in (Nieuwoudt & Massoud, 2006).

4. Performance analysis of carbon nanotube interconnects

This Section is devoted to performance analysis of CNT interconnects, compared to conventional Cu ones. The investigated scenarios refer to typical arrangements foreseen for future 22 nm technology node, expected for year 2016 and beyond (ITRS, 2007). In the following the circuit models for CNT interconnects are those presented in Section 3, whereas the conventional ones are described through classical TL models.

4.1 Signal integrity analysis for CNT on-chip interconnects

Let us consider the on-chip interconnect in Fig.12 made by two horizontal traces and a vertical via. The electrical and geometrical parameters given in Table 4 are typical values for the intermediate level at 22 nm technology node (ITRS, 2007). The signal traces on the layers and the via barrel may be either constituted by a solid Cu conductor or by a SWCNT bundle. In particular we consider the case of traces made by bundles of SWCNTs of $D = 0.94$ nm, with a fraction of 1/3 metallic. At the ends of the two horizontal tracts, a lumped contact resistance of $50 \, k\Omega$ is considered for each metallic CNT. For the Cu case, the horizontal tracts are described as striplines, whereas the via is modeled through a series impedance R-L and a pad capacitance to ground (*e.g.,* Chiariello et al., 2009):

$$R_{via} = \frac{\rho l_{via}}{\pi (d_{via}/2)^2}, \; C_{via} \approx \frac{1.41\varepsilon_r D_P T}{D_{AP} - D_P} [pF], \; L_{via} \approx 5.08 \cdot l_{via} \cdot \left[\ln\left(\frac{4l_{via}}{d_{via}}\right) + 1 \right] [nH], \quad (38)$$

where d_{via} and l_{via} are the diameter and the length of the via barrel, respectively, T is the dielectric thickness, D_P and D_{AP} are the diameters of the pad and the antipad, respectively.
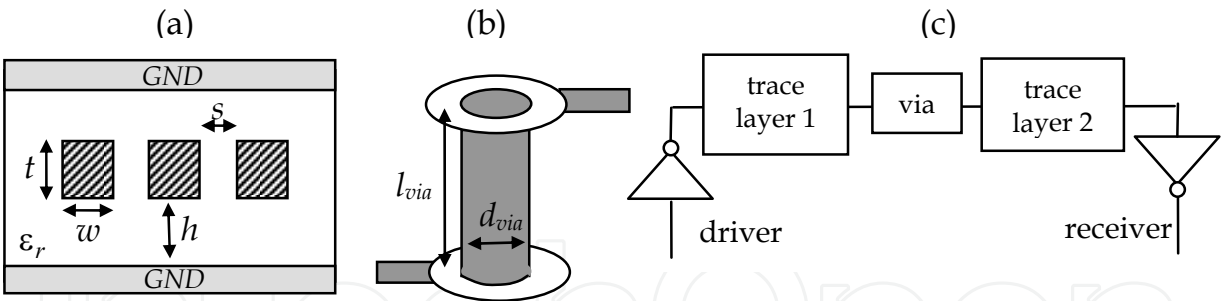

Fig. 12. On-chip interconnect: (a) section of the horizontal tracts; (b) vertical via; (c) circuit.

w [nm]	t [nm]	h [nm]	s [nm]	d_{via} [nm]	l_{via} [μm]	ε_r [SiO$_2$]	ρ_{Cu} [μΩ·cm]
44	44	39.6	44	60	1.0	4.5	4.235

Table 4. Parameters for the case study in Fig.12.

	R [Ω/μm]	L [pH/μm]	C [fF/μm]	R_{via} [Ω]	L_{via} [pH]
Cu	22.31	0.30	0.17	10.19	0.03
CNT	2.29	2.62	0.17	1.29	1.47

Table 5. Computed pul and lumped parameters.

As for the terminations in Fig.12c, we assume that the driver is modeled as a voltage source of $V_s = 0.8V_{DD}$ and a series resistor of $1.5\,k\Omega$, whereas the receiver is modeled as a capacitor of 1 fF. These are typical values for the 22-nm node, obtained from the minimum size gate values in the case with a size factor of 20x (Li et al., 2009).

Let us investigate the signal integrity performances of these two systems, assuming an high data rate (DR) signal transmission. Figure 13 shows the eye-diagrams obtained for the Cu case (Fig.13a-b) and the CNT case (Fig.13c-d), for values of the data rate DR ranging from 3 to 20 Gbit/s. The complete SI analysis is given in Table 6. The jitter is of the same order, whereas the performances of CNTs in terms of eye-opening factor are much better than the Cu ones. For instance at 10 Gbit/s the Cu channel could not be used (Fig.13b), whereas the CNT one still shows good performances (Fig.13c). The behavior obtained here is justified from the consideration that the higher inductance of CNT is compensated by its lower resistance and the SI performance of the overall CNT channel is better than Cu.
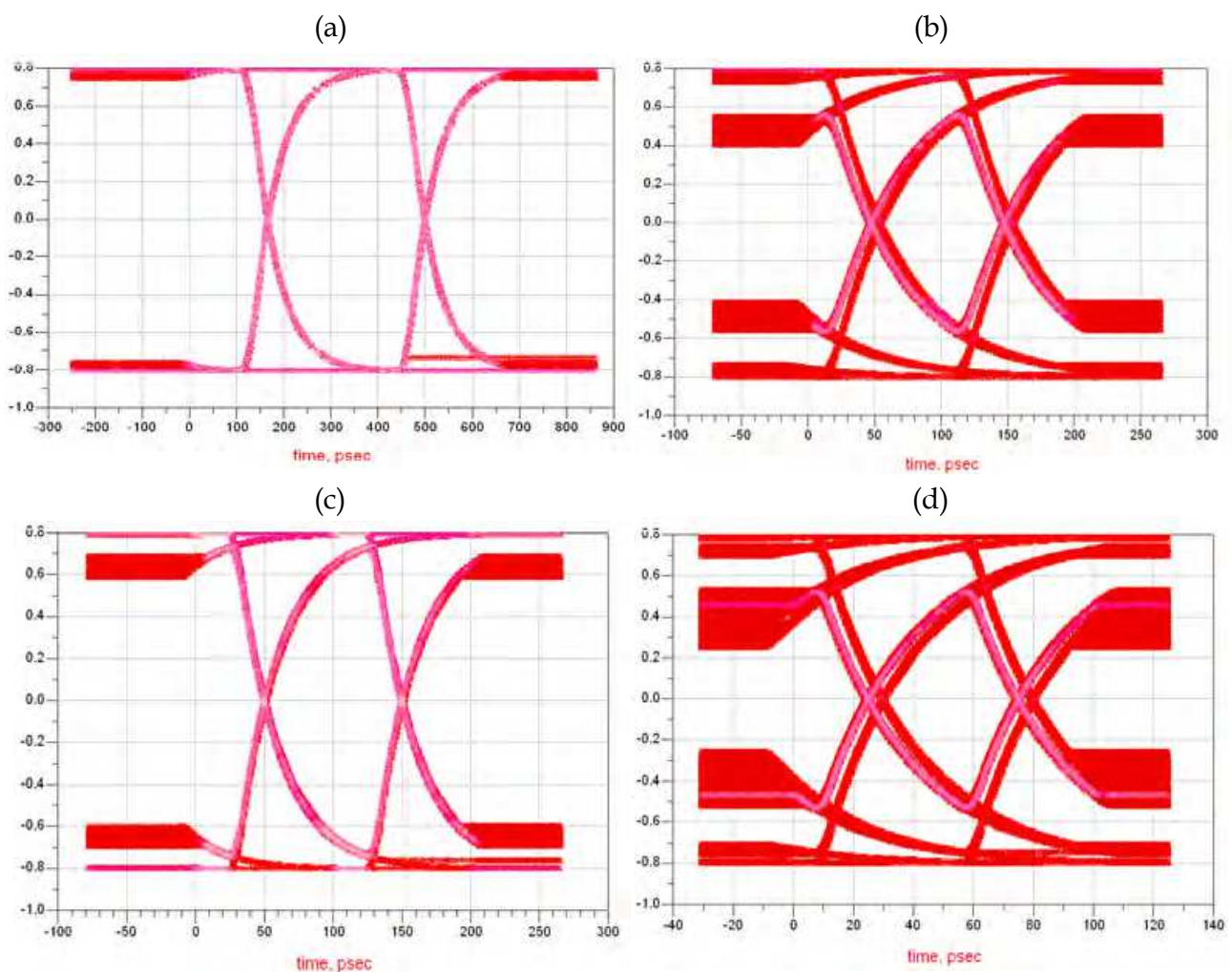

Fig. 13. Eye-diagram analysis for the case in Fig.12: Cu interconnect with (a) DR =3 Gbit/s, t_r=33 ps; and (b) DR =10 Gbit/s, t_r=10 ps; CNT interconnect with (c) DR =10 Gbit/s, t_r=10 ps; and (d) DR =20 Gbit/s, t_r=5 ps.

	DR [Gbit/s]	t_r [ps]	Eye jitter [ps - rms]	Eye opening factor
CNT	1	100	2.1	1
Cu	1	100	1.6	1
CNT	3	33	166	0.99
Cu	3	33	167	0.97
CNT	5	20	100	0.98
Cu	5	20	100	0.91
CNT	10	10	50	0.89
Cu	10	10	50	0.76
CNT	20	5	24	0.72
Cu	20	5	22	0.47

Table 6. Results of the Eye-Diagram analysis for the case-study in Fig.12.

4.2 EMC behavior of CNT interconnects

As pointed out in Section 3, the parameters of the circuit model for CNT interconnects are strongly affected by the presence of a kinetic inductance and a quantum capacitance. Within the limits of the model used here, these two parameters are independent on frequency, hence the frequency behavior of CNT interconnects may be completely different from that of conventional ones. Here we investigate two typical problems arising when performing an electromagnetic compatibility (EMC) analysis: the high-frequency effects on the line impedance and the crosstalk noise.

The case-study used for EMC analysis is the stripline configuration in Fig.14a, assuming global level parameters: $w = 0.32\,\mu m$, $t = 0.19\,\mu m$, $h = 0.15\,\mu m$ and ε_r = 2.2. Three different realizations of the signal traces are compared: solid copper, a bundle of SWCNTs and a bundle of MWCNTs. At such a scale the copper resistivity is $\rho \approx 2.942\,\mu\Omega\cdot cm$.

Let us first study the high-frequency impedance. The first realization of the signal traces is made a bundle of SWCNTs, of diameter $D = 1.4\,nm$, assuming the area of the trace filled by CNTs with a density of 80%, and considering 1/3 of the total number of CNTs to be metallic. The second realization is made by a bundle of MWCNTs, with the same density, each of them with outer shell diameter of $D_{out} = 40\,nm$, inner shell diameter $D_{in} = 0.5 D_{out}$ and inter-shell distance $\delta = 0.34\,nm$. The temperature is 300 K.
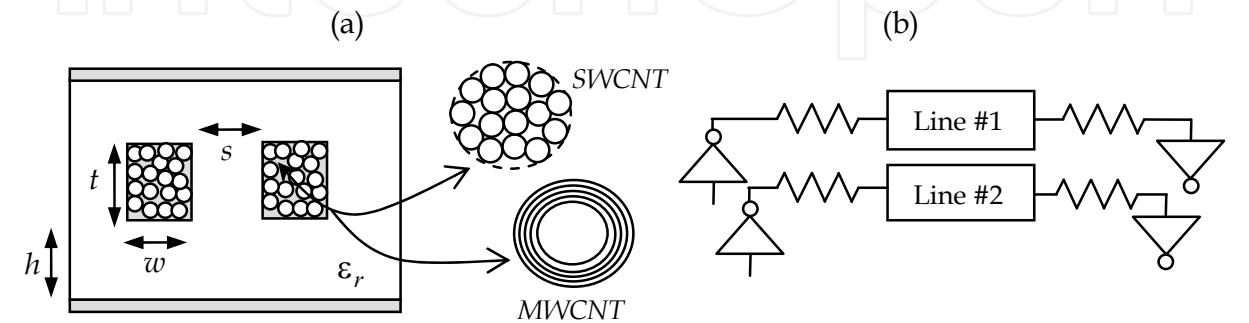

Fig. 14. Interconnect geometry used for EMC analysis: (a) the signal traces are made by either solid Cu or CNT bundles (SWCNT or MWCNTs); (b) circuit for the crosstalk analysis.

The p.u.l. resistances for the three realizations are reported in Fig.15a. For Cu lines this parameter is affected by the skin-effect, whereas for CNTs this effect is not present. For instance, at 200 GHz the penetration depth is $0.19\,\mu m$, equal to t and lower than w. The CNT interconnects outperform the Cu one and show a resistance parameter insensitive to the skin effect. In particular, the MWCNTs bundle outperforms the other two realizations in the whole frequency range: this is due to the fact that all the shells contribute to the conduction. As for the p.u.l. inductance (Fig.15b), the Cu line shows a slight decrease of this parameters, due to the decrease of the contribution of the internal inductance with increasing frequency, whereas the CNTs bundles are again insensitive to it. The MWCNT bundle interconnect introduces an inductance comparable to the Cu one, whereas the SWCNT realization shows an higher inductance. Since the kinetic inductance of the bundles decreases as (34) or (36), it may be comparable to the magnetic one, as in this case. As a conclusion we may assume that CNT interconnects exhibit parameters independent on frequency, whereas the conventional ones would suffer from this problem.

For the crosstalk analysis we consider a coupled stripline as in Fig.14a, assuming $s=w$, and typical intermediate level parameters: $w=t=44$ nm, $h=39.6$ nm and ε_r = 2.2. The considered circuit is shown in Fig.14b. We assume all the resistors of $1450\,\Omega$ assuming that line 1 is fed at near end by a time-harmonic voltage source of amplitude V_{in} = 1 (a.u.). The far-end receivers are modeled as capacitors of 1 fF. For the CNT realizations we consider a density of 80%, a temperature of 300 K and a mean free path $l_{mfp}=0.5\,\mu m$. The SWCNTs diameter is $D=1.4$ nm, whereas for the MWCNTs the outer shell diameter is $D_{out}=30$ nm, the inner shell diameter is $D_{in}=0.5D_{out}$ and the inter-shell distance is $\delta=0.34$ nm. Because of the large number of CNTs in the bundle, we may disregard the effect of the parasitic contact resistances. Figure 16 shows the computed near-end and far-end crosstalk voltages, normalized to the input voltage V_{in}. The crosstalk noise for the three realizations is of the same order, hence from this point of view we may assume an equivalent EMC performance.
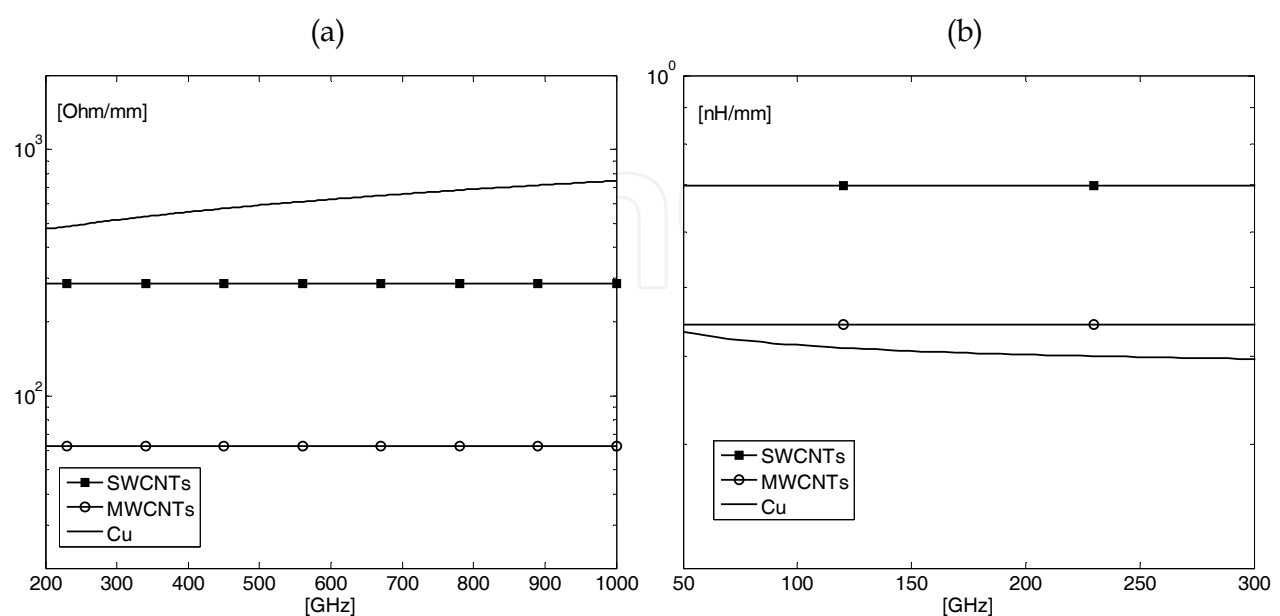

Fig. 15. (a) P.u.l. resistance for a trace made by Cu, a bundle of SWCNTs or MWCNTs;
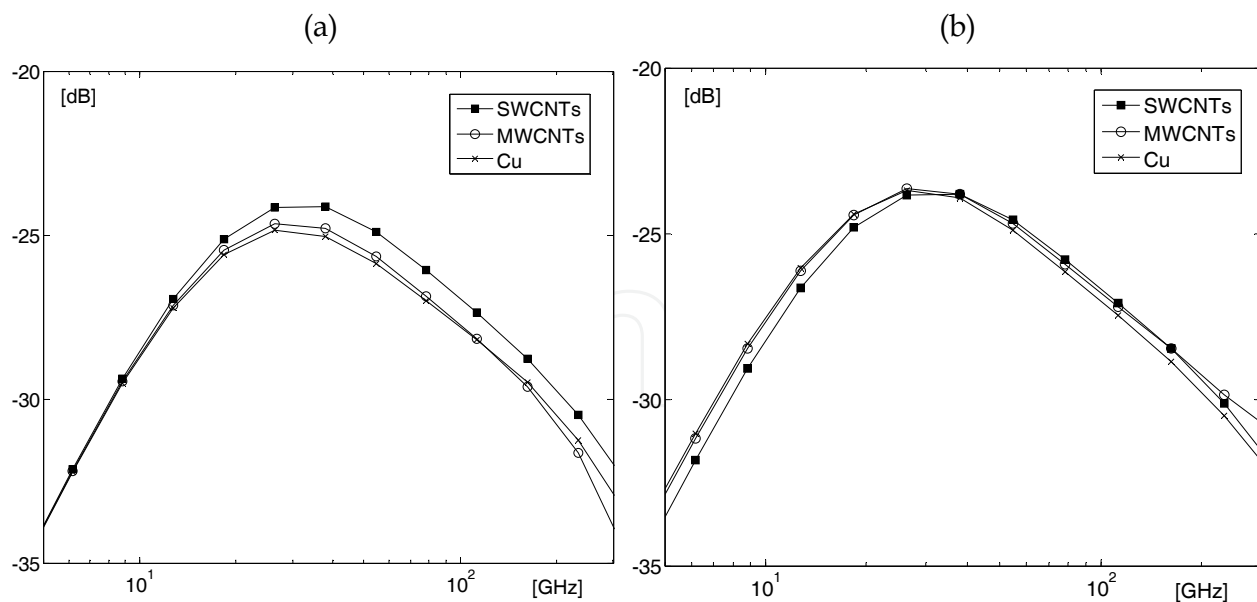(b) P.u.l. inductance for a trace made by Cu, a bundle of SWCNTs or MWCNTs.

(a) (b)

Fig. 16. Crosstalk level for a coupled line made by copper trace, SWCNTs bundle and MWCNTs bundle: (a) near end; (b) far end.

4.3 CNT interconnects as pillars for nanopackaging

Recently the CNTs have been proposed as material for nanopackaging interconnects, since their unique electrical, mechanical and thermal properties allows meeting many of the new requirements for bonding, molding compound, underfill, thermal interface, die attach (Morris, 2008; Maffucci, 2009). One of the main reasons pushing towards the use of CNTs in nanopackaging is the possibility either to achieve good electrical performances and implement new heat removal technologies. The thermal management of future nanoelectronics requires new approaches, since the classical heat removal techniques are inadequate. Using the conventional approach, for instance, the cooler for future Systems in Package would require much greater volume than the semiconductor itself. Recently, CNTs have been proposed as microchannel coolers in thermofluidic cooling approaches and as thermal interface material (TIM). The main limit to the practical use of CNTs is still given by the possibility of integrating a high density of aligned CNTs in a polymer matrix, without degrading thermal conductivity.

In order to investigate the electrical properties of CNT nanopackaging interconnects, let us consider the use of CNTs bundles as chip to package interconnect, following the scheme depicted in Fig.17a: the two vertical pillars are realized using Cu, a bundle of SWCNTs (Fig.17b) or a bundle of MWCNTs (Fig.17c). In terms of electrical properties, the goal of a good packaging interconnect is of course to introduce low parasitics.

As done before, in the following we assume that in the SWCNT bundle only a fraction of 1/3 of the total CNTs is metallic, and similarly in a MWCNT only 1/3 shells are metallic. Using the results in Section 2, since the semiconducting SWCNTs do not give contribution to the conduction, very high density bundles must be fabricated to have low resistance CNT pillars. In MWCNTs, instead, the semiconducting shells give a non-negligible contribution to the conduction and the conduction of the metallic shells themselves is improved, hence the optimal density is a trade-off between the MWCNT outer diameter and its shell number.
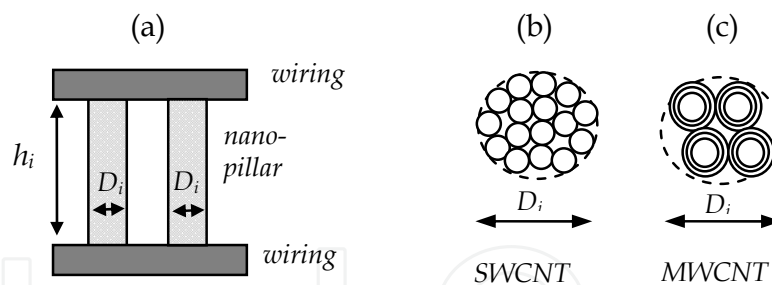
Fig. 17. Chip-to-package interconnect: (a) scheme; (b) SWCNTs and (c) MWCNTs realization
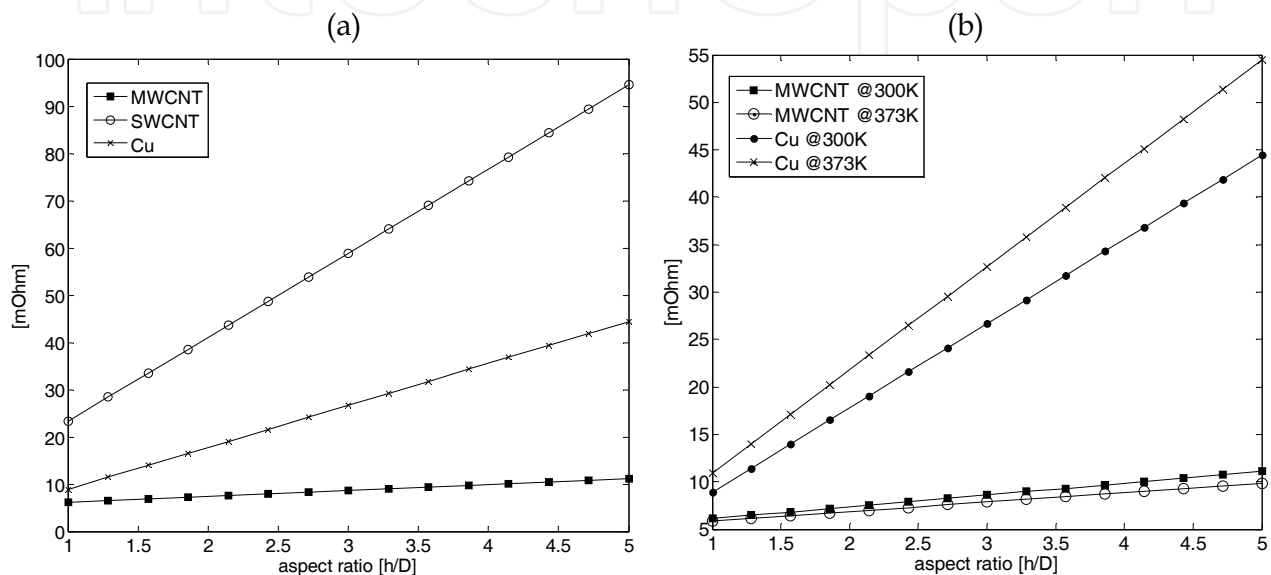

Fig. 18. Parasitic resistance: (a) vs aspect ratio, at T=300K; (b) vs temperature.

Let us consider the case of Fig. 17, assuming the pillars to be cylinders of diameter D_i and height h_i, a wire bond pitch of $20 \, \mu m$ and an operating frequency of 10 GHz at a temperature of 300 K. First we consider Cu interconnects with $\sigma_{Cu} = 5.8 \cdot 10^7 \, S/m$. Next, we fill the interconnect with a bundle of SWCNTs of mean diameter 2.7 nm, assuming a density of 80% of the total area and assuming 1/3 of metallic CNTs. In addition, a parasitic contact resistance of $20 \, k\Omega$ in series to any single CNT is considered. Finally we consider the bundle to be filled by MWCNTs with outer diameter $D_{out} = 30 \, nm$, inner diameter $D_{in} = 0.5 D_{out}$ and inter-shell distance 0.34 nm. We assume again 1/3 of metallic shells and the rest semiconducting, a density of 80% and a contact resistance of $10 \, k\Omega$ per shell. The considered mean free path for all CNTs is $l_{mfp} = 0.5 \, \mu m$.

For this case the parasitic inductances are well beyond the maximum allowed of $5 - 10 \, pH$, hence we focus on the resistance. Figure 18a compares the resistance values obtained for aspect ratios ranging from 1 to 5. The lowest values are obtained by MWCNT bundle, whereas SWCNT bundle shows higher resistance. The better performance of MWCNTs is due to the contribution to the conduction given by the semiconducting shells.

Let us now investigate the problem of the compatibility of CNT interconnects with the temperatures of CMOS technology. Let us consider a realistic chip operating temperature of

T=373 K. When the temperature increases the kinetic inductance decreases, as shown in Section 2, and the difference is more pronounced for large diameters, hence for MWCNTs rather than for SWCNTs. Let us neglect the variation of the mean-free path with temperature: in the same conditions described above, the parasitic resistance for the MWCNT bundle and the Cu interconnects would be that shown in Fig.18b. Note that for the copper at 373K we have assumed $\sigma_{Cu} = 4.0 \cdot 10^7$ S/m. The MWCNT interconnect has better performances at higher frequencies, which is opposite to the behaviour of Cu ones.

Finally let us focus on the problem of proximity effect, which is a major problem for packaging interconnect, since it leads to non uniform current crowding which cause local thermal hot-spots. Figure 19 shows the distribution of the current density over two CNT bundles at a distance equal to their width. The coupled line is driven by a differential signal (in the figures the underlying PEC ground is omitted). The simulations have been carried out by means of the full-wave 3D numerical simulator SURFCODE (Miano & Villone, 2005). As shown in Figs.19a-b, the CNT bundles do not suffer from the proximity effect, which is a problem for the conventional interconnects, Figs.18c-d. This is again due to the presence in the CNT electrodynamics of a kinetic term which dominates over the magnetic one. Within the limits of validity of our model, this kinetic term is independent from the distance between the bundles, which, on the contrary, is a crucial parameter in determining the proximity effect when conventional interconnects are analyzed.

(a)   (b)

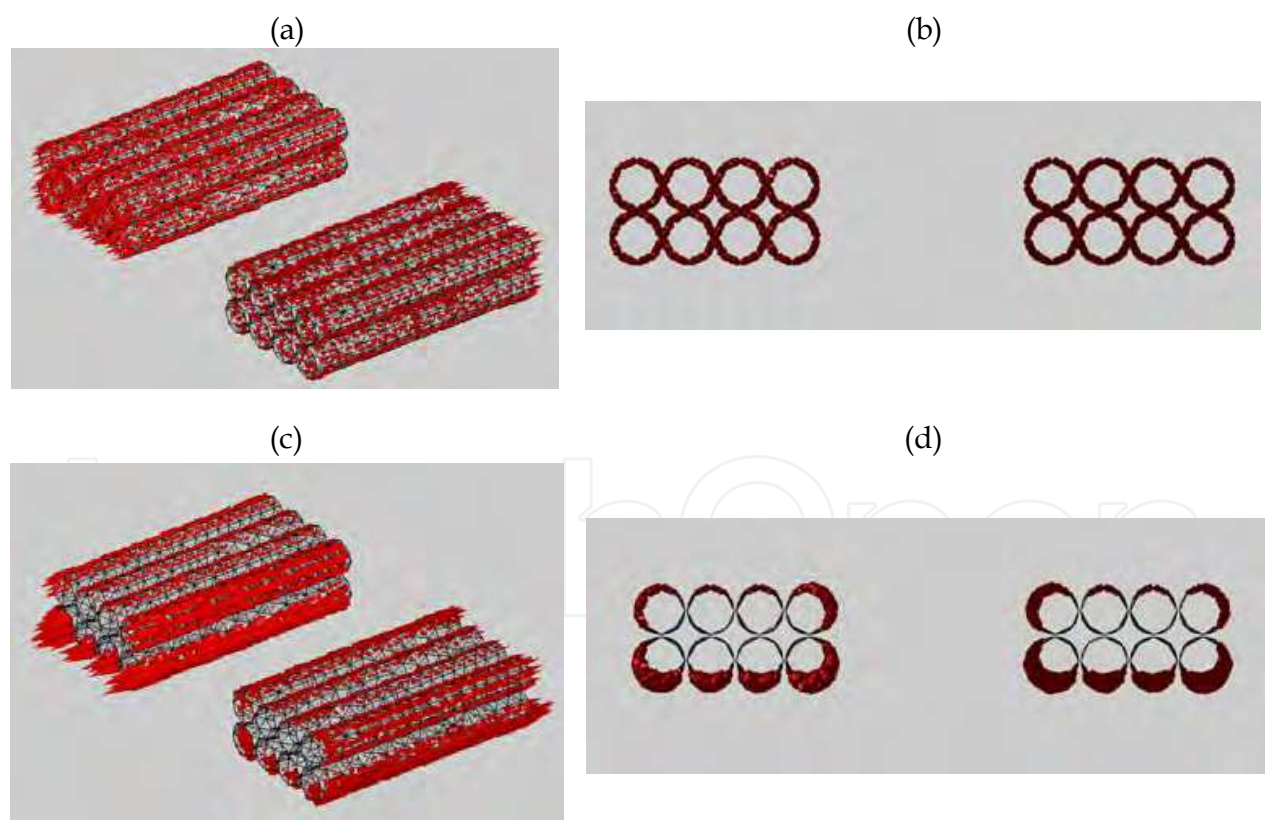(c)   (d)

Fig. 19. Current density distributions over two bundles (differential mode): (a) CNT bundles; (b) CNT transverse section; (c) PEC bundles; (d) PEC transverse section.

5. Conclusion

This Chapter has analyzed the use of carbon nanotubes as possible innovative material to fabricate high-speed interconnects at various hierarchical levels of the future nanoelectronics technology.

The electromagnetic model presented here to describe the electrical propagation along CNT interconnects is derived from a semi-classical solution of the transport equation. All the quantistic and kinetic effects affecting the electrodynamics of charge carriers are taken into account through global parameters (*kinetic inductance* and *quantum capacitance*) which have been simply related to the number of effective conducting channel per CNT shell. Both the rigorous approach and a simple approximation have been provided to evaluate this number for CNT shells of arbitrary chirality, hence allowing the modelling of either single-wall and multi-wall CNTs.

The electrodynamical model provides a constitutive equation for the CNT interconnect, which is coupled to the Maxwell equation: the problem is then solved in the frame of the classical transmission line theory, so obtaining simple equivalent RLC circuits for single CNTs and bundles of CNTs. These models are consistent with those used in literature for metallic CNTs, but introduce the possibility to deal with arbitrary chiralities and CNT diameters. The analysis of CNT transmission lines highlights unusual behaviours with respect to the Cu lines, mainly due to the strong influence of the kinetic and quantistic terms over the classical electromagnetic inductance and capacitance parameters.

Some case-studies have been carried out referring to a realistic use of CNTs to build interconnects for wiring and for packaging nanotechnology ICs. In the considered examples typical values of the future 22nm technology nodes have been assumed. A signal integrity analysis is performed with reference to an on-chip interconnect arrangement, where the conventional Cu material is replaced by a bundle of CNTs. Next, the EMC behaviour of two adjacent traces in a stripline is analyzed, checking the high-frequency effects and the crosstalk noise. Finally the use of CNTs as pillars for nanopackaging is studied and the introduced parasitics are compared to conventional material realization.

Carbon nanotube interconnects are shown to have better behaviour with respect to conventional Cu ones in terms of SI and EMC performances, introduced parasitics and response to temperature variations. From a practical point of view, these simulated performances may be achieved provided that good quality bundles are realized, *i.e.* bundles of high density, good direction control and good terminal contacts.

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This book has been outlined as follows: A review on the literature and increasing research interests in the field of carbon nanotubes. Fabrication techniques followed by an analysis on the physical properties of carbon nanotubes. The device physics of implemented carbon nanotubes applications along with proposed models in an effort to describe their behavior in circuits and interconnects. And ultimately, the book pursues a significant amount of work in applications of carbon nanotubes in sensors, nanoparticles and nanostructures, and biotechnology. Readers of this book should have a strong background on physical electronics and semiconductor device physics. Philanthropists and readers with strong background in quantum transport physics and semiconductors materials could definitely benefit from the results presented in the chapters of this book. Especially, those with research interests in the areas of nanoparticles and nanotechnology.

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