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Diffraction Based Overlay Metrology for Double Patterning Technologies

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1. Introduction

193nm optical immersion lithography is approaching its minimum practical single-exposure limit of 80nm pitch [1]. The semiconductor industry has adopted double patterning technology (DPT) as an attractive solution for the low k_1 regime until extreme ultraviolet (EUV) lithography becomes commercialized. DPT also brings additional demands of increased critical dimension uniformity (CDU) and decreased overlay errors. The International Technology Roadmap for Semiconductors (ITRS) [2] target for overlay control at the 32nm DRAM node in single patterned lithography steps is 6nm. The process budget is reduced to 1.1nm for DPT. If 20% of the process error budget is allowed to occur in the metrology tool, as the ITRS states, then the measurement error budget at the 32nm node is 1.2nm for single patterning, and 0.22nm for DPT.

The ITRS defines total measurement uncertainty (TMU) for overlay only in terms of precision, tool-induced shift (TIS) variation and site-to-site tool matching differences. Determining whether a measurement technology is capable of controlling these advanced processes is no longer a case of simple data self-consistency checks on precision, TIS and matching. For example, the error arising from assumptions of a linear change of overlay error with position is significant. This error can be reduced by using very small targets [3] and performing in-device overlay measurements, but the demanding sub-nanometer measurement budget in overlay measurements still remains a considerable challenge.

Recent advances in lithography metrology for advanced patterning have led to the proposal of three different pitch splitting technologies [Fig. 1]. The Litho-Etch-Litho-Etch method (LELE, Fig. 1a) involving two process steps requires very tight overlay control and is both very expensive and slow, making alternative methods attractive. The first alternative process flow is Litho-Freeze-Litho-Etch (LFLE), which reduces the processing cost by replacing the intermediate etch step with a process step in the litho track (Fig. 1b). After exposing the first pattern, the resist is baked in a post-exposure bake (PEB) step and developed. Exposed pattern is coated with material to freeze the resist. The second resist layer is added and the second exposure is done. The freezing material prevents the first resist layer from washing away during the second layer PEB and develop steps. This

technique allowed printing 2D logic cells and dense poly lines with two lithography steps, illustrating good resolution and process margin [4].

The next alternative process is Self-Aligned Double Patterning (SADP, Fig. 1c), in which a spacer film is formed on the sidewalls of pre-patterned features. Etching removes all the material of the original pattern, leaving only the spacer material. Since there are two spacers for every line, the line density has now doubled. The spacer approach is unique in that with one lithographic exposure the pitch can be halved indefinitely with a succession of spacer formation and pattern transfer processes. The spacer film deposition process is very uniform and results in extremely good SADP CDU of less than 1nm. The spacer lithography technique has most frequently been applied in patterning fins for FinFETs and metal layers [5].

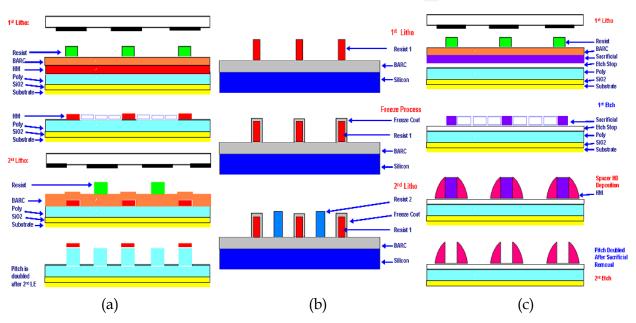


Fig. 1. Various double patterning schemes: (a) Litho-etch-litho-etch (LELE), (b) Litho-freeze-litho-etch (LFLE), and (c) Self-aligned double patterning (SADP)

These pitch splitting double patterning techniques not only involve more demanding process steps, they also require tighter overlay control than conventional single patterning [2]. Therefore measurement of overlay with much higher certainty is a necessity. As technology transitions toward the 22nm and 16nm nodes using these methods there is serious concern about the capability of the available metrology solutions, both in process development and production control.

High TIS and tool-to-tool matching errors make it difficult to meet the measurement uncertainty requirements using the traditional Image-Based Overlay method (IBO), even though most advanced IBO tools are operating at TMU levels under 1nm. Diffraction-based (scatterometry) overlay (DBO) measurement is an alternative optical measurement technique that has been reported to offer better precision than IBO and near zero TIS [6, 12, 14-15], and is therefore a possible solution to the measurement uncertainty budget. Bischoff et al. proposed measuring overlay using the diffraction efficiencies of the first diffracted orders [7]. Chun-Hung Ko used angular scatterometry combined with an experimental library to determine the overlay error on ADI stacks with intermediate poly-silicon lines [8]. H.-T. Huang et al. used spectra from reflection symmetry gratings and a rigorous coupled-

wave analysis (RCWA) regression approach to calculate the overlay error [9]. W. Yang *et al.* [10] and D. Kandel *et al.* [11] used arrays of specially constructed pads with programmed offsets to determine overlay without the need for model fitting. These DBO methods have the potential to meet the demanding overlay metrology budget for sub-32nm technology nodes. In this chapter, the advantages of DBO for precise and accurate overlay measurement in LELE, LFLE and SADP processes will be shown.

2. Spectroscopic scatterometry

2.1 Experimental setup

Spectroscopic scatterometry is used to measure overlay errors between stacked periodic structures (*e.g.*, gratings). In this technique, broadband linearly polarized light is incident perpendicular to the wafer surface and the zero-order diffracted signal (spectrum) is measured as a function of wavelength. Fig. 2 shows a typical experimental configuration. At normal incidence, different reflectance spectra are obtained for various angles of polarization with respect to that of the periodic structure. Typical data collection involved both TE and TM spectra. A specific advantage of using polarized light is that it provides enhanced sensitivity as both the amplitude and phase differences between the TE and TM spectra can be measured.

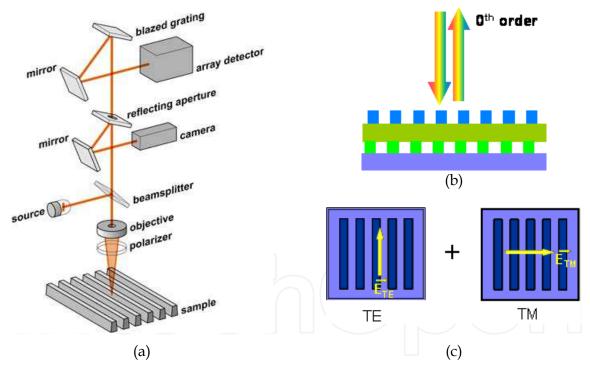


Fig. 2. The figure shows a typical hardware set up for a normal incidence scatterometer (a) spectroscopic reflectometry, (b) normal incidence reflection, and (c) TE TM data acquisition modes.

2.2 Theory

Spectra are obtained from pads, each of which has gratings patterned in both layers between which the overlay error is being measured (Fig. 3). The gratings in each pad are overlaid but by design shifted with respect to each other. Spectra from pads with shifts of equal magnitude but opposite direction are identical due to symmetry:

$$R(+x_0,\lambda) = R(-x_0,\lambda) \tag{1}$$

Here $R(x_0,\lambda)$ is the reflectance spectrum from one pad as a function of wavelength λ and shift $+x_0$. The difference spectra $(\Delta R(\lambda) = R(+x_0,\lambda) - R(-x_0,\lambda))$ from two pads with shifts $+x_0$ and $-x_0$ is zero in the absence of noise in the measuring tool. A small overlay error shifts both upper gratings in the same direction and breaks the symmetry. The resulting differential spectrum is proportional to the direction and magnitude of the overlay error:

$$\Delta R(\lambda) = R(x_0 + \varepsilon, \lambda) - R(-x_0 + \varepsilon, \lambda) = R(x_0 + \varepsilon, \lambda) - R(x_0 - \varepsilon, \lambda) \approx 2\varepsilon \frac{\partial R}{\partial x}\Big|_{x_0}$$
 (2)

Here, ε is the overlay error, x_0 is the offset bias, and a Taylor expansion of the reflectance around x_0 has been applied. The overlay error can now be calculated by comparing the measured differential spectrum, ΔR , to a second differential spectrum, $\Delta R'$, acquired from a pair of test pads having a known relative offset. If, for example, a shift of $x_0+\delta$ is designed into a third pad, then within the linear-response range the difference between its spectrum and that from the $+x_0$ pad is:

$$\Delta R'(\lambda) = R(x_0 + \delta + \varepsilon, \lambda) - R(x_0 + \varepsilon, \lambda) \cong \delta \frac{\partial R}{\partial x}\Big|_{x_0}$$
(3)

Equation (3) provides the calibration required to calculate the overlay error, ε:

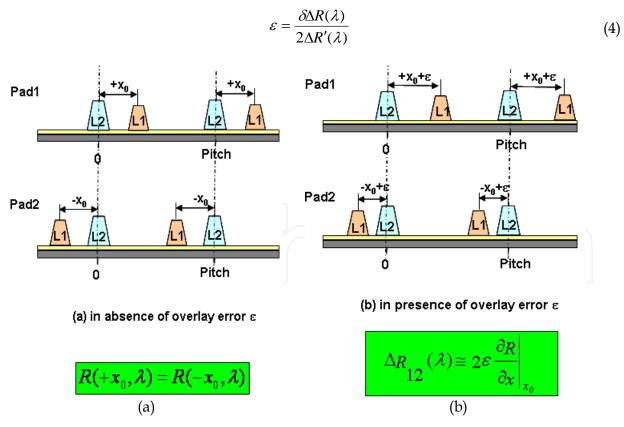


Fig. 3. Illustration of DBO targets design: (a) reflection symmetry with $+x_0$ and $-x_0$ shift (no spectral difference), (b) reflection symmetry broken due to overlay error with $+x_0+\varepsilon$ and $-x_0+\varepsilon$ shift (ΔR_{12} spectral difference between pads 1 and 2).

From equation (4), the ratio $\Delta R(\lambda)/\Delta R'(\lambda)$ must be independent of λ . This arises from our assumption that ϵ is small and only applies in the linear-response range where this assumption holds.

Equation (4) shows that the overlay can be measured in one direction using a minimum of three pads with suitably defined offsets. In practice four pads are often used (Fig. 4a), with an additional calibration pad with offset $-x_0$ - δ , because the additional data improves precision and provides a check that the overlay error is within the linear response range (Fig. 4b). To measure overlay in two directions two sets of gratings are required. The second set with the gratings rotated by 90° from the first. As overlay is a vector quantity, it is usually measured in both X&Y directions. The nomenclature "2x3 target" and "2x4 target" indicates whether three or four pads are used to measure in each axis.

The nomenclature CD/pitch is also used to indicate the designed CD and pitch of the gratings in each target. For example 65/390 means CD=65nm and pitch=390nm. All targets use the same CD and pitch at both layers.

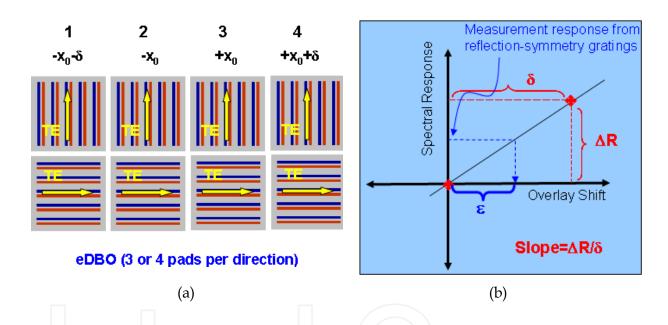


Fig. 4. (a) 2x4 target used for measurement of signal by a normal incidence scatterometer and (b) linear dependency of overlay shift as a function of spectral response.

2.3 Spectral response to overlay

Fig. 5(a) and (b) show the spectral response to overlay in the difference spectra from programmed reflection-symmetry gratings (Equation 2). The black line in the figure is the average $\Delta R'(\lambda)$ from both pairs of calibration pads, scaled by $(2\varepsilon/\delta)$. The scaled signal shows excellent agreement with the measured response, in accordance with equation 4. The maximum response increases from ~15x (Noise) to ~45x (Noise) for a ~3x change in overlay error. As expected, the spectral response also changes in sign with the measurement. The maximum spectral response at any wavelength is about 4x (Noise) per 1 nm overlay error. Measurement uncertainty much better than 0.25nm is possible because the data is summed over all available wavelengths.

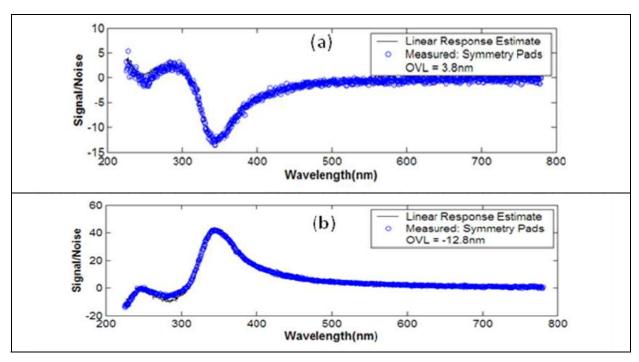


Fig. 5. Spectral response (signal to noise ratio) for corresponding overlay errors (shown in the legend box). The plots (a) and (b) also show response (black line) calculated from the calibration pad spectra.

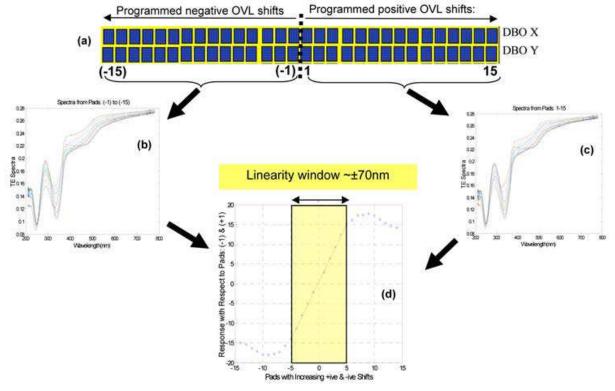


Fig. 6. (a) shows Pads 1-15 with increasing programmed positive overlay shifts and Pads (-1)-(-15) with increasing negative overlay shifts, (b) and (c) show TE spectra from corresponding vertical and horizontal gratings pad sequence respectively, and (d) shows measured spectral response in arbitrary units.

2.4 Range of linearity

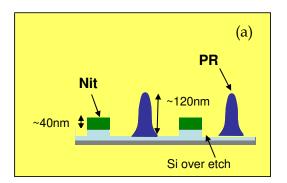
The linearity range is tested by printing a sequence of pads with varying overlay shifts. Pads in the right half of the sequence (pads 1-15) have increasing positive overlay shifts (Fig. 6(a)) in 15 nm steps. Pads (-1) to (-15) have increasing negative overlay shifts in 15 nm steps. Fig. 6b and 6c shows raw TE spectra collected from these pads. The difference signal is calculated by subtracting the pad 1 spectrum for pads 2-15, and the pad -1 spectrum for pads -2 to -15. The range of linearity observed (~±70 nm) is significant for this process as shown in Fig. 6(d). A similar linearity range is observed for the horizontal gratings.

3. Litho-etch-litho-etch (LELE)

In the absence of a EUV processing solution below 80nm pitch, DPT using the litho-etch-litho-etch dual step became an attractive solution for the low k₁ regime.

3.1 DPT structures for testing DBO

The first DPT test structure is on a silicon substrate (fig 7a). The structure consists of ~120 nm photoresist lines and ~40 nm nitride lines with silicon over etch. The second DPT stack (Fig. 7b) comes from a gate (bitline)-level patterning step in a NOR flash process.



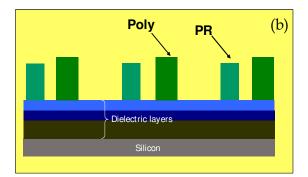


Fig. 7. (a) DPT test structure with \sim 120 nm photoresist lines and \sim 40 nm nitride lines with silicon over etch on a silicon substrate. (b) Gate level (bitline) patterning step in a NOR flash process.

3.2 DBO Measurement accuracy: correlation with IBO and CD-SEM

DBO measurement accuracy is assessed by comparing the results against IBO and CD-SEM data.

3.2.1 Correlation with IBO data

Fig. 8(a) shows the comparison between DBO and IBO measurements on all 143 fields on the wafer. There is a very good correlation between DBO and IBO measurements (R^2 =0.99) with an offset of ~7 nm. The correlation is good for the subset of measurements less than ±3 nm (Fig. 8b). The inset histogram in Fig. 8a shows the difference in IBO and DBO measurements after removing the ~7 nm constant offset. The distribution is approximately normal with standard deviation of 1.8nm.

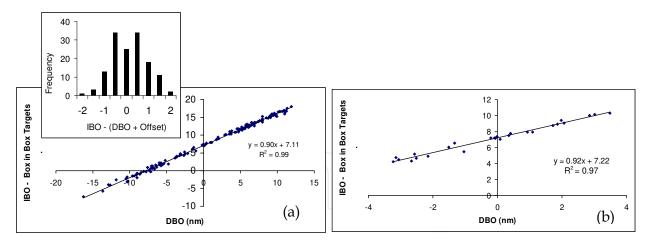


Fig. 8. DBO vs. IBO correlations: (a) shows correlations on 143 targets throughout the wafer, and (b) shows a subset of the same data in a narrow range of ~±3 nm.

3.2.2 Correlation with CD-SEM

Fig. 9a shows a CD-SEM image of the nitride (dark) and photo resist (gray) lines. Fig. 9(b) and 9(c) show the correlation between DBO and CD-SEM measurements. In Fig. 9b, the overlay errors are calculated from the CD-SEM data for the top of the lines, while in Fig. 9c, the bottom CD data is used. The inset histograms in Fig. 9b and 9c show the distributions of the difference between IBO and CD-SEM measurements after subtracting offsets.

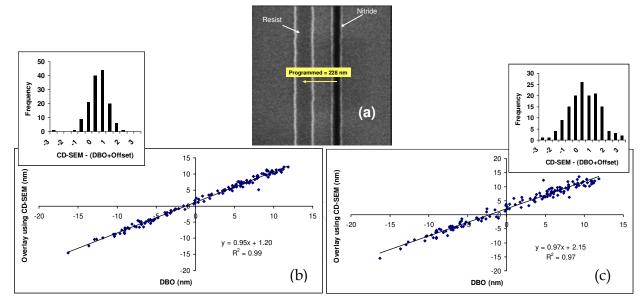


Fig. 9. (a) CD-SEM image of the DPT structure. Dark lines are nitride and the light gray lines are the photo resist, (b) correlations between DBO and top CD-SEM measurements from all 143 fields. The inset histogram shows the histogram of differences between the CD-SEM and DBO results. (c) as (b) but using the bottom CD data.

The slope of the CD-SEM/DBO correlation is 0.95 (for top CD) and 0.97 (bottom CD) compared to 0.9 for the DBO-IBO correlation. The offset between DBO and CD-SEM measurements is in the order of 1 to 2 nm. Concerns of accuracy of IBO measurements have been raised before. Dusa *et al.* reported an offset of ~5 nm between CD-SEM and IBO for a

DPT application [10]. It is possible that this offset might be coming from differences in mask writing errors between the DBO and IBO targets. The CD-SEM results are closer to the DBO values (Fig. 9), which suggests that the accuracy of the DBO technique is better than that of IBO. The observed DBO repeatability is better than CD-SEM. DBO is also non-destructive, as the sample is not subjected to the charging effects that occur in a CD-SEM.

3.3 DBO target types – 4 vs. 3 pads

While scatterometry offers precise and accurate overlay measurements for DPT, the number of reference and sample pads required for such measurements are still a concern. In this section the possibility of reducing the number of pads required without sacrificing the performance is explored using two target types – 2x4 and 2x3 targets (see definitions of target types in Section 2). Fig. 10 shows excellent correlation between overlay measured from these 2 target types. Table 1 shows the root mean square dynamic precision on two 2x3 targets and a 2x4 target. The precision on the 2x3 targets is a factor of ~1.4 (= $\sqrt{2}$) higher than that on the 2x4 target because the number of difference spectra used in the 2x4-target algorithm is twice the number used for the 2x3-target algorithm. The figure shows excellent correlation, suggesting that a 3-pad target is sufficiently accurate for this application. The dynamic (load-unload) and static (no unload) precision is excellent.

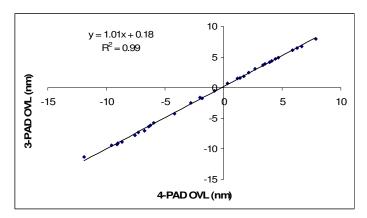


Fig. 10. Correlation between DBO targets with 4 pads/direction and 3 pads/direction.

Target/Measurement Type	RMS Precision				
Targey Wieasurement Type	3 _σ (nm)				
2x3 Dynamic, Target-II	0.31				
2x3 Dynamic, Target-II	0.32				
2x4 Dynamic, Target II	0.20				
2x4 Static, Target II	0.11				

Table 1. Shows the dynamic precision for the 2x3 target is higher by a factor of $\sim \sqrt{2}$

3.4 DBO performance: precision, TIS, matching, and TMU

DBO capability was further assessed by calculating the measurement uncertainty (TMU) from the measurement data using equation (5). Short-term (dynamic) precision (DYNP) is

the Root-Mean-Square (RMS) of three times the standard deviation of the 20-cycle measurements at each target, and combines static precision (occurring without any movement within the tool) and the effects of target reacquisition. $TIS3 \sigma_T$ is three times the standard deviation of the TIS in the measurements at each target. Where multiple tools are available, site-by-site tool matching (TM) is included. TM is three times the standard deviation of the difference in the average measurement from each tool at the same location. Matching data is not included in the DBO to IBO TMU comparison.

$$TMU = \sqrt{(DYNP)^2 + (TIS3\sigma_T)^2 + (TM)^2}$$
 (5)

The results for both DPT applications are summarized in Table 2. For the gate level DPT the dynamic precision is less than 0.1 nm and the TMU is 0.26 nm Average TIS is under 0.1nm. DBO matching data is between two tools (Atlas and FLX) with similar reflectometer optical heads. For the DPT structure on a silicon surface precision is 0.2nm. TIS and matching data is not available for this structure.

TECHNOLOGY	Process Step	DYNP (nm)	TIS Avg (nm)	TIS 3 _σ (nm)	Tool Match	TMU
DBO	DPT Silicon Substrate	0.2				
DBC	DPT Gate Patterning NOR Flash	0.07	-0.04	0.17	0.18	0.18
IBO	DPT Silicon Substrate	0.48	-0.37	0.31		0.57
IDO	DPT Gate Patterning NOR Flash	0.33	-2.05	6.03		6.04

Table 2. Performance summary of DBO on two stacks discussed in Fig. 4.

4. Litho-freeze-litho-etch (LFLE)

While LELE involving two process steps offers an adequate solution for DPT process steps, both are very expensive and slow. The alternative Litho-Freeze-Litho-Etch (LFLE, Fig. 1b) process reduces cost by replacing the intermediate etch step with a process step in the litho track.

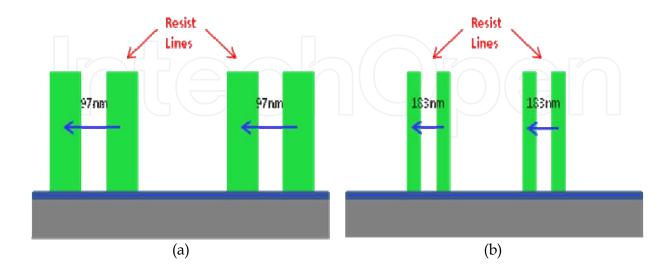


Fig. 11. DPT structure that has alternative photo resist lines with silicon over etch: (a) 65/390 Line/Pitch ratio, (b) 110/660 Line/Pitch ratio

4.1 DPT Structure

An example LFLE DPT structure consists of ~120 nm photoresist lines with silicon over etch as shown in Fig. 11(a) and (b).

4.2 Prediction based on simulation

The LFLE stack is built on silicon with BARC and two resist lines patterned on top. Simulated spectral response curves (for example from NanoDiffractTM, Nanometrics scatterometry software) are used to predict the static precision of overlay measurements in the range of interest for the LFLE DPT stack.

Static precision for the LFLE model can be determined by two different methods. The first method uses analysis of measurement covariance as described by Vagos *et al* [16], and which is referred to as the "Uncertainty and Sensitivity Analysis" method (U&SA). In the second method random noise is introduced into the simulated DBO spectra for all four pads and static precision determined as if the spectra are obtained experimentally. The predicted static precision for the LFLE stack is 0.24nm (3 σ) using the U&SA method and 0.30nm by the noise induced method.

4.3 DBO accuracy (freeze process)

4.3.1 Correlation with IBO

The CD-SEM image in Fig. 12a shows the resist lines in one of the four pads of a 110/660 target. To test measurement accuracy, the results from two 2x4 DBO targets (65/390, 110/660) are compared with IBO measurements using nearby Blossom targets. Fig. 12b shows the excellent correlation ($R^2 \sim 0.99$) between the DBO and Blossom data.

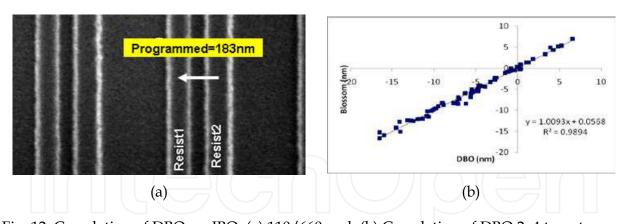


Fig. 12. Correlation of DBO vs. IBO: (a) 110/660 pad, (b) Correlation of DBO 2x4 targets vs. blossom

4.4 LFLE DBO performance

Good overlay control requires good measurement capability. Table 3 summarizes the precision, TIS, tool matching and measurement uncertainty (TMU) for DBO of LFLE structures. DBO dynamic precision was less than 0.2nm and TMU less than 0.5nm. Average TIS was under 0.1nm.

Table 3 contains site-by-site (SBS) 3σ DBO matching data for this stack from three tools with the same design of reflectometer optical head. Tool matching of 0.14nm or less is achieved

without calibration or adjustment of the tools. This is possible because the method of equation (4) is self-calibrating. Absolute spectral matching between tools is not necessary.

TECHNOLOGY	CD:Pitch Tool	Tool	DYNP (nm)		TIS Avg (nm)		TIS 3 _σ (nm)		Tool Match		TMU	
		1001	X	Y	X	Y	X	Y	X	Y	X	Y
	65:390	Tool1	0.19	0.17	0.06	-0.01	0.42	0.44	0.09	0.14	0.47	0.49
DBO	65:390	Tool2	0.18	0.18	-0.07	0	0.36	0.37	0.09	0.14	0.41	0.44
220	110:660	Tool2	0.21	0.18	0.07	0.02	0.28	0.32	0.06	0.07	0.36	0.38
	110:660	Tool3	0.21	0.19	-0.08	-0.08	0.21	0.19	0.06	0.07	0.3	0.28
IBO		Tool4	0.71	0.59	0.18	-0.13	0.56	0.31			0.9	0.67

Table 3. Performance summary of DBO on LFLE stack.

4.5 Prediction vs. observation

The dynamic precision (3 σ) of ~0.20nm is slightly better than the predictions made in section 4.2 of 0.24nm (U&SA method) and 0.30nm (noise induced method).

5. Spacer double patterning

While LFLE minimizes the number of process steps needed and thus reduces cost it still requires very tight overlay control. Spacer (SADP) forms lines around pre-patterned features, relaxing the requirement for overlay control and potentially allowing the indefinite pitch halving.

5.1 Spacer 1st layer patterning

In this section we discuss some results from first layer patterning by SADP. Experimental TE and TM spectral data obtained for an SADP stack (Fig. 13a) is fitted to modeled spectra (Fig. 13b). Although the model has not been optimized to improve the fit there is good agreement between the modeled and actual spectrum.

The CD-SEM image in Fig. 13c shows the spacer pattern. The measured bottom CD is around 48nm for layer 1 spacers. Although the spacer structures are expected to be identical for both layer 1 and layer2, the CD may vary depending on the application. The model fitting data (Fig. 13a) is consistent with uniform spacer formation across the wafer. The spacer width at the bottom is 42.5 ± 1 nm and SWA is 72.3 ± 0.2 deg.

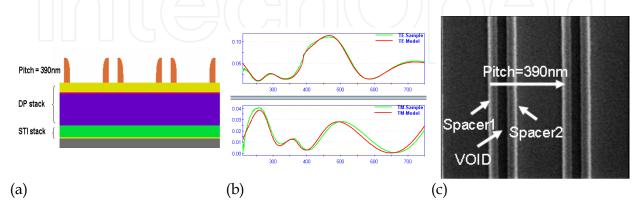


Fig. 13. (a) STI based spacer Layer 1 stack, (b) Modeled and experimental spectra, and (c) CD-SEM Image of spacer L1.

5.2 Prediction of spacer 2nd layer precision

The second spacer layer patterning characteristics were predicted using simulation. Since the static precision predicted for LFLE (in sections 4.2 and 4.4) was consistent with the experimental data in table 3, predictions for the double layer SADP case should also be valid. The good fit between modeled and experimental TETM spectra in Fig. 13b further supports the validity of this approach.

The final SADP stack after completion of the patterning steps is shown in Fig. 14. For LELE and LFLE, both patterning steps are done on the same layer. In the spacer case, the first patterning step is done on L1 (STI+DPT) and second patterning step is done on L2 (WL + DPT) with a programmed shift. The CD of the two spacers can be different.

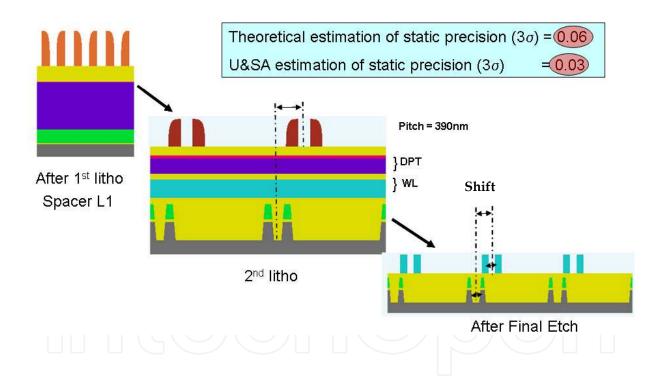


Fig. 14. The spacer stack DPT steps shown for pitch of 390nm and bottom CD ~65nm

The simulated TETM spectra for the spacer stack in Fig. 15(a) shows sensitivity across the spectral region. The TETM spectral response to overlay shift in Fig. 15(b) is linear for overlay around 25% of the grating pitch.

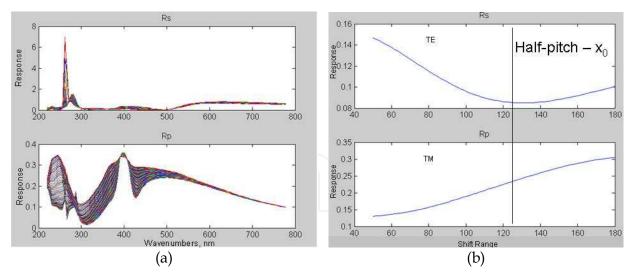


Fig. 15. (a) RCWA simulated spectra; (b) DBO sensitivity over the shift range

5.3 Spacer DBO prediction vs. expectation

The dynamic precision (3o) observed for a 2x4.65/390 target in the case of LFLE was better than predicted (sections 4.2-4.4). Assuming the same behavior applies to SADP, precision should be of the order of ~ 0.05 nm.

6. Model-based overlay measurement (mDBO)

The success of scatterometry for CD and profile measurement comes from the ability to model the signal formation process. The signature contains enough information that the measurement can be made by finding those parameters that give the closest fit between modeled and experimental signatures. The same approach can be applied to overlay measurement, reducing the number of measurement pads needed and providing profile data as well as overlay.

6.1 mDBO LELE sample

The first mDBO DPT structure consists of alternate photo resist and nitride lines on a silicon substrate (Fig. 16a). As mentioned in section 3, eDBO measurements are performed on four specially designed pads per direction (Fig. 4) with D designed to be around 25-35% of the pitch to ensure maximal overlay sensitivity [6]. The mDBO measurements are performed on two of the pads with shift +D and -D. For normal incident polarized reflectometry, it is found that the TE spectrum is more sensitive to overlay than TM [6]. To reduce measurement time without compromising sensitivity, only TE spectra are collected and used for data analysis. In mDBO analysis a physical model is first set up using NanoDiffract Software to describe the sample structure. Fig. 16 shows the model of one of the pads with designed shift +D. Four parameters, nitride bottom CD (NI_BW), resist bottom CD (PR_BW), resist height (PR_HT) and the distance between the nitride and resist lines (S), are floated to optimize the model fit to the measured spectra. When there is an overlay error ε , the distance between the nitride and resist lines, denoted as S(+) with + for positive shift, is given by $D+\varepsilon$. The model for the second pad with designed shift -D is identical to the first pad except that the shift denoted by S(-) is $D-\varepsilon$. In the regression it is assumed that the

corresponding thickness and CDs are the same for these two pads due to proximity. D is fixed to the designed value of 244nm. Fig. 18(b) displays the experimental spectrum and theoretical calculation at best fit for one of the pads. The agreement is excellent. The shape of the spectrum and fit quality for the second pad is very similar to the first one.

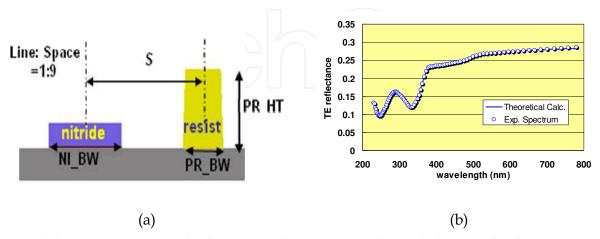


Fig. 16. (a) DPT structure with alternative photo resist and nitride lines with silicon over etch. Four parameters are floated: nitride bottom CD (NI_BW), resist bottom CD (PR_BW), resist height (PR_HT) and the shift (*S*) of resist from nitride lines, measured from center to center. (b) Experimental spectrum and theoretical calculation.

To check the stability and performance of the model, uncertainty and sensitivity analysis (U&SA) was performed using NanoDiffractTM software [16]. Fig. 17 shows the signal to noise ratio corresponding to a 2 nm change in the overlay error. Reasonable sensitivity is observed. The parameter correlation matrix and predicted static precision (3 σ) are summarized in table 4. No strong correlation is found between overlay and other parameters. The predicted static precision for overlay is 0.16nm (3 σ), which compares well with the eDBO result of 0.25nm in section 3.3.

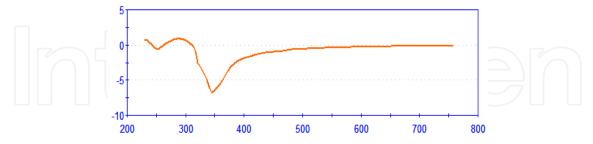
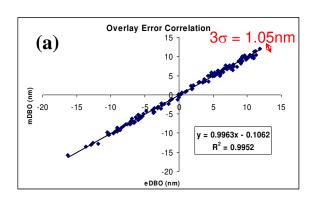


Fig. 17. Overlay signal/noise ratio. The signal corresponds to 2nm change in overlay.

Fig. 18a compares two-pad mDBO measurements with 4-pad eDBO results. Both data sets are from \sim 140 dies across the wafer. Excellent correlation (R² \sim 0.99) and a slope of 1.00 are achieved. The offset is about 0.1nm. Fig. 18b shows the histogram of the deviation of the data points from the correlation curve shown in Fig. 18a. The distribution follows a normal distribution, indicating the absence of systematic error between these two analysis methods. Standard deviation (3 σ) is 1.05nm, which contains measurement uncertainties from both measurement methods.

Paramete	Precision (3σ) (nm)				
NI_BW	1				0.091
S (overlay)	-0.19	1			0.16
PR_HT	-0.47	0.54	1		0.047
PR_BW	0.05	-0.68	-0.62	1_	0.052

Table 4. Parameter correlation matrix and precision predicted using model shown in Fig. 16 (a).



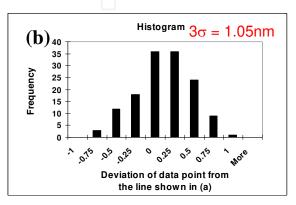
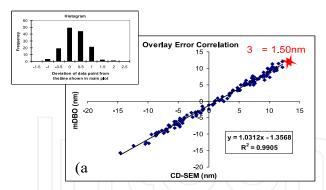


Fig. 18. (a) Correlation of mDBO and eDBO for LELE sample. (b) Histogram of the deviation of the data points form the straight line shown in (a). Data shown here for X and similar overlay error is observed for Y direction



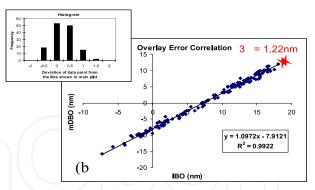


Fig. 19. (a) Correlation of mDBO and CD-SEM for LELE wafer. The inset shows the histogram of the deviation of the data points from the straight line shown in main plot. (b) Correlation of mDBO and IBO. The inset shows the histogram of the deviation of the data points from the straight line shown in main plot.

To further evaluate the accuracy of scatterometry measurement, mDBO results are compared with other metrology techniques, i.e., The CD-SEM data is from the DBO targets. Image based overlay (IBO) measurements are made on standard box-in-box targets nearby. Correlations of mDBO to these two techniques are shown in Fig. 19. A good correlation (R²=0.99) and a slope of 1.03 are observed between eDBO and CD-SEM. The offset between eDBO and CD-SEM measurements is ~1.3nm. A good correlation (R²=0.99) is also observed between eDBO and IBO. However, there is an offset of ~7.9 nm between the two methods.

The source of the offset is not clear. The deviation of the data points from the linear correlation curve is 1.50nm 3σ between mDBO and CD-SEM, and 1.22nm 3σ between mDBO and IBO.

6.2 mDBO LFLE sample

Targets composed of only one pad are desirable because they further reduce total target size. 2D gratings that are sensitive to overlay errors in both X and Y directions may be used [13, 17]. One example is shown in Fig. 20. A 2D lattice (similar to an IBO box-in-box target) is formed with a period on the order of hundreds of nanometers, chosen to maximize diffraction efficiency and overlay sensitivity. For IBO targets , the scale of the boxes is on the order of microns to a few tens of microns. The size of the IBO target is limited by optical resolution.

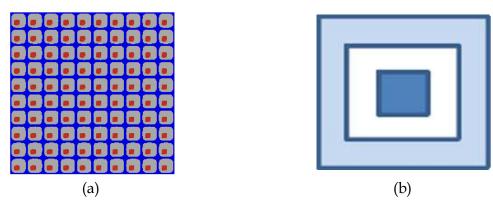


Fig. 20. (a) mDBO 2D grating target; (b) IBO box-in-box target

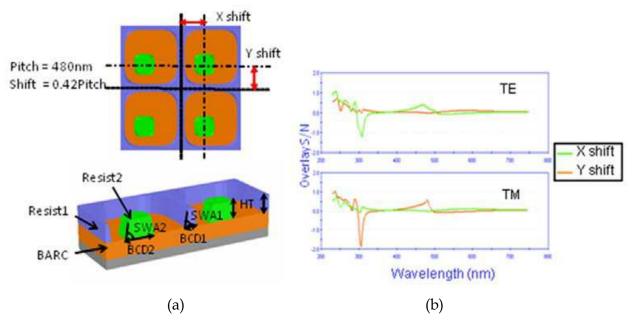


Fig. 21. (a) 2D DBO targets for the LFLE sample. Seven parameters are floated: resist1 BCD, SWA and HT, resist2 BCD, SWA and HT (coupled to resist1 HT), X shift and Y shift defined from the center of the grids (resist1) to the center of the squares (resist2). (b) Overlay S/N spectrum. The signal corresponds to 0.5nm change in X and Y overlay.

The overlay errors are extracted from the DBO target using the modeling approach. The sample structure and modeling details are shown in Fig. 21a. The pitch is 480nm and the nominal values of X shift and Y shift are 42% of the pitch. The design is symmetric in X and Y so that resist side wall angles (SWA) and bottom critical dimensions (BCD) can be coupled between X and Y directions. Seven parameters are floated: resist1 BCD, SWA and height (HT), resist2 BCD, SWA and HT (coupled to resist1 HT), X shift and Y shift defined from the center of the grids (resist1) to the center of the squares (resist2). Sensitivity analysis shows that TE spectra are more sensitive to X overlay while TM spectra are more sensitive to Y overlay (Fig. 21b). Therefore, both TE and TM spectra are used in the measurement.

The experimental spectra and RCWA fits are shown in Fig. 22. In fig. 21b, the overlay S/N corresponds to 0.5nm change in overlay. Sensitivity to overlay of the 2D targets (Fig. 11), is about half of that of 1D targets for the most sensitive wavelength, if both are normalized to 1nm. This is reasonably understood considering the reduction in the target size.

Experimental spectrum (sample) and fit (model)

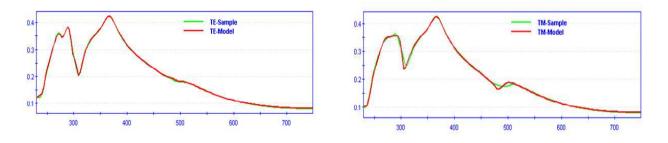
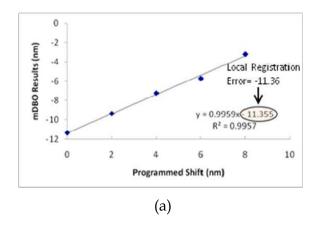


Fig. 22. Experimental TE and TM spectra and theoretical fits for the structure in Fig. 21a.

Parameters	Resist1 SWA	Resist Ht	Resist1 BCD	Y Shift	Resist2 SWA	X Shift	Resist2 BCD
Resist1 SWA	-1						
Resist Ht	-0.79	-1					
Resist1 BCD	0.95	-0.85	-1				
Y Shift	-0.03	-0.34	-0.04	- 1			
Resist2 SWA	-0.62	0.28	-0.6	0.19	- 1		
X Shift	-0.03	-0.37	-0.05	0.43	0.18	-1	
Resist 2 BCD	-0.5	0.001	-0.64	0.29	0.83	0.32	-1

Table 5. Parameter correlation matrix for the model shown in Fig. 21(a).

The parameter correlation matrix is shown in table 5. There are no strong correlations between overlay and the other parameters. The accuracy is first verified by measuring a series of five targets with designed shifts increasing by 2nm between two neighboring targets. The correlation of the measurement and the programmed overlay is displayed in Fig. 23a. R^2 is 0.996 and slope is 0.996. An offset of -11.36nm is observed. It comes from the local registration error due to scanner alignment errors. This can be corrected by adding an overlay offset between the layers during exposure. IBO measurements on a Blossom target next to the DBO targets show a registration error of -11.18nm, which agrees with the offset within 0.2nm. The DBO accuracy is further verified by measuring 49 fields across the wafer and correlating to Blossom measurements. The correlation plot is shown in Fig. 23b, with R^2 of \sim 0.98 and slope of \sim 0.97.



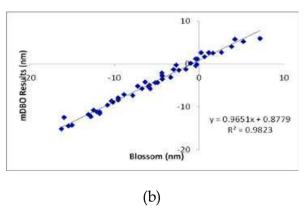


Fig. 23. (a) Correlation of mDBO 2D target measurements with programmed shifts. (b) Correlation of mDBO 2D targets with IBO blossom measurements.

6.3 mDBO LFLE performance

The dynamic repeatability (DYN 3σ), TIS mean, and TIS 3σ are reported in table 5. The dynamic repeatability is measured from 15 load/unload cycles on multiple fields (9 fields for all 1D targets, and 15 fields for the 2D targets). DYN 3σ is reported as the average of the 3σ -precisions from the measurement sites. TIS is defined as in eq. (6),

$$TIS = \frac{OVL_0 + OVL_{180}}{2} \tag{6}$$

where OVL_0 is the overlay measurement result at 0° loading angle, and OVL_{180} is the measurement result at 180° loading angle. The reported TIS mean is measured over 71 sites for 1D target and 49 sites for 2D targets across the wafer. For all DBO targets, TIS mean is

nearly zero. TIS 3σ reported is from multiple fields (9 fields for all 1D targets, and 15 fields for the 2D targets); with OVL_0 and OVL_{180} results averaged over 15 load/unload cycles respectively. By removing the contribution from dynamic variations for each loading angle, TIS 3σ is very small (on the order of 0.01nm).

All three types of standard 1D DBO targets have shown excellent performance with TMU <0.1nm and the 2D 1x1 target has a TMU ~0.2nm (not including tool matching). It is worth mentioning that the mDBO 2x2 target has better TMU, which is a good balance between measurement performance, target size, and measurement time. Similar performance is also observed for Y.

Technology	DYNP (nm)	TIS Avg (nm)	TIS 3_{σ} (nm)	TMU*
Targets	X	Х	Х	Х
eDBO 1D 2x4 target	0.088	-0.006	0.029	0.092
mDBO 1D 2x2 target	0.050	-0.006	0.028	0.058
mDBO 1D 2x1 target	0.085	0.005	0.042	0.095
mDBO 2D 1x1 target	0.172	0.057	0.120	0.209

Table 6. Performance summary of eDBO and mDBO targets (*TMU does not include tool matching)

7. Conclusion

The multi-pad empirical diffraction-based overlay (eDBO) technique is capable of controlling the overlay in double patterning optical lithography processes (DPT). The usable range of LELE DPT eDBO is ±70nm. eDBO results agree well with traditional image-based overlay (IBO) results and with overlay calculated from CD-SEM data. While good correlation and linearity between DBO and IBO was observed, a significant systematic offset can occur that appears to originate in the IBO data. Reduction in the number of pads from 2x4 to 2x3 results in only a small deterioration in precision.

DBO measurements have near-zero TIS. TMU (including tool-to-tool matching) is less than 0.5nm for both the LELE and freeze process. The overlay errors determined by eDBO (4 pad measurement) agree well with modeled DBO (2 pad measurement) data. This ability to model the signal formation process allows model-generated spectra to be used to predict measurement precision with good success.

Simultaneous model-based measurement of overlay in X&Y (2D mDBO), is possible with good results and also allows reduction in the overall target size. mDBO requires knowledge of the film stack, material optical properties and target layout and consequently more effort in creating recipes than eDBO but provides significant value in reducing measurement time

and target size. In addition to overlay data, mDBO provides CD measurements and profile data for the target, which is not possible with other methods. The multi-pad DBO approach is a good method of overlay process control, especially if combined with in-chip measurements using an alternative technique [3].

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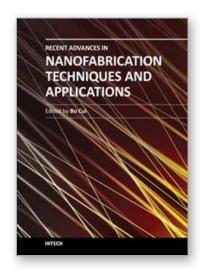
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Nanotechnology has experienced a rapid growth in the past decade, largely owing to the rapid advances in nanofabrication techniques employed to fabricate nano-devices. Nanofabrication can be divided into two categories: "bottom up" approach using chemical synthesis or self assembly, and "top down" approach using nanolithography, thin film deposition and etching techniques. Both topics are covered, though with a focus on the second category. This book contains twenty nine chapters and aims to provide the fundamentals and recent advances of nanofabrication techniques, as well as its device applications. Most chapters focus on indepth studies of a particular research field, and are thus targeted for researchers, though some chapters focus on the basics of lithographic techniques accessible for upper year undergraduate students. Divided into five parts, this book covers electron beam, focused ion beam, nanoimprint, deep and extreme UV, X-ray, scanning probe, interference, two-photon, and nanosphere lithography.

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